

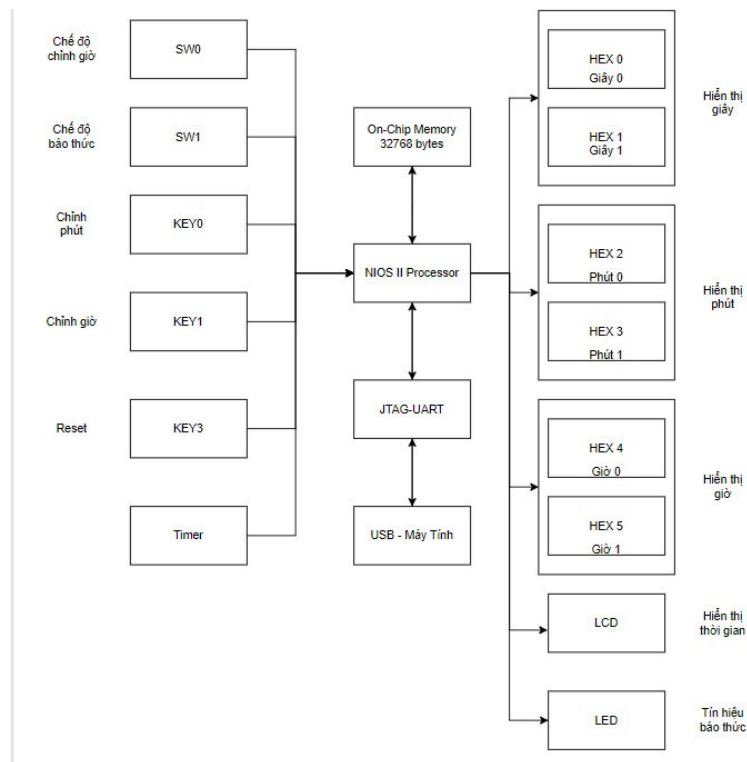
BÁO CÁO THỰC HÀNH “THIẾT KẾ SoC”

Tên bài thực hành: Thiết kế Đồng Hồ.....

I. Mô tả tóm tắt nội dung bài thực hành

Thiết kế Đồng Hồ có các chức năng sau:

- Chạy giờ, phút, giây lên LCD và HEX
- Có thể chỉnh thời gian giờ và phút
- Có thể Reset thời gian về thời gian mà đã đặt
- Có thể cài thời gian báo thức thông qua LEDG 5s.



II. Tiến trình thực hiện bài thực hành

Phần 1: Tạo Hardware

Bước 1: Tạo File Quartus II với tên DongHo

Bước 2: Tạo Qsys với tên file là system

Cần chỉnh thông số như sau:

Nios II Processor - nios2_qsys_0

Select a Nios II Core

Nios II Core: ☒ Nios II/e ☐ Nios II/s ☐ Nios II/f

| | Nios II/e | Nios II/s | Nios II/f |
|----------------------------------|----------------------|--|---|
| Nios II Selector Guide | RISC 32-bit | RISC 32-bit Instruction Cache Branch Prediction Hardware Multiply Hardware Divide | RISC 32-bit Instruction Cache Branch Prediction Hardware Multiply Hardware Divide Barrel Shifter Data Cache Dynamic Branch Prediction |
| Memory Usage (e.g. Stratix IV) | Two M9Ks (or equiv.) | Two M9Ks + cache | Three M9Ks + cache |

- LCD ON

PIO (Parallel I/O) - LCD_ON

Block Diagram

Basic Settings

Width (1-32 bits): 1

Direction: ☐ Bidir ☐ Input ☐ InOut ☒ Output

Output Port Reset Value: 0x0000000000000000

- LCD BLON

PIO (Parallel I/O) - LCD_BLON

Block Diagram

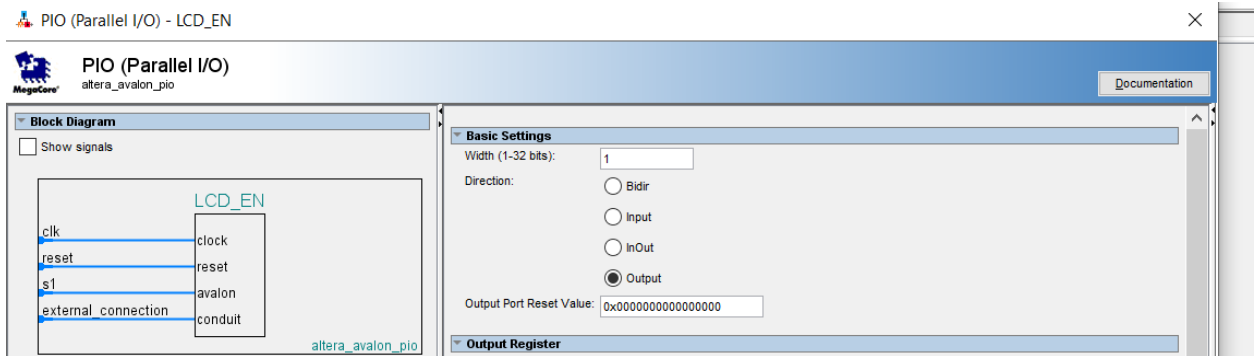
Basic Settings

Width (1-32 bits): 1

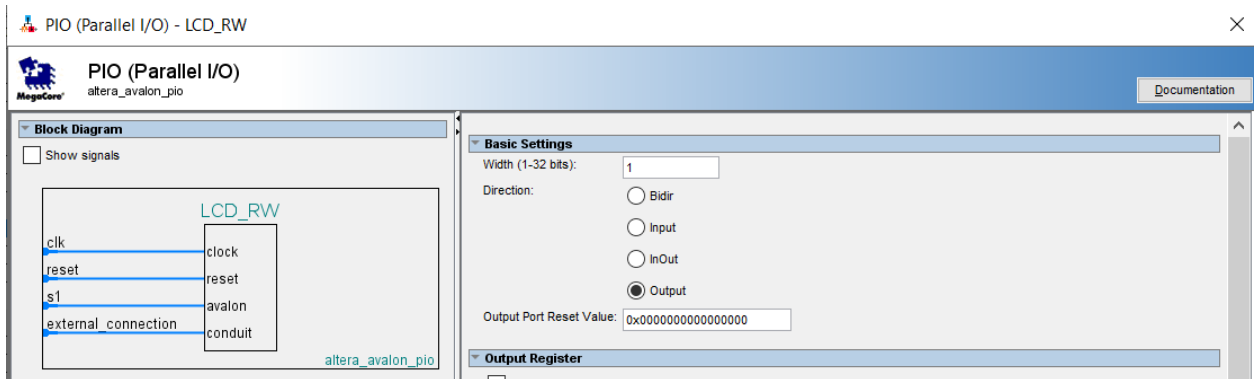
Direction: ☐ Bidir ☐ Input ☐ InOut ☒ Output

Output Port Reset Value: 0x0000000000000000

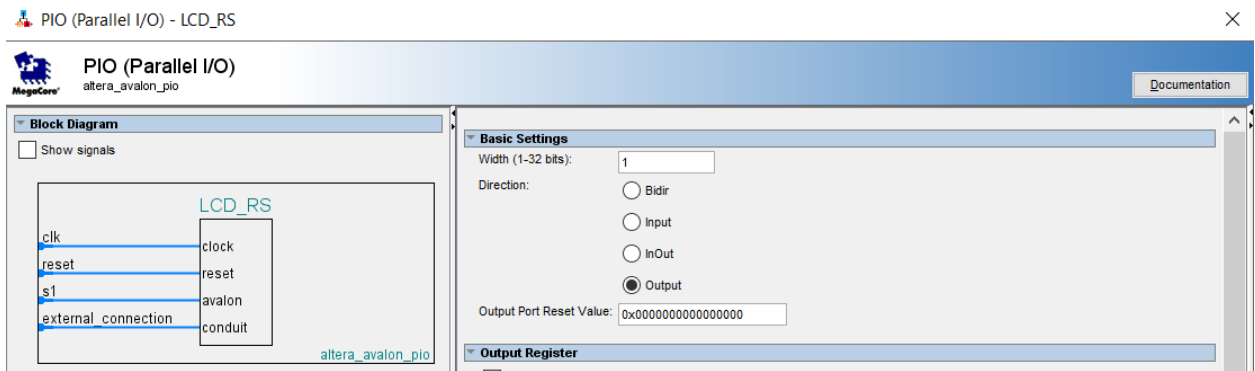
- LCD EN



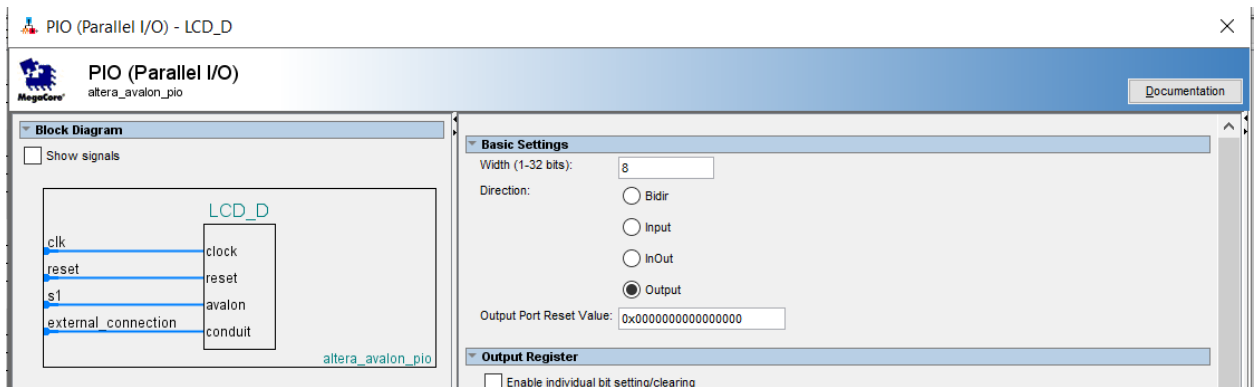
- LCD RW



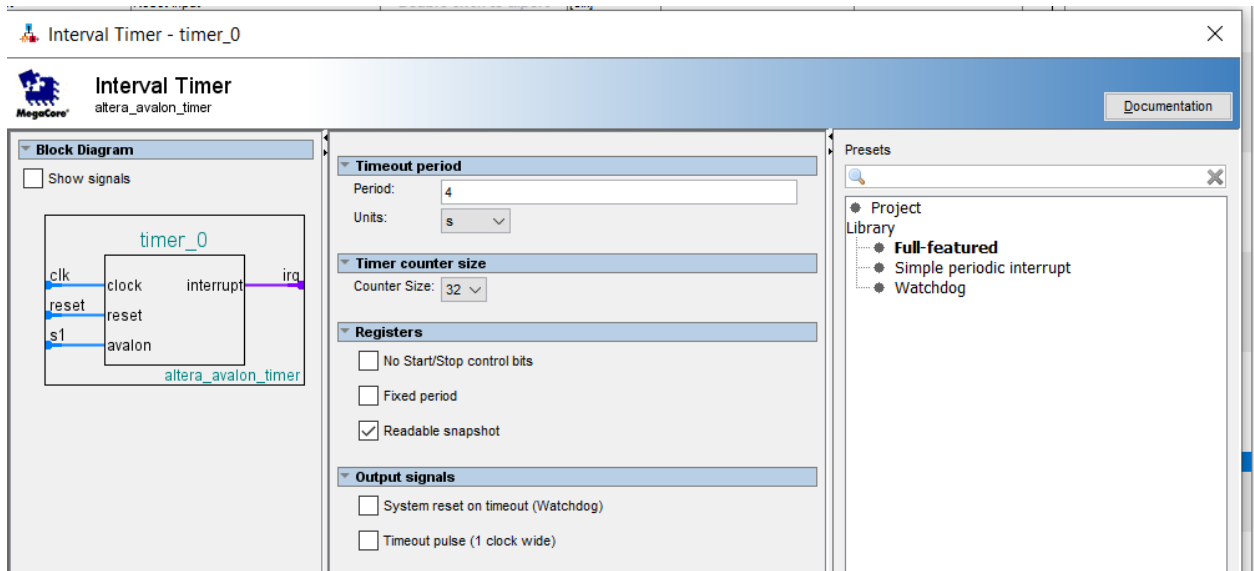
- LCD RS



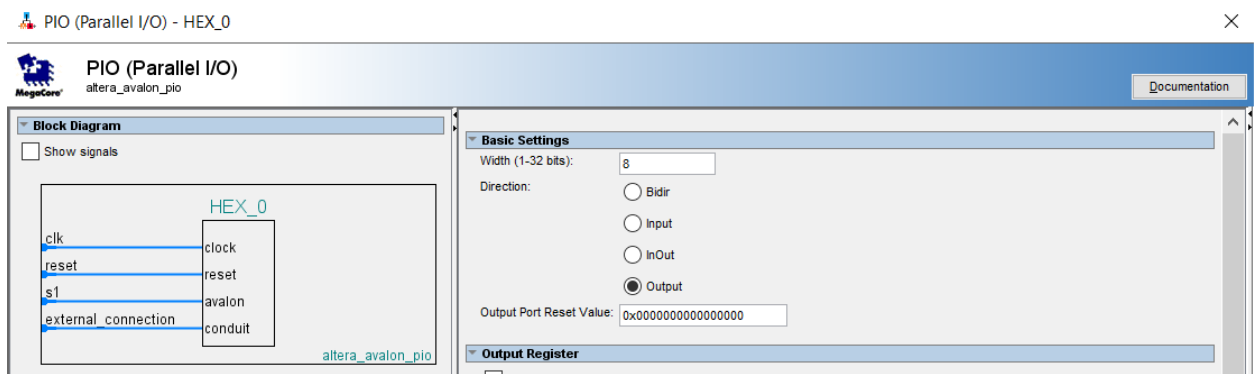
- LCD D



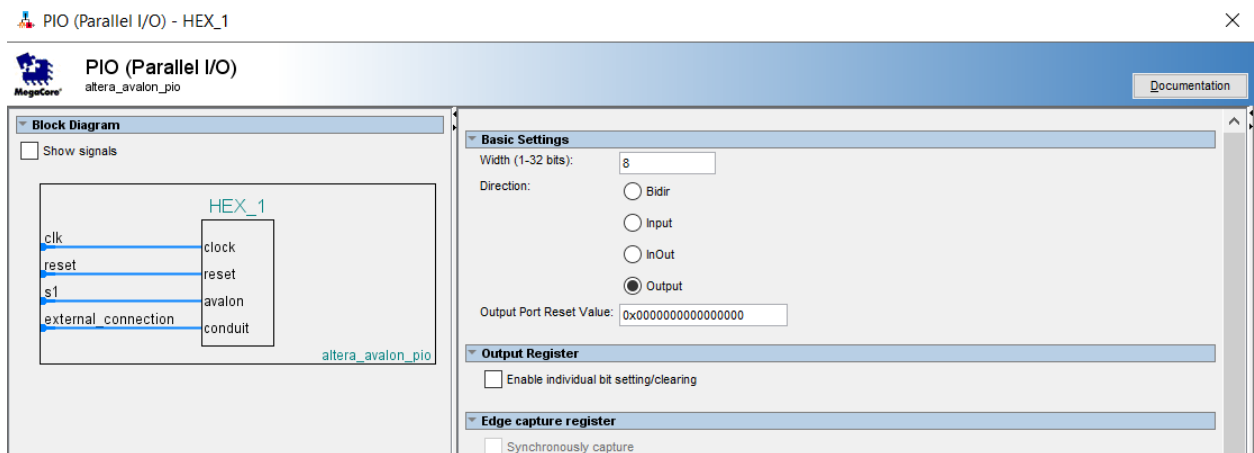
- Timer 0



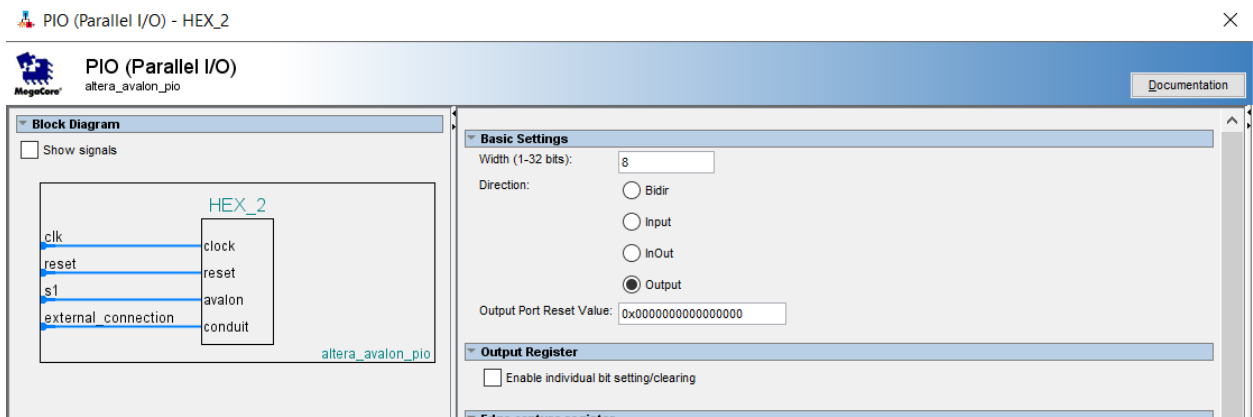
- Hex 0



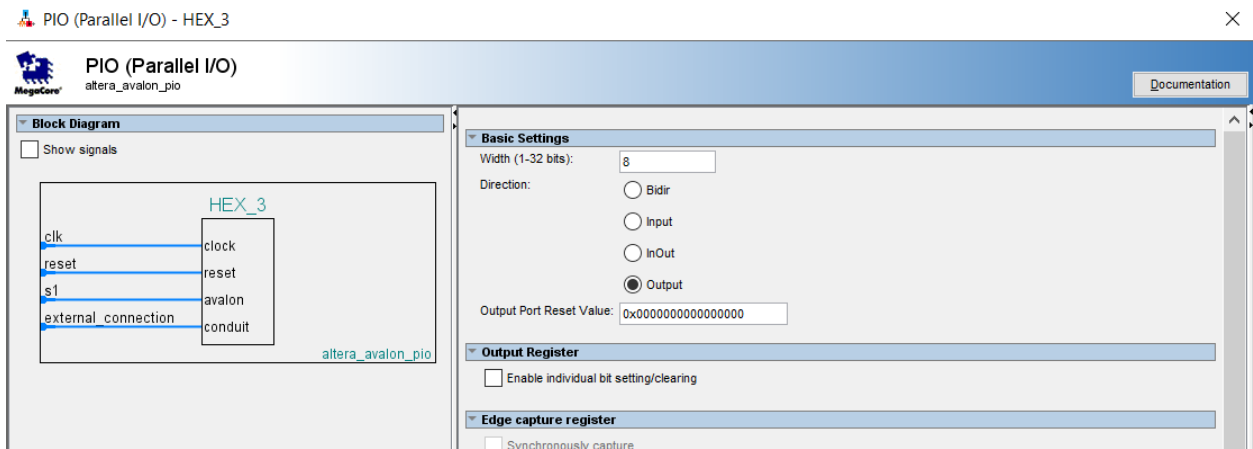
- Hex 1



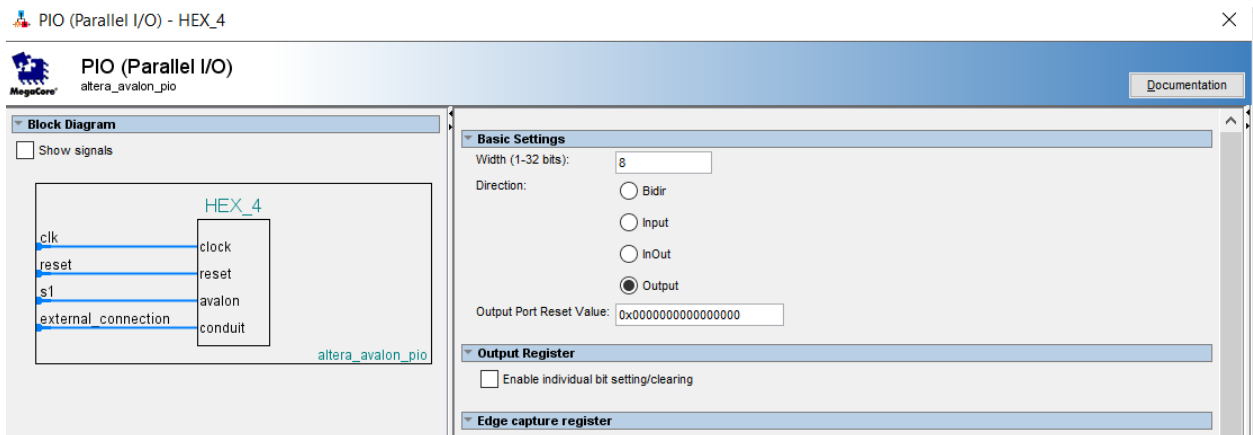
- Hex 2



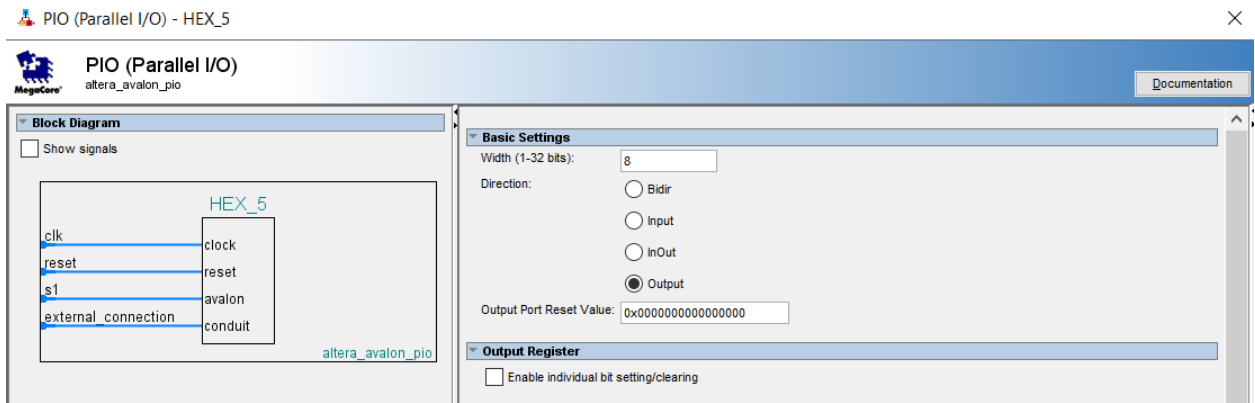
- Hex 3



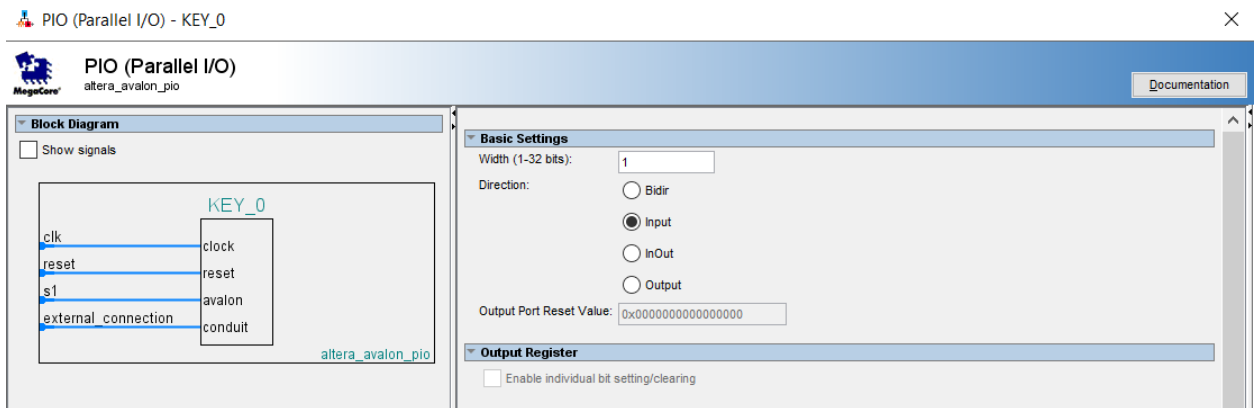
- Hex 4



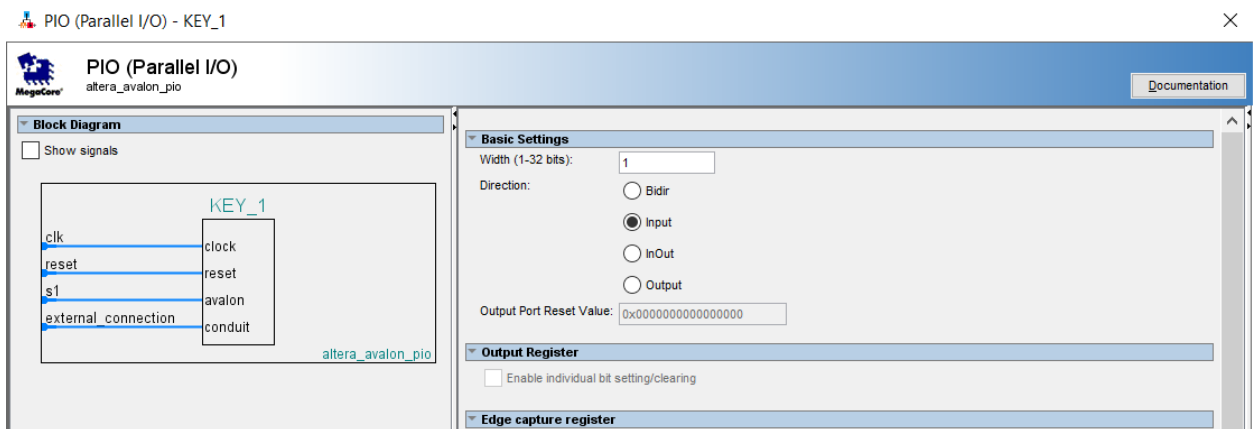
- Hex 5



- Key 0



- Key 1



- Key 3

PIO (Parallel I/O) - KEY_3

PIO (Parallel I/O)
altera_avalon_pio

Documentation

Block Diagram

☐ Show signals

Basic Settings

Width (1-32 bits): 1

Direction:

☐ Bidir

☒ Input

☐ InOut

☐ Output

Output Port Reset Value: 0x0000000000000000

Output Register

☐ Enable individual bit setting/clearing

Edge capture register

☐ Synchronously capture

- LED

PIO (Parallel I/O) - LED

PIO (Parallel I/O)
altera_avalon_pio

Documentation

Block Diagram

☐ Show signals

Basic Settings

Width (1-32 bits): 8

Direction:

☐ Bidir

☐ Input

☐ InOut

☒ Output

Output Port Reset Value: 0x0000000000000000

Output Register

☐ Enable individual bit setting/clearing

Edge capture register

☐ Synchronously capture

- SW 0

PIO (Parallel I/O) - SW_0

PIO (Parallel I/O)
altera_avalon_pio

Documentation

Block Diagram

☐ Show signals

Basic Settings

Width (1-32 bits): 2

Direction:

☐ Bidir

☒ Input

☐ InOut

☐ Output

Output Port Reset Value: 0x0000000000000000

Output Register

☐ Enable individual bit setting/clearing

Edge capture register

☐ Synchronously capture

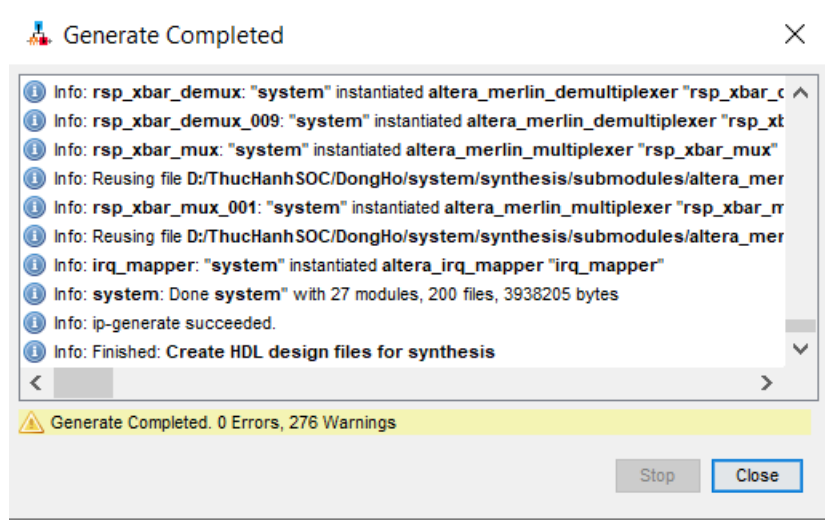
- SW 1

| System Contents | | | | | | | | | |
|-------------------------------------|-------------|---|---|---|-------------------------|-------------|-------------|-----|-------------|
| Address Map | | | | | | | | | |
| Clock Settings | | | | | | | | | |
| Project Settings | | | | | | | | | |
| Instance Parameters | | | | | | | | | |
| System Inspector | | | | | | | | | |
| HDL Example | | | | | | | | | |
| Generation | | | | | | | | | |
| Use | Connections | Name | Description | Export | Clock | Base | End | IRQ | Opcode Name |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> LCD_D | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export lcd_d_external_connection | clk_0 [clk] [clk] | 0x0001_1160 | 0x0001_116f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> timer_0 | Interval Timer Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export | clk_0 [clk] [clk] | 0x0001_1020 | 0x0001_103f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> HEX_0 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export hex_0_external_connection | clk_0 [clk] [clk] | 0x0001_1130 | 0x0001_113f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> HEX_1 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export hex_1_external_connection | clk_0 [clk] [clk] | 0x0001_1120 | 0x0001_112f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> HEX_2 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export hex_2_external_connection | clk_0 [clk] [clk] | 0x0001_1110 | 0x0001_111f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> HEX_3 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export hex_3_external_connection | clk_0 [clk] [clk] | 0x0001_1100 | 0x0001_110f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> HEX_4 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export hex_4_external_connection | clk_0 [clk] [clk] | 0x0001_10f0 | 0x0001_10ff | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> HEX_5 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export hex_5_external_connection | clk_0 [clk] [clk] | 0x0001_10e0 | 0x0001_10ef | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> KEY_0 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export key_0_external_connection | clk_0 [clk] [clk] | 0x0001_10d0 | 0x0001_10df | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> KEY_1 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export key_1_external_connection | clk_0 [clk] [clk] | 0x0001_1140 | 0x0001_114f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> KEY_3 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export key_3_external_connection | clk_0 [clk] [clk] | 0x0001_10a0 | 0x0001_10af | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> LED | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export led_external_connection | clk_0 [clk] [clk] | 0x0001_10b0 | 0x0001_10bf | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> SW_0 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export sw_0_external_connection | clk_0 [clk] [clk] | 0x0001_1150 | 0x0001_115f | | |
| <input checked="" type="checkbox"/> | | <input checked="" type="checkbox"/> SW_1 | PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit | Double-click to export Double-click to export Double-click to export sw_1_external_connection | clk_0 [clk] [clk] | 0x0001_10c0 | 0x0001_10cf | | |

Thiết kế đúng sẽ hiển thị nội dung sau:

| Messages | |
|----------------------|-----------------|
| Description | |
| | 5 Info Messages |
| 0 Errors, 0 Warnings | |

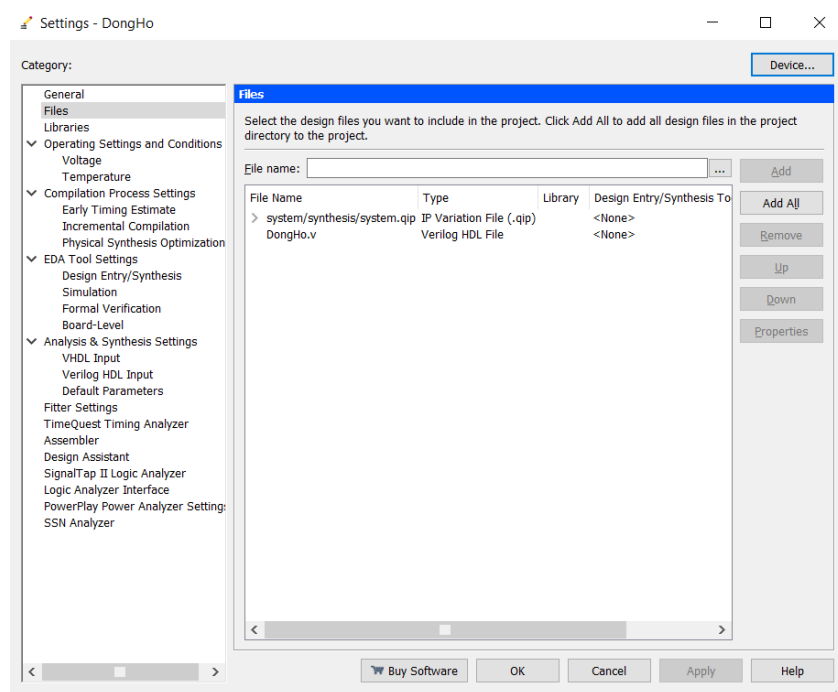
Sau đó qua Generation và nhấn Generate để chạy:



Sau khi chạy xong thì nhấn Close và trở lại Quartus để thực hiện tiếp.

Bước 3: Ta vào Quartus nhấn vào Project + Add/Remove Files in Project....

Tiếp theo tìm kiếm file .qip để add vào như hình bên dưới:



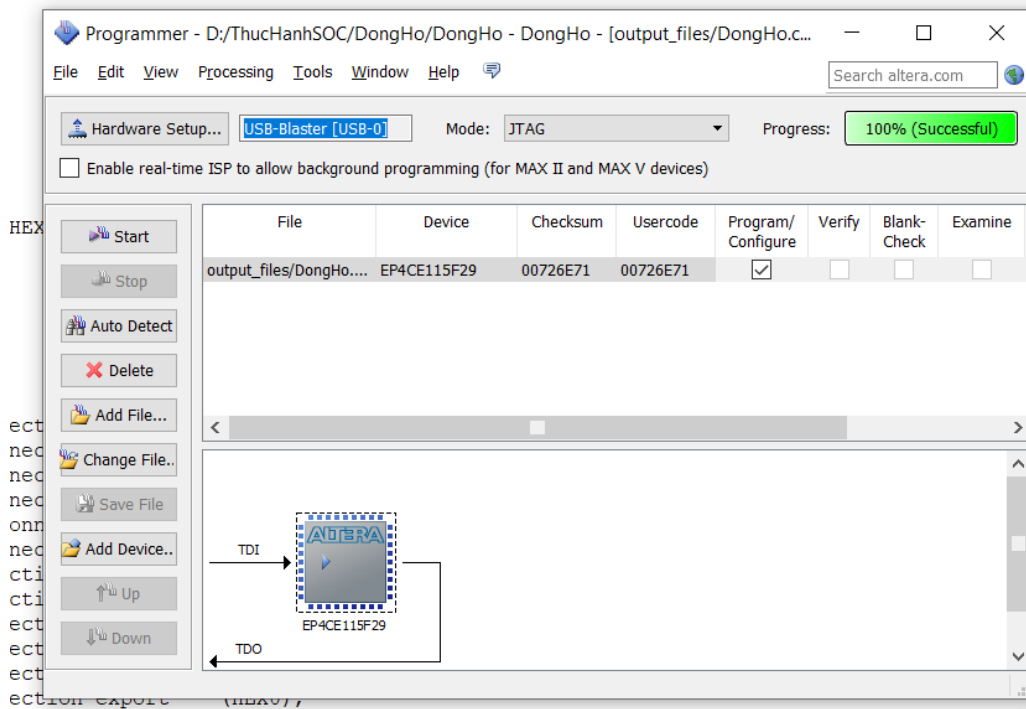
Tạo file .v có tên là DongHo.v

```

1 module DongHo (
2     input  CLOCK_50,
3     input  [3:0] KEY,
4     input  [1:0] SW,
5     output [7:0] LCD_DATA,
6     output      LCD_EN,
7     output      LCD_RS,
8     output      LCD_RW,
9     output      LCD_BLON,
10    output      LCD_ON,
11    output [6:0] HEX0,HEX1,HEX2,HEX3,HEX4,HEX5,HEX6,HEX7,
12    output [7:0] LEDG
13 );
14     assign HEX6 = 7'b1111111;
15     assign HEX7 = 7'b1111111;
16
17 system u0 (
18     .clk_clk                (CLOCK_50),
19     .lcd_d_external_connection_export (LCD_DATA),
20     .lcd_rs_external_connection_export (LCD_RS),
21     .lcd_rw_external_connection_export (LCD_RW),
22     .lcd_en_external_connection_export (LCD_EN),
23     .lcd_blon_external_connection_export (LCD_BLON),
24     .lcd_on_external_connection_export (LCD_ON),
25     .sw_0_external_connection_export (SW[0]),
26     .sw_1_external_connection_export (SW[1]),
27     .key_0_external_connection_export (KEY[0]),
28     .key_1_external_connection_export (KEY[1]),
29     .key_3_external_connection_export (KEY[3]),
30     .hex_0_external_connection_export (HEX0),
31     .hex_1_external_connection_export (HEX1),
32     .hex_2_external_connection_export (HEX2),
33     .hex_3_external_connection_export (HEX3),
34     .hex_4_external_connection_export (HEX4),
35     .hex_5_external_connection_export (HEX5),
36     .led_external_connection_export (LEDG)
37 );
38
39 endmodule

```

Chạy chương trình và nạp vào kit



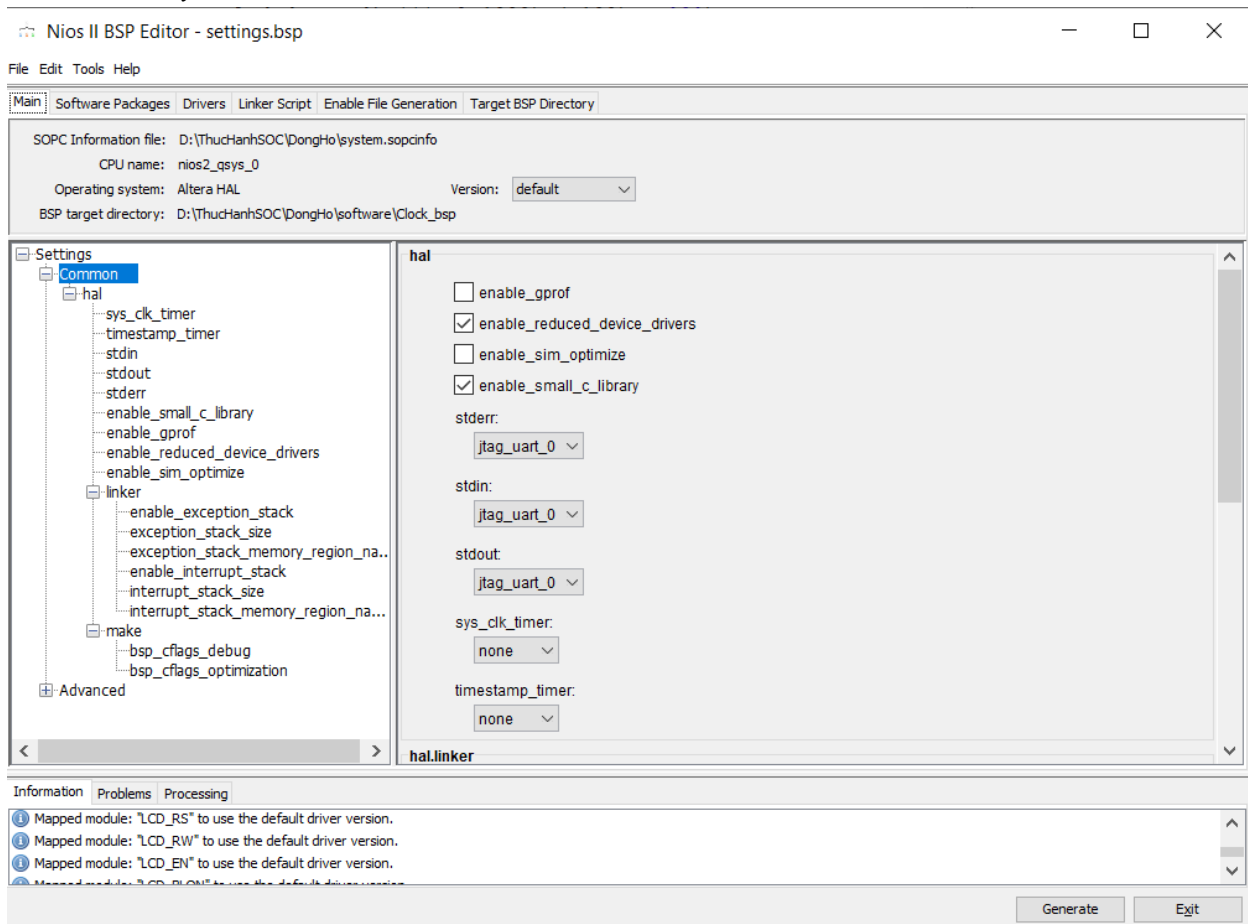
Phần 2: Viết code firmware (code C trong Eclipse)

Bước 1: Mở phần mềm eclipse

Tạo Project với đường link bài làm

Tiếp theo tạo file .c có tên là Clock.c

Bước 2: Chuột phải vào .bsp đưa chuột đến Nios II + BSP Editor... để điều chỉnh như hình dưới đây:

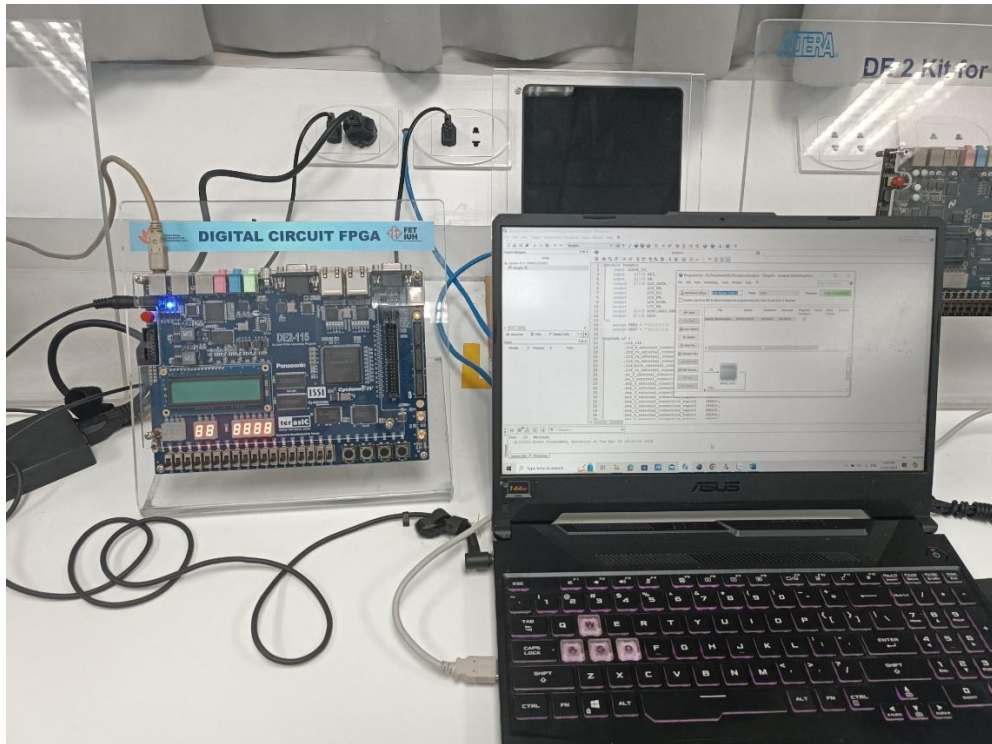


Bước 3: Chạy code và nạp chương trình vào Kit

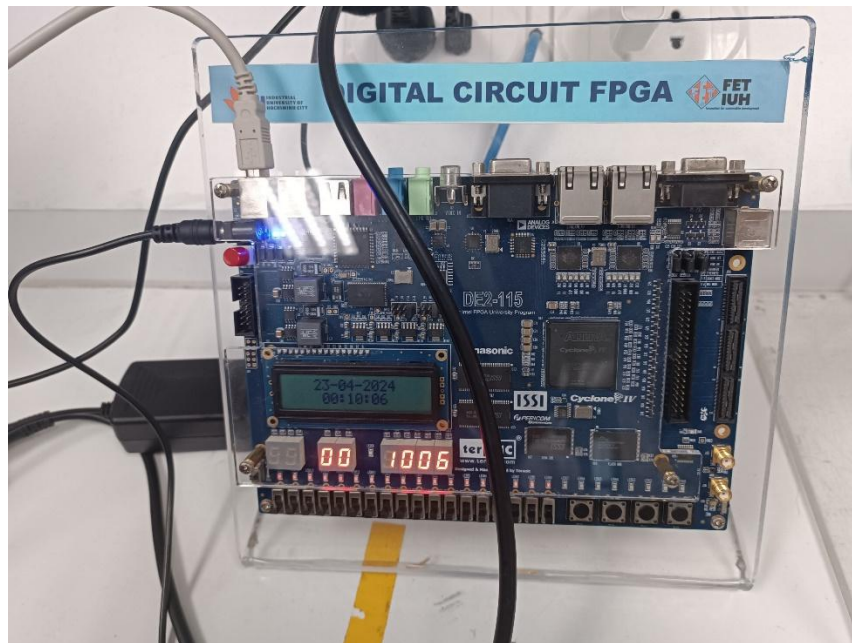
Phần 3: Kiểm thử trên kit DE2

Bước 1: Chuẩn bị Kit, Dây cắm, Laptop

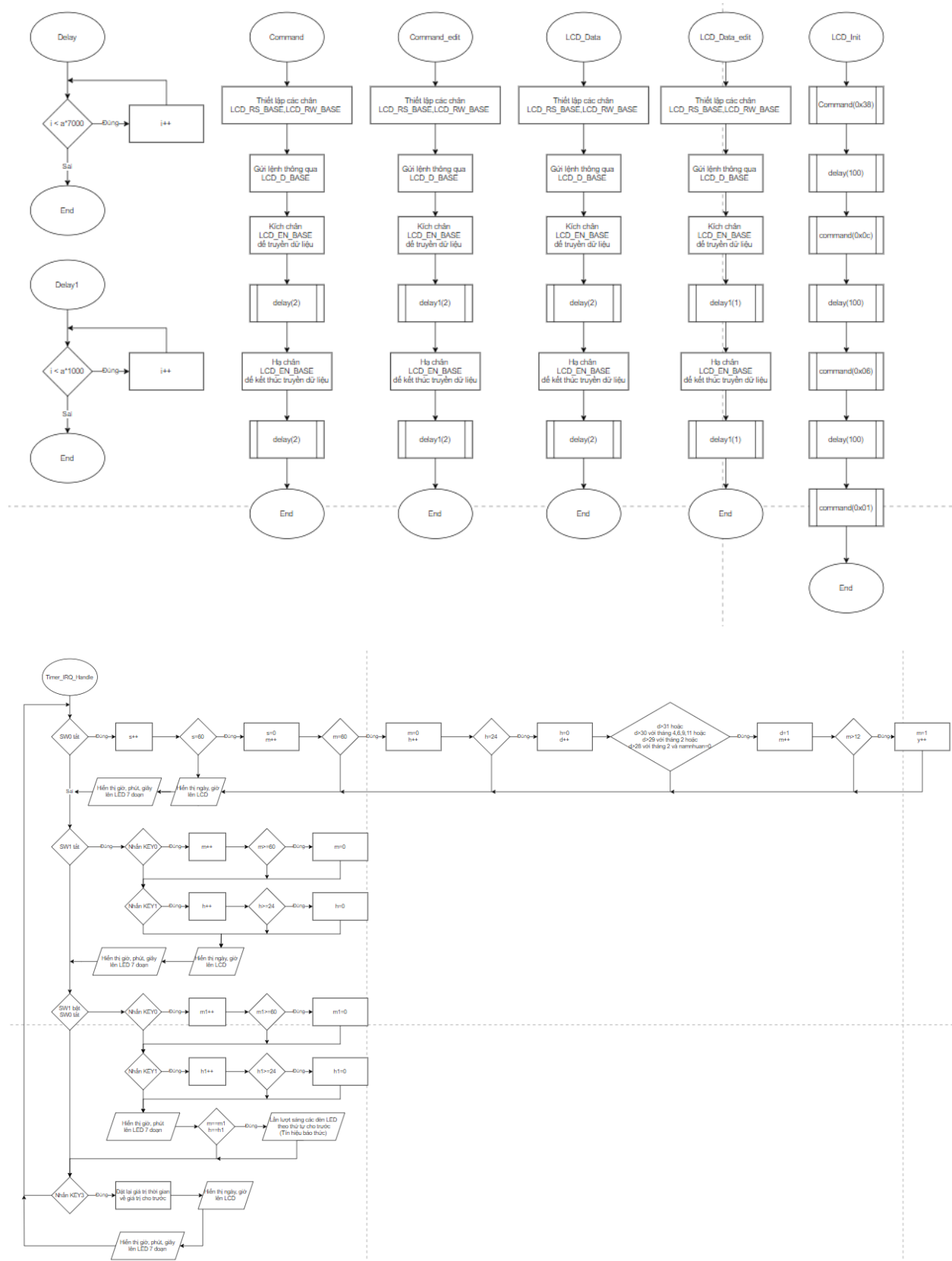
Bước 2: Kết nối vào kit và nạp chạy kit

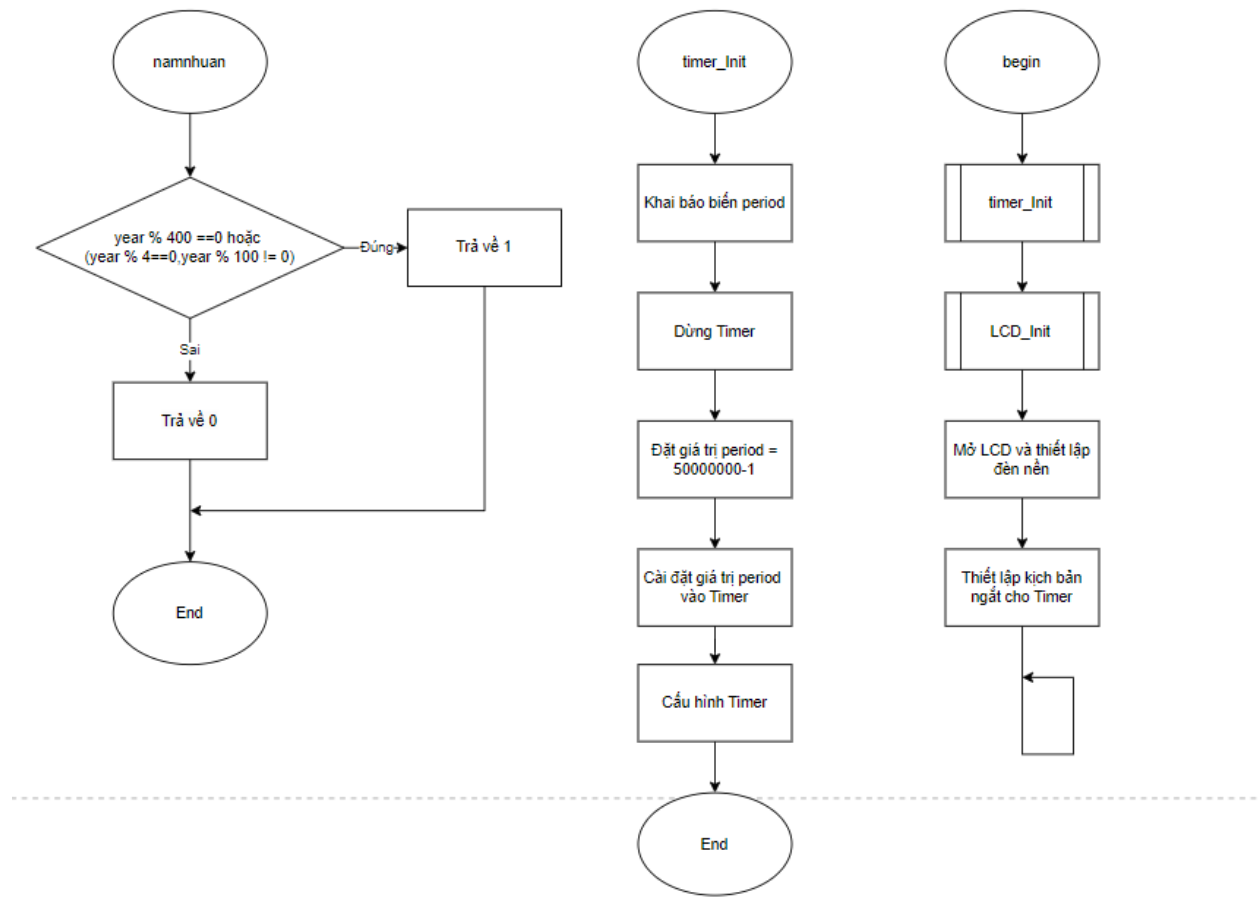


Bước 3: *Nạp code Eclipse xuống kit để xem kết quả:*



III. Lưu đồ giải thuật





IV. Giải thích code Firmware

Code eclipse

```

#include <stdio.h>
#include "io.h"
#include "system.h"
#include "altera_avalon_timer_regs.h"
#include "sys/alt_irq.h"
#include "sys/alt_stdio.h"
#include "altera_avalon_pio_regs.h"
int s=0,h=0,m=0,d=23,mt=4,y=2024,m1=0,h1=0,c=0,t=0;
const unsigned char decoder[10] = {0xC0, 0xF9, 0xA4, 0xB0, 0x99, 0x92, 0x82, 0xF8, 0x80, 0x90};
void delay(int a)
{
    volatile int i = 0;
    while(i < a*7000)
    {
        i++;
    }
}
void delay1(int a)
{
    volatile int i = 0;
    while(i < a*1000)

```

```

    {
        i++;
    }
}

void command(data)
{
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RS_BASE, 0x00);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RW_BASE, 0x00);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_D_BASE, data&0xFF);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x01);
    delay(2);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x00);
    delay(2);
}

void command_edit(data)
{
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RS_BASE, 0x00);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RW_BASE, 0x00);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_D_BASE, data&0xFF);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x01);
    delay1(2);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x00);
    delay1(2);
}

void lcd_data(char data)
{
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RS_BASE, 0x01);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RW_BASE, 0x00);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_D_BASE, data&0xFF);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x01);
    delay(1);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x00);
    delay(1);
}

void lcd_data_edit(char data)
{
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RS_BASE, 0x01);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_RW_BASE, 0x00);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_D_BASE, data&0xFF);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x01);
    delay1(1);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_EN_BASE, 0x00);
    delay1(1);
}

void lcd_init()
{
    command(0x38);
    delay(10);
    command(0x0c);
    delay(10);
    command(0x06);
    delay(10);
    command(0x01);
}

int namnhuan(int year)
{
    if ((year % 400 == 0) || ((year % 4 == 0) && (year % 100 != 0)))
        return 1;
    else
        return 0;
}

void Timer_IRQ_Handle(void* isr_context)
{
    //const unsigned int decoder[10] = {0xC0,0xF9,0xA4,0xB0,0x99,0x92,0x82,0xF8,0x80,0x90};

```



```

while(1)
{
    if(IORD(SW_0_BASE, 0) == 0)
    {
        s++;
        if (s == 60)
        {
            s = 0;
            m++;
            if (m == 60)
            {
                m = 0;
                h++;
                if (h == 24)
                {
                    h = 0;
                    d++;
                    if (d > 31 || (d > 30 && (mt == 4 || mt == 6 || mt == 9 || mt == 11))
|| (d > 29 && mt == 2) || (d > 28 && mt == 2 && !namnhuan(y))) {
                        d = 1;
                        mt++;
                        if (mt > 12)
                        {
                            mt = 1;
                            y++;
                        }
                    }
                }
            }
        }
        command(0x02); // Clear display
        lcd_data((d / 10) + '0');
        lcd_data((d % 10) + '0');
        lcd_data('-');
        lcd_data((mt / 10) + '0');
        lcd_data((mt % 10) + '0');
        lcd_data('-');
        lcd_data((y / 1000) + '0');
        lcd_data(((y % 1000) / 100) + '0');
        lcd_data(((y % 100) / 10) + '0');
        lcd_data((y % 10) + '0');
        command(0xc0);
        lcd_data((h / 10) + '0');
        lcd_data((h % 10) + '0');
        lcd_data('-');
        lcd_data((m / 10) + '0');
        lcd_data((m % 10) + '0');
        lcd_data('-');
        lcd_data((s / 10) + '0');
        lcd_data((s % 10) + '0');
        IOWR(LED_BASE, 0, 0x00);
        IOWR(HEX_0_BASE, 0, decoder[s % 10]);
        IOWR(HEX_1_BASE, 0, decoder[s / 10]);
        IOWR(HEX_2_BASE, 0, decoder[m % 10]);
        IOWR(HEX_3_BASE, 0, decoder[m / 10]);
        IOWR(HEX_4_BASE, 0, decoder[h % 10]);
        IOWR(HEX_5_BASE, 0, decoder[h / 10]);
    }
}
else
{
    if(IORD(SW_1_BASE, 0) == 0)
    {
        if(IORD(KEY_0_BASE, 0) == 0)

```

```

        {
            m++;
            if(m>=60)
            {
                m=0;
            }
        }
        if(IORD(KEY_1_BASE, 0)==0)
        {
            h++;
            if(h>=24)
            {
                h=0;
            }
        }
        command_edit(0x02); // Clear display
        lcd_data_edit((d / 10) + '0');
        lcd_data_edit((d % 10) + '0');
        lcd_data_edit('-');
        lcd_data_edit((mt / 10) + '0');
        lcd_data_edit((mt % 10) + '0');
        lcd_data_edit('-');
        lcd_data_edit((y / 1000) + '0');
        lcd_data_edit(((y % 1000) / 100) + '0');
        lcd_data_edit(((y % 100) / 10) + '0');
        lcd_data_edit((y % 10) + '0');
        command_edit(0xc0);
        lcd_data_edit((h / 10) + '0');
        lcd_data_edit((h % 10) + '0');
        lcd_data_edit(':');
        lcd_data_edit((m / 10) + '0');
        lcd_data_edit((m % 10) + '0');
        lcd_data_edit(':');
        lcd_data_edit((s / 10) + '0');
        lcd_data_edit((s % 10) + '0');
        IOWR(LED_BASE, 0, 0x00);
        IOWR(HEX_0_BASE, 0, decoder[s % 10]);
        IOWR(HEX_1_BASE, 0, decoder[s / 10]);
        IOWR(HEX_2_BASE, 0, decoder[m % 10]);
        IOWR(HEX_3_BASE, 0, decoder[m / 10]);
        IOWR(HEX_4_BASE, 0, decoder[h % 10]);
        IOWR(HEX_5_BASE, 0, decoder[h / 10]);

    }

}

if((IORD(SW_1_BASE, 0) == 1)&&(IORD(SW_0_BASE, 0) == 0))
{
    if(IORD(KEY_0_BASE, 0)==0)
    {
        m1++;
        if(m1==60)
        {
            m1=0;
        }
    }
    if(IORD(KEY_1_BASE, 0)==0)
    {
        h1++;
        if(h1==24)
        {
            h1=0;
        }
    }
}

```

```

IOWR(HEX_0_BASE, 0, 0xFF);
IOWR(HEX_1_BASE, 0, 0xFF);
IOWR(HEX_2_BASE, 0, decoder[m1 % 10]);
IOWR(HEX_3_BASE, 0, decoder[m1 / 10]);
IOWR(HEX_4_BASE, 0, decoder[h1 % 10]);
IOWR(HEX_5_BASE, 0, decoder[h1 / 10]);

if (m1==m && h1==h)
{
    if(t==0)
    {
        c=5;
        t=1;
    }
    switch (c)
    {
        case 1:
            IOWR(LED_BASE, 0, 0x18);
            break;
        case 2:
            IOWR(LED_BASE, 0, 0x24);
            break;
        case 3:
            IOWR(LED_BASE, 0, 0x42);
            break;
        case 4:
            IOWR(LED_BASE, 0, 0x81);
            break;
        case 5:
            IOWR(LED_BASE, 0, 0xFF);
            break;
        default:
            break;
    }
    c--;
}
else t=0;
}
if(IORD(KEY_3_BASE, 0) == 0){
s=20,h=14,m=50,d=23,mt=4,y=2024;
command_edit(0x02); // Clear display
lcd_data_edit((d / 10) + '0');
lcd_data_edit((d % 10) + '0');
lcd_data_edit('-');
lcd_data_edit((mt / 10) + '0');
lcd_data_edit((mt % 10) + '0');
lcd_data_edit('-');
lcd_data_edit((y / 1000) + '0');
lcd_data_edit(((y % 1000) / 100) + '0');
lcd_data_edit(((y % 100) / 10) + '0');
lcd_data_edit((y % 10) + '0');
command_edit(0xc0);
lcd_data_edit((h / 10) + '0');
lcd_data_edit((h % 10) + '0');
lcd_data_edit(':');
lcd_data_edit((m / 10) + '0');
lcd_data_edit((m % 10) + '0');
lcd_data_edit(':');
lcd_data_edit((s / 10) + '0');
lcd_data_edit((s % 10) + '0');
IOWR(LED_BASE, 0, 0x00);
IOWR(HEX_0_BASE, 0, decoder[s % 10]);
IOWR(HEX_1_BASE, 0, decoder[s / 10]);
IOWR(HEX_2_BASE, 0, decoder[m % 10]);
IOWR(HEX_3_BASE, 0, decoder[m / 10]);

```

```

        IOWR(HEX_4_BASE, 0, decoder[h % 10]);
        IOWR(HEX_5_BASE, 0, decoder[h / 10]);

    }

    IOWR_ALTERA_AVALON_TIMER_STATUS(TIMER_0_BASE,
    ALTERA_AVALON_TIMER_STATUS_TO_MSK);
}

void timer_Init()
{
    unsigned int period = 0;
    IOWR_ALTERA_AVALON_TIMER_CONTROL(TIMER_0_BASE,
    ALTERA_AVALON_TIMER_CONTROL_STOP_MSK);
    period = 50000000 - 1;
    IOWR_ALTERA_AVALON_TIMER_PERIODL(TIMER_0_BASE, period);
    IOWR_ALTERA_AVALON_TIMER_PERIODL(TIMER_0_BASE, (period >> 16));
    IOWR_ALTERA_AVALON_TIMER_CONTROL(TIMER_0_BASE,
    ALTERA_AVALON_TIMER_CONTROL_CONT_MSK |
    ALTERA_AVALON_TIMER_CONTROL_ITO_MSK | ALTERA_AVALON_TIMER_CONTROL_START_MSK);
}

int main()
{
    timer_Init();
    lcd_init();
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_ON_BASE, 0x01);
    IOWR_ALTERA_AVALON_PIO_DATA(LCD_BLON_BASE, 0x01);
    alt_ic_isr_register(0, TIMER_0_IRQ, Timer_IRQ_Handle, (void*)0, (void*)0);
    while(1);
    return 0;
}

```

V. Báo cáo lỗi trong thực hành

Không có lỗi

VI. Kết luận bài thực hành

Chạy đúng với yêu cầu đề ra