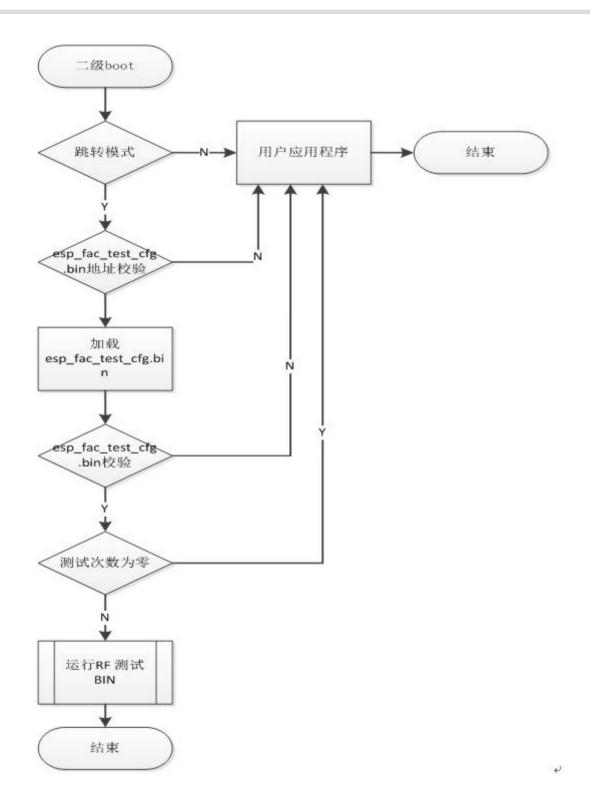
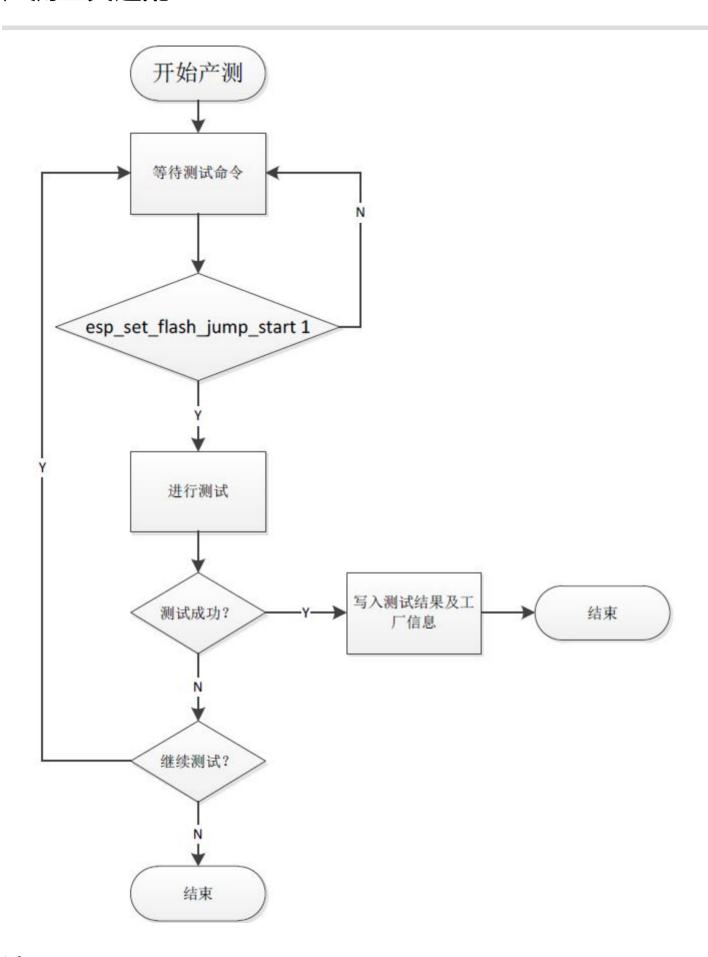
方案概述

ESP8266 的二级 boot 跳转测试功能,是旨在提供产线生产效率的一种测试方案。在测试时,flash中既包含客户的应用固件,也包含产测固件。系统上电后,boot 通过判断flash中的 esp_init_data.bin 的标志位来决定是否进入跳转测试模式。当被配置成使能跳转测试时,运行一定次数的产测固件进行产测。产测结束后,可正常运行客户固件。产测 BIN 文件可以提前和应用固件一起离线烧写至 Flash 中,减少生产测试时在线 download 测试 BIN的时间,提高生产效率。

运行流程



产测工具适配



演示





Config Log Help

ALL START

Position: Cloud

Status

MainWindow

Overall production state

Chip Type : **wROOM-02**Firmware Ver : **ver_1.1.1**PO No : **c0393**

MPN No : 2018020001 Total Status : WORKING

Local production state

Server address: factory.espressif.com

Factory ID : esp-own-test-FID-dbd42d01

Batch ID : bf54c4adf0
Fac-Plan ID : just-test
Loacl Status : xx/xxx



Dut Config Test flow

Spec

[State]run SER OPEN OK [mac]2C3AE808000F UART DOWNLOAD OK ANALOG TEST OK TX TEST OK RX TEST OK [state]pass [state]finish [state]idle [state]run SER OPEN OK [mac]2C3AE808000F UART DOWNLOAD OK ANALOG TEST OK TX TEST OK RX TEST OK [state]pass



START MAC: 2c3ae8080000 STOP

[state]run SER OPEN OK VART DOWNLOAD ERROR SERIAL PORT EXCEPTION [state]finish [state]idle

DUT3

START MAC:2c3ae80800000 STOP

IDLE

[state]run SERIAL PORT EXCEPTION [state]finish [state]idle

IDLE

DUT4

START MAC: 2C3AE808000F

[state]run
SER OFEN OK
[mac]2C3AE808000F
UART DOWNLOAD OK
ANALOG TEST OK
TX TEST OK
RX TEST OK
[state]pass
[state]finish
[state]idle

文档

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