

Lecture 18: March 11 , 2020

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18.1 Code Generation - x86 code generation

- Branching Instructions

```
1 jmp label ; sets eip to constant value
2 cmp eax, ebx ; Check two values, result of comparison is stored in flags
3 je label ; ==
4 jne label ; !=
5 jg label ; > (Signed manner). jge, jl, jle are all also valid
6 ja label ; > (Unsigned manner) ja, jb, jbe are all also valid
```

- Call instructions

```
1 call label ; pushes eip on stack and sets eip to label
2 ret ; pops eip from stack
3 push eax
4 pop eax
5 int 0x80 ; system call
```

- Exiting

- Set eax to 1
- Set ebx with exit code

- Assembler Directives

```
1 dd 1234 ; .word (puts constant in machine language output)
2 db "hello" ; Output one byte for each character in the string
3 global label ; export label
4 extern label ; import label
5 section .text ; executable code
6 section .data ; readable and writeable data
```

18.2 Code Generation - Key Tasks

1. Plan data layout
 - Document
 - Write helper functions to generate code to access data
2. Generate code for each kind of ast node

18.3 Data in JOOS/Java

- Local variables (parameters)
- Objects
 - Instance field
 - type tag (pointer to class)
- Classes
 - static fields
 - pointers to method implementations
- Array
 - type tag
 - length
 - elements
- Primitives
 - 32 bit Integers
 - 16 bit Short
 - 16 bit Char
 - 8 bit byte
 - 1 bit boolean
 - 32 bit References to object

18.4 Storage options

- Constants
 - at labelled memory location (`dd 1234`)
 - as part of instruction (`mov eax, 42`)
- Registers
 - Limited number available in x86
- Fixed (labelled) memory addresses
- Stack - LIFO