



## LETTER OF VERIFICATION

..../10/2024

To whom this may concern,

This is to certify that Mr. Tran Nhat Minh was an employee of Renesas Design Vietnam with further details as below:

- **Duration:** 01/03/2018 – 15/06/2019
- **Basis:** Full-time (40 hours/week)
- **Job title:** Hardware Design Engineer
- **Responsibilities:**

- Verify/Debug logic/schematic of ICs before tape-out.
- Build environment to verify RTL Designs by using System Verilog Language.
- Experienced in protocol USB, PCIe.
- Experienced in Linux and scripting languages (C-shell, perl, python).
- IC Design and Verification - develop firmware to verify IC design.

If you have any further questions, please do not hesitate to contact me at the undersigned.

Sincerely,

Name: Hoi Nguyen

Position:

Tel:

Work Email:

**Renesas Design Vietnam**

Address: Lot W.29-30-31a, Tan Thuan Road, Tan Thuan Export Processing Zone, District 7, Ho Chi Minh City,

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