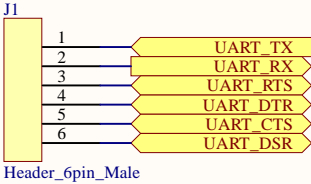
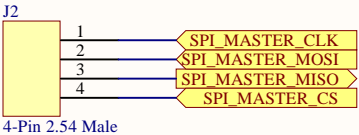


CONNECTORS

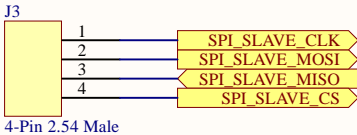
UART Connector



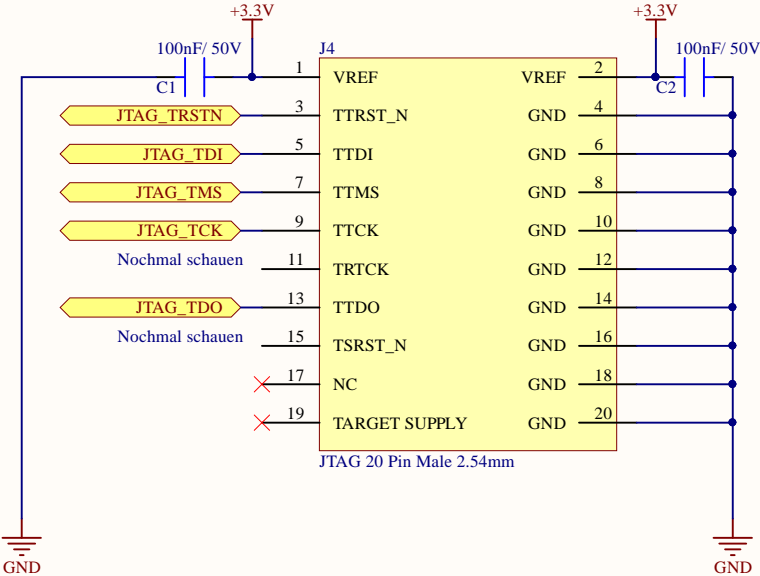
SPI-Master Connector



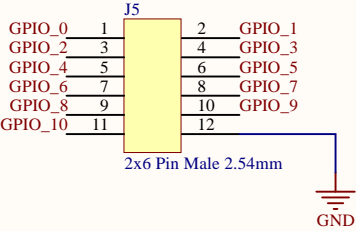
SPI-Slave Connector



JTAG Connector



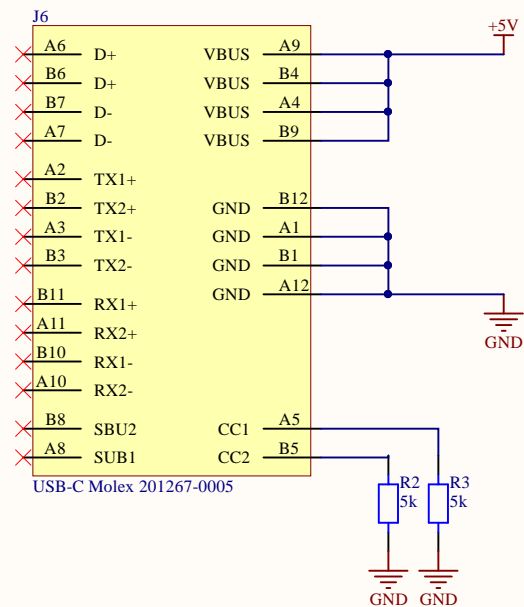
GPIO Connector



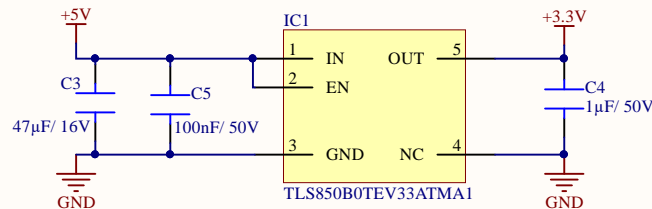
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Size	Number	Revision
A4		
Date:	11.05.2020	Sheet of
File:	C:\Users\...\SchematicPorts.SchDoc	Drawn By:

POWER SUPPLY

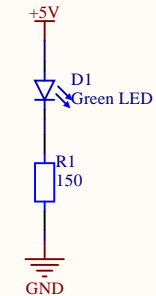
USB-C Connector



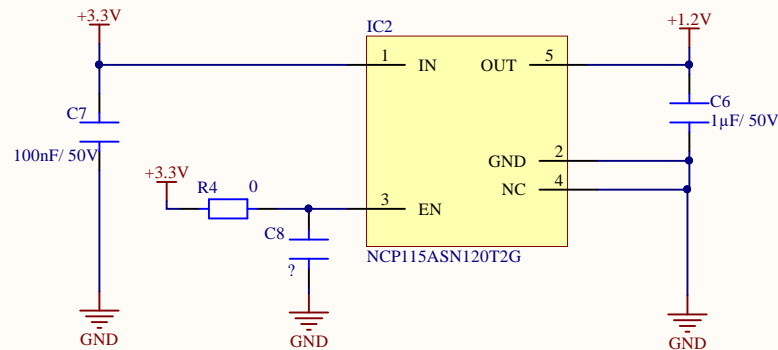
Linear Voltage Regulator 3.3V



LED Power Indicator



Linear Voltage Regulator 1.2V



With the RC element a time delay for switching on the linear voltage regulator can be set. If not needed use R = 0hm and do not solder the capacitor C?.

Title		
Size A4	Number	Revision
Date:	11.05.2020	Sheet of
File:	C:\Users\...\SchematicPowerSupply.SchDoc	
Drawn By:		

PULPino MCU

PORTS

GPIO_[0..10] 

SPI_SLAVE_CLK_i
SPI_SLAVE_CS_i
SPI_SLAVE_SDO
SPI_SLAVE_SDI

SPI_MASTER_CSN
SPI_MASTER_CLK
SPI_MASTER_SDO
SPI_MASTER_SDI

GPIO_10
GPIO_9
GPIO_8
GPIO_7
GPIO_6
GPIO_5
GPIO_4
GPIO_3
GPIO_2
GPIO_1
GPIO_0

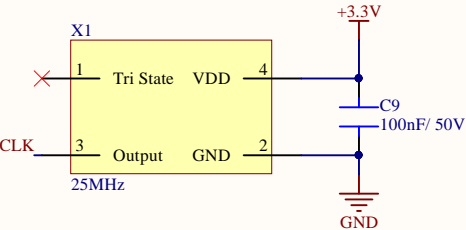
UART_TX
UART_RX
UART_RTS
UART_DTR
UART_CTS
UART_DSR

+1.2V
+3.3V
FetchEN
RESET
CLK
JTAG_TCK
JTAG_TRSTN
JTAG_TMS
JTAG_TDI
JTAG_TDO

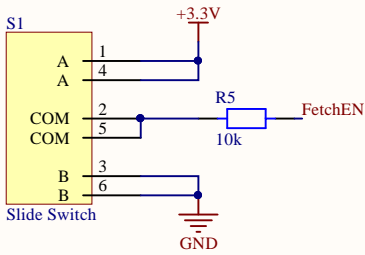
ThermalPad
VSS_IO
VSS_IO
VSS_CORE
VSS_CORE

PULPino V1 by Bastian Koppelman

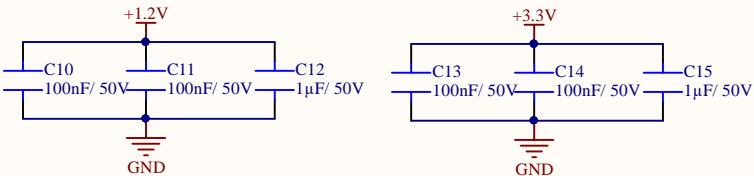
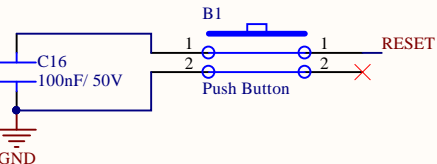
Clock



Fetch Enable



Reset Button



Title		
Size	Number	Revision
A4		
Date:	11.05.2020	Sheet of
File:	C:\Users\...\SchematicPULPinoMCU.SchDDrawn By:	

U_SchematicPULPiniMCU
SchematicPULPiniMCU.SchDoc



U_SchematicPowerSupply
SchematicPowerSupply.SchDoc

Title			
Size A4	Number		Revision
Date:	11.05.2020	Sheet	of
File:	C:\Users\...\TopSheet.SchDoc	Drawn By:	