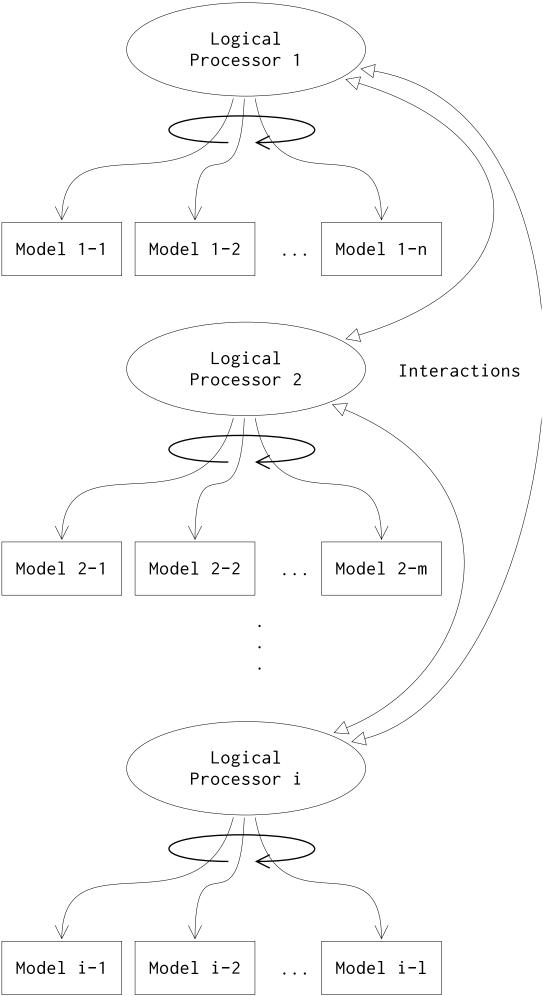


Scheduling of a Cyber-Physical System Simulation



Distributed simulation

Theoretical framework



Higher-level Global Scheduling parallel domain

Lower-level Local Scheduling sequencial domain

Scheduled entity Model

SEAplanes

High Level Architecture standard

i.

Simulation Execution Architecture Proto-Logical processor Allocation Nodes with Extensible inline Scheduler

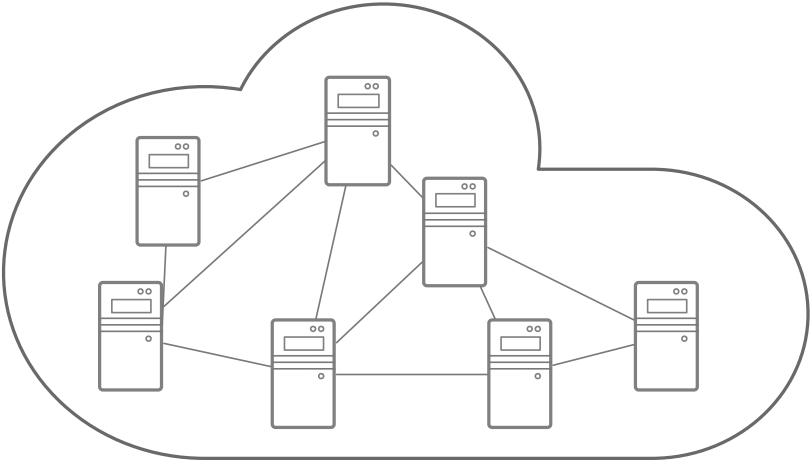
Architecture Proto-Logical

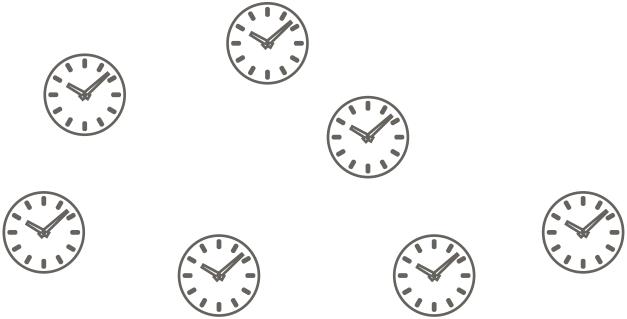
Simulation Execution

with Extensible inline

Scheduler

processor Allocation Nodes





Model A

Model B

Model



Logical clock A

Logical clock B

Logical clock C