

Scheduling of a Cyber-Physical System Simulation



The simulation execution architecture

Formalization of simulation scheduling

 $sea = \langle LP, gs, c \rangle$

 $\langle T, ls, c \rangle$ lp

Logical processors

High-level scheduler

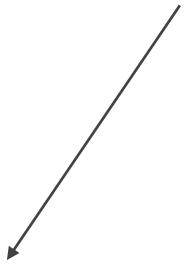
Interprocessor communication

Low-level scheduler

Intraprocessor communication



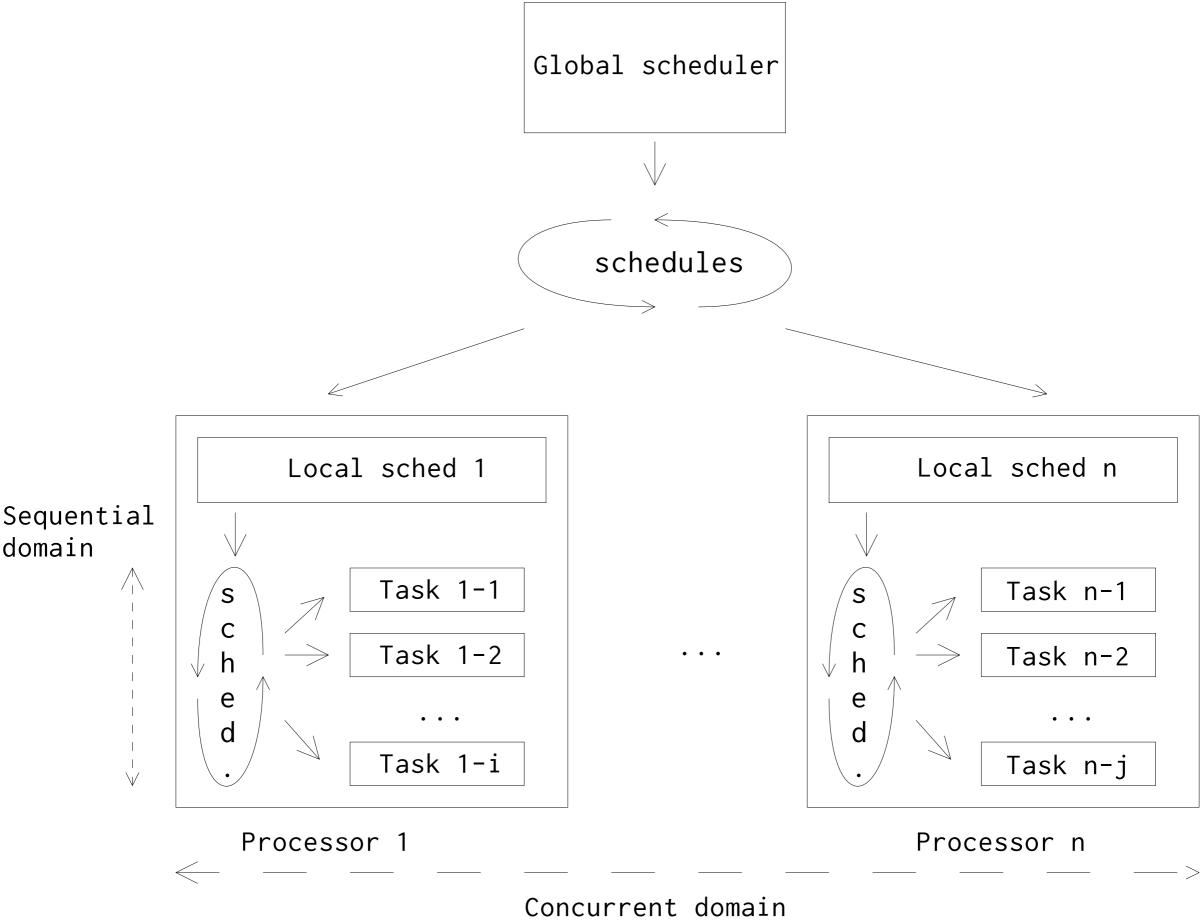












The allocation

 $sea = \langle LP, gs, c \rangle$

 $\langle T, ls, c \rangle$ lp











