

| Lock\_DD | 2022-10-13

# Lock\_DD

Software and calibration documentation (detailed) [COMP]



# I [Lock\_DD]

## **1 Function Definition**

#### 1.1 Purpose

Input the purpose of developing this function

#### 1.2 Introduction

Lock device driver

## **2 Function Description**

# 2.1 Behavior in normal mode

Figure 1 Lock\_DD [Lock\_DD]



(in 00.0,1) with 8 and 4		
	op woodop ngaj so na si	
		upa udopropropriate con
	10000007100740,746,19	TRANSPORTED COM
	MANAGER AND THE REAL PROPERTY.	The refer the parking
	recording any sales, ray, an	The well the second
	recording to the late	
	Miles Trapes, Net, 18	The appropriation
	-Borrandolphic, say, an	———
	1800 miles (Mr. 18	the standales and
		operations and the state
	Minimum (Late, Text, 19	ulter representation con-
	*BOST mercel ( Aug. 10)	the streethies, car
	1800 HER \$40, No. 9	TRACKT HART RAIL AND
	1000148710[40,746,9	riparar material paid, com
	recompagnersons, say, an	The voltage and the
	reconstruction and an exemption of the	the width the prices
	-8000084 se-suso, 'se, ir	The vicinal and on
	*80000(14 se*suso, 164, 18	The word of the contract
	180001041, risk, 18	——-Œ
	10071000401004012	the soleting is con-
\$5.00 ()   00 \$5.00 ()   00 \$5.00 ()   00		- NEW THOMOMY SERVE

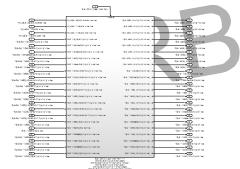




Figure 2 Lock\_DD\_Lock\_DD\_Runnable\_IN\_10ms\_sys [Lock\_DD\_Lock\_DD\_Runnable\_IN\_10ms\_sys]

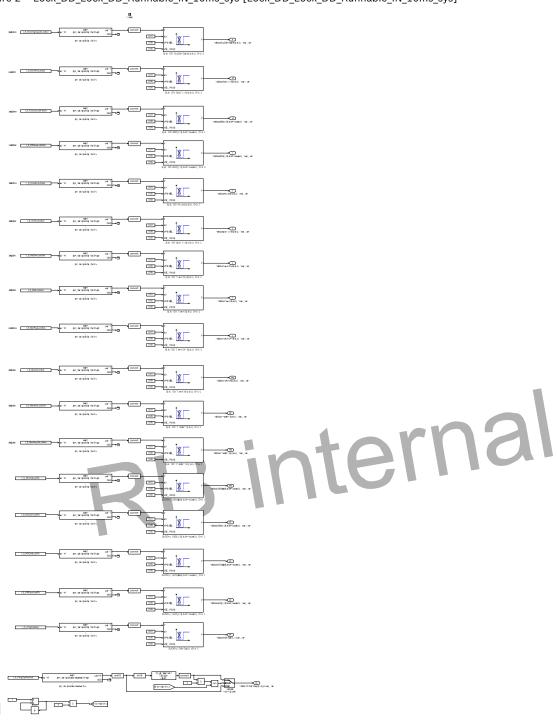


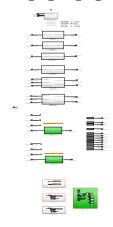
Figure 3 Lock\_DD\_Lock\_DD\_Runnable\_Init [Lock\_DD\_Lock\_DD\_Runnable\_Init]



Lock\_DD 4 | 25

**BOSCH** 

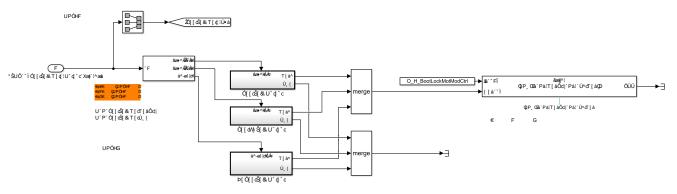
Figure 4 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys]





Lock\_DD 5 | 25

Figure 5 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_BootLockMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_BootLockMotorOutput]

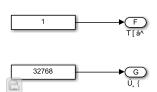


位(点aT(4;v))
- (有aT(4;v)-Xa)-, Har

Figure 6 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_BootLockMotorOutput\_BootLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_-sys\_BootLockMotorOutput\_BootLockOutput]



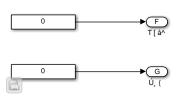
Figure 7 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_BootLockMotorOutput\_BootUnLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_1-0ms\_sys\_BootLockMotorOutput\_BootUnLockOutput]



&æ:^KÁ,Áq

Figure 8 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_BootLockMotorOutput\_NoBootLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_1-0ms\_sys\_BootLockMotorOutput\_NoBootLockOutput]





Lock\_DD 6 | 25

Figure 9 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ChrgCap-LockMotorOutput]

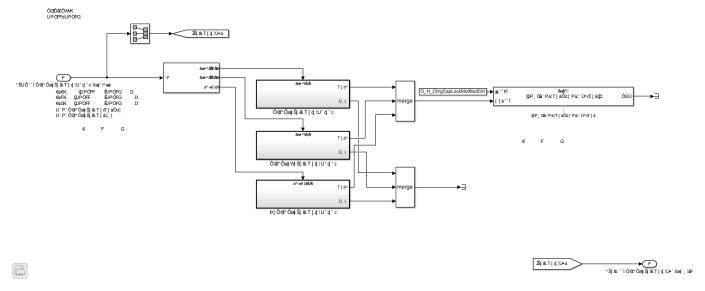
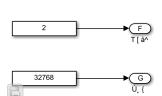


Figure 10 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput\_ChrgCapLockMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput]

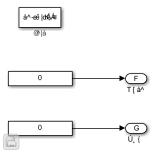


Figure 11 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput\_ChrgCapUnLockMotorOutput [Lock\_DD\_Lock\_DD\_-Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput\_ChrgCapUnLockMotorOutput]



&æ•^KÁ,Ána @∖lå

Figure 12 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput\_NoChrgCapLockMotorOutput [Lock\_DD\_Lock\_DD\_-Runnable\_OUT\_10ms\_sys\_ChrgCapLockMotorOutput\_NoChrgCapLockMotorOutput]



Lock\_DD 7 | 25

Figure 13 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput]

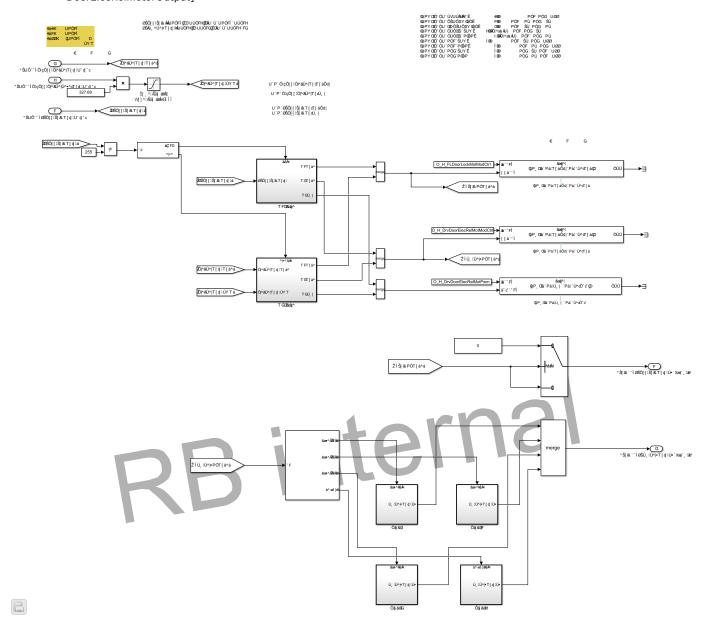


Figure 14 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_Cinch [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_-DrvDoorElecRelMotorOutput\_Cinch]





Lock\_DD 8 | 25



Figure 15 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_Cinch1 [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_-sys\_DrvDoorElecRelMotorOutput\_Cinch1]





Figure 16 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_Cinch2 [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_-sys\_DrvDoorElecRelMotorOutput\_Cinch2]





Figure 17 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_Cinch3 [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_-sys\_DrvDoorElecRelMotorOutput\_Cinch3]



Lock\_DD 9 | 25

Figure 18 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active]

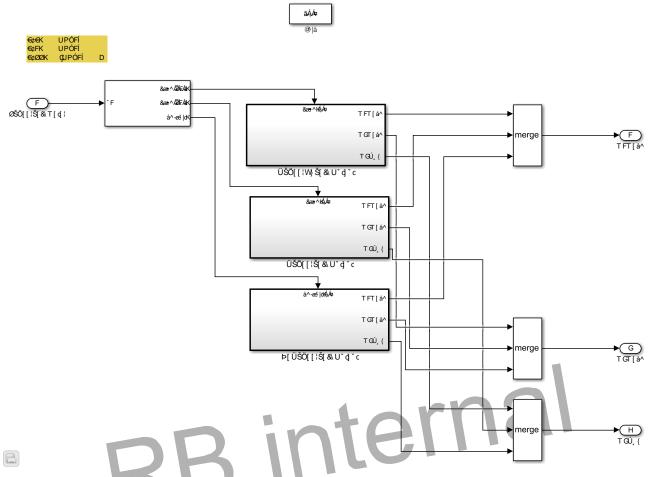
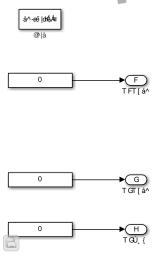


Figure 19 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active\_NoRLDoorLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active\_NoRLDoorLockOutput]



Lock\_DD 10 | 25

Figure 20 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active\_RLDoorLockOutput [Lock\_DD\_Lock\_DD\_-Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active\_RLDoorLockOutput]





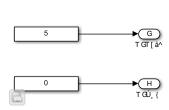
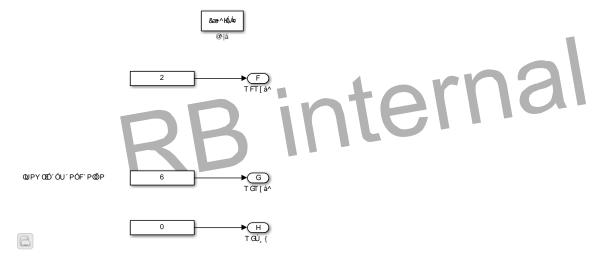


Figure 21 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active\_RLDoorUnLockOutput [Lock\_DD\_Lock\_DD\_-Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M1Active\_RLDoorUnLockOutput]



Lock\_DD 11 | 25

Figure 22 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active]

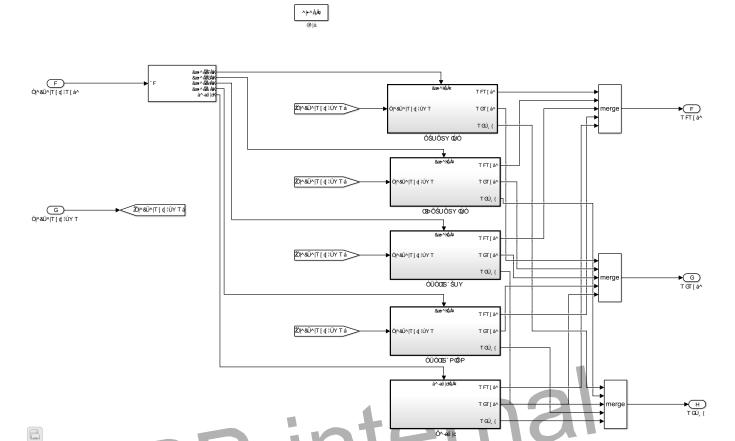
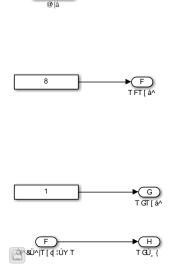


Figure 23 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_ANCLOCKWISE [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_ANCLOCKWISE]



Lock\_DD 12 | 25

Figure 24 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_BREAK\_HIGH [Lock\_DD\_Lock\_DD\_Runnable\_-OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_BREAK\_HIGH]

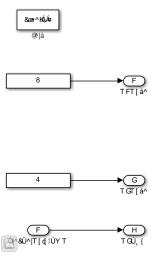
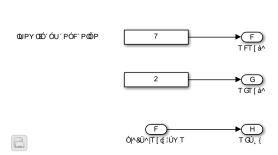


Figure 25 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_BREAK\_LOW [Lock\_DD\_Lock\_DD\_Runnable\_-OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_BREAK\_LOW]



Figure 26 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_CLOCKWISE [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_CLOCKWISE]



Lock\_DD 13 | 25

Figure 27 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_Default [Lock\_DD\_Lock\_DD\_Runnable\_O-UT\_10ms\_sys\_DrvDoorElecRelMotorOutput\_M2Active\_Default]

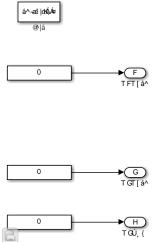


Figure 28 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_- ElectcGlvBoxLockMotorOutput]

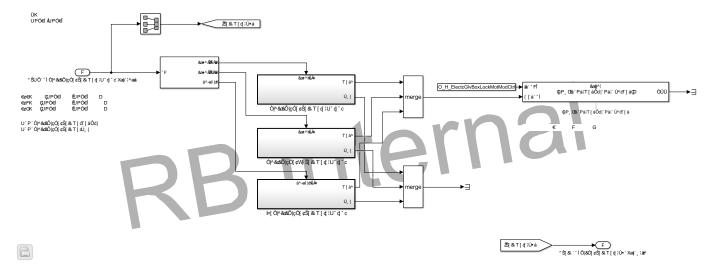
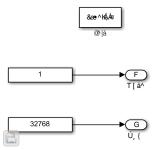


Figure 29 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput\_ElectcGlvBoxLockMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput\_ElectcGlvBoxLockMotorOutput]



Lock\_DD 14 | 25



Figure 30 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput\_ElectcGlvBoxUnLockMotorOutput [Lock\_DD\_Lock\_-DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput\_ElectcGlvBoxUnLockMotorOutput]



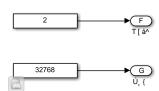
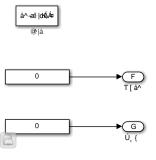


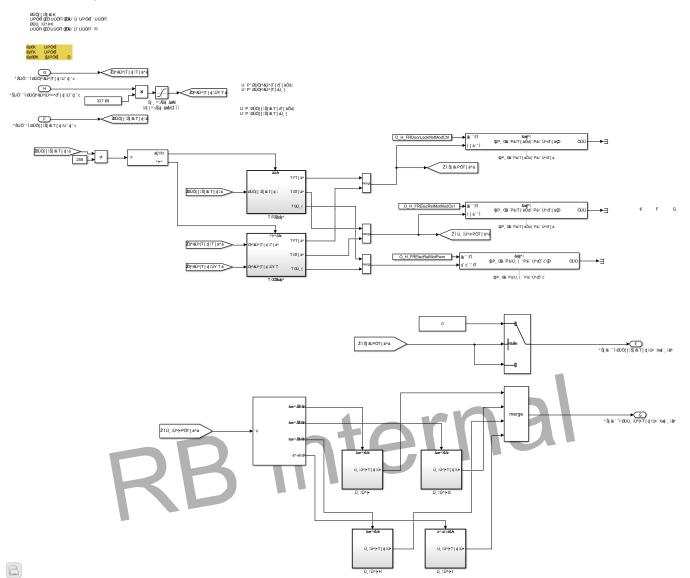
Figure 31 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput\_NoElectcGlvBoxLockMotorOutput [Lock\_DD\_Lock\_-DD\_Runnable\_OUT\_10ms\_sys\_ElectcGlvBoxLockMotorOutput\_NoElectcGlvBoxLockMotorOutput]





Lock\_DD 15 | 25

Figure 32 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput]



Lock\_DD 16 | 25

Figure 33 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_F-RElecRelMotorOutput\_M1Active]

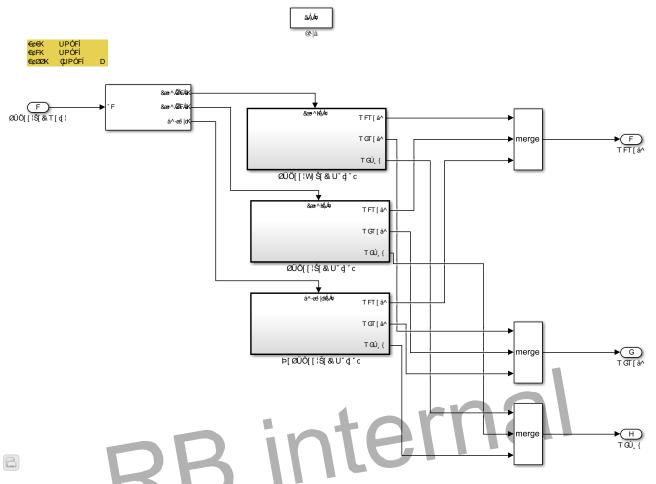
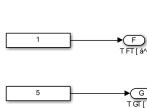


Figure 34 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active\_FRDoorLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active\_FRDoorLockOutput]



UPÓEÏPÓFÁPAT@ UÚÓFÍPÓGÁĞ[,

UÚÓFÍ PÓFÁŠ[ UÚÓFI PÓGÁJØØ Lock\_DD 17 | 25



Figure 35 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active\_FRDoorUnLockOutput [Lock\_DD\_Lock\_DD\_-Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active\_FRDoorUnLockOutput]



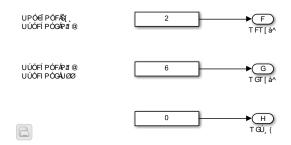


Figure 36 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active\_NoFRDoorLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M1Active\_NoFRDoorLockOutput]



Lock\_DD 18 | 25

Figure 37 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_F-RElecRelMotorOutput\_M2Active]

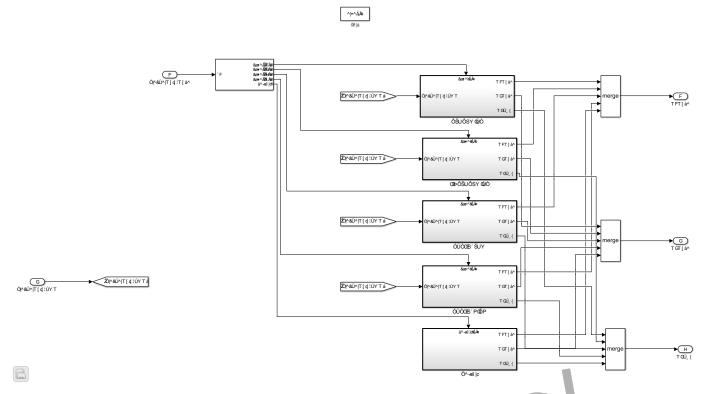


Figure 38 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_ANCLOCKWISE [Lock\_DD\_Lock\_DD\_Runnable\_O-UT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_ANCLOCKWISE]



Figure 39 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_BREAK\_HIGH [Lock\_DD\_Lock\_DD\_Runnable\_O-UT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_BREAK\_HIGH]

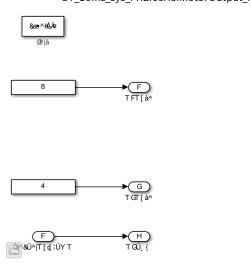


Figure 40 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_BREAK\_LOW [Lock\_DD\_Lock\_DD\_Runnable\_O-UT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_BREAK\_LOW]

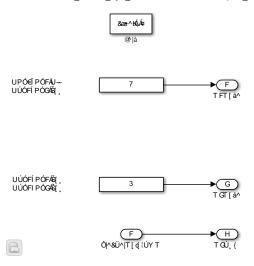


Figure 41 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_CLOCKWISE [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_-10ms\_sys\_FRElecRelMotorOutput\_M2Active\_CLOCKWISE]



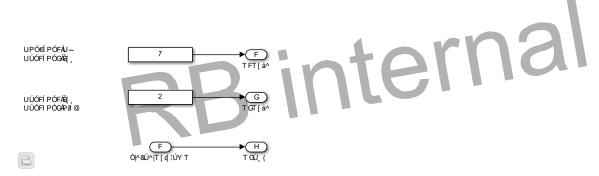
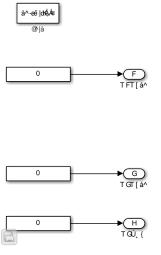


Figure 42 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_M2Active\_Default [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_1-0ms\_sys\_FRElecRelMotorOutput\_M2Active\_Default]



Lock\_DD 20 | 25



Figure 43 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_PwrRels [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_PwrRels]





Figure 44 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_PwrRels2 [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_PwrRels2]

8æ•^hÁ,Án; O£8ca[}ÁÚ[¦c



Figure 45 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_PwrRels3 [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_F-RElecRelMotorOutput\_PwrRels3]



Figure 46 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_FRElecRelMotorOutput\_PwrRels4 [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_F-RElecRelMotorOutput\_PwrRels4]

å^-æĕ|dxÁ,Ána ORBoal}ÁÚI¦c



Lock\_DD 21 | 25

Figure 47 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_OilTankLockMotorOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_OilTank-LockMotorOutput]

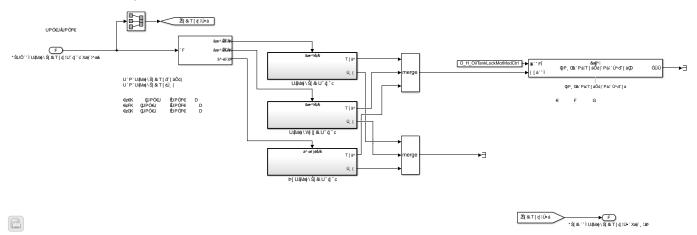


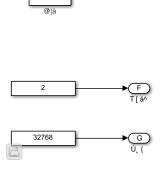
Figure 48 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_OilTankLockMotorOutput\_NoOilTankLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_O-UT\_10ms\_sys\_OilTankLockMotorOutput\_NoOilTankLockOutput]



Figure 49 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_OilTankLockMotorOutput\_OilTankLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_-10ms\_sys\_OilTankLockMotorOutput\_OilTankLockOutput]



Figure 50 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_OilTankLockMotorOutput\_OilTankUnlockOutput [Lock\_DD\_Lock\_DD\_Runnable\_O-UT\_10ms\_sys\_OilTankLockMotorOutput\_OilTankUnlockOutput]



8æe∙^kÁ,Áq

Lock\_DD 22 | 25

Figure 51 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput]

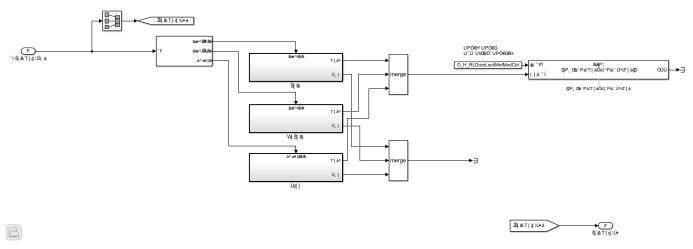


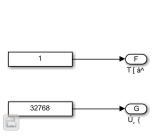
Figure 52 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput\_Lock [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoor-LockOutput\_Lock]



Figure 53 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput\_Stop [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput\_Stop]



Figure 54 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput\_UnLock [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RLDoorLockOutput\_UnLock]



Lock\_DD 23 | 25

Figure 55 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput]

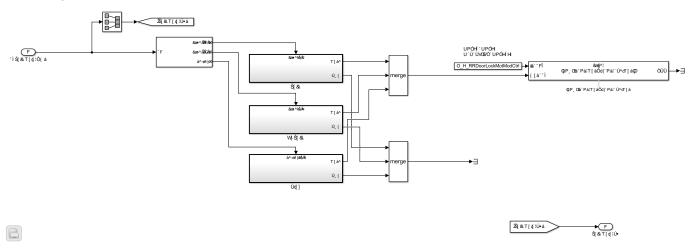


Figure 56 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput\_Lock [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput\_Lock]



Figure 57 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput\_Stop [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput\_Stop]



Figure 58 Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput\_UnLock [Lock\_DD\_Lock\_DD\_Runnable\_OUT\_10ms\_sys\_RRDoorLockOutput\_UnLock]

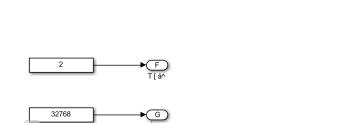


Table 1 Data Types for port interfaces [PortInterfaceDataTypes]

Port	AccessMode	Interface	DE	Datatype
gHoodLock_bLockCmdT-rigUds	ExplicitReceive	gHoodLock_bLockCmdT- rigUds	Val	boolean

Port	AccessMode	Interface	DE	Datatype
gHoodLock_u8LockCmd	ExplicitReceive	gHoodLock_u8LockCmd	Val	uint8
gHoodLock_u8LockCmd- Uds	ExplicitReceive	gHoodLock_u8LockCmd- Uds	Val	uint8
gLockCtrl_u8BootLockMo- torOutput	ExplicitReceive	gLockCtrl_u8BootLockMo- torOutput	Val	uint8
gLockCtrl_u8ChrgCap- LockMotorOutput	ExplicitReceive	gLockCtrl_u8ChrgCap- LockMotorOutput	Val	uint8
gLockCtrl_u8ElectcGlv- BoxLockMotorOutput	ExplicitReceive	gLockCtrl_u8ElectcGlv- BoxLockMotorOutput	Val	uint8
gLockCtrl_u8FRDoorLock- MotorOutput	ExplicitReceive	gLockCtrl_u8FRDoorLock- MotorOutput	Val	uint8
gLockCtrl_u8OilTankLock- MotorOutput	ExplicitReceive	gLockCtrl_u8OilTankLock- MotorOutput	Val	uint8
gLockCtrl_u8RLChildLock- MotorOutput	ExplicitReceive	gLockCtrl_u8RLChildLock- MotorOutput	Val	uint8
gLockCtrl_u8RLDoorLock- MotorOutput	ExplicitReceive	gLockCtrl_u8RLDoorLock- MotorOutput	Val	uint8
gLockCtrl_u8RRChildLock- MotorOutput	ExplicitReceive	gLockCtrl_u8RRChildLock- MotorOutput	Val	uint8
gLockCtrl_u8RRDoorLock- MotorOutput	ExplicitReceive	gLockCtrl_u8RRDoorLock- MotorOutput	Val	uint8
gLockCtrl_u8FLDoorElec- RelMotorOutput	ExplicitReceive	gLockCtrl_u8FLDoorElec- RelMotorOutput	Val	uint8
gLockCtrl_u8FLDoorElec- RelResetMotorOutput	ExplicitReceive	gLockCtrl_u8FLDoorElec- RelResetMotorOutput	Val	uint8
gLockCtrl_u8FRElecRel- MotorOutput	ExplicitReceive	gLockCtrl_u8FRElecRel- MotorOutput	Val	uint8
gLockCtrl_u8FRElecRelRe- setMotorOutput	ExplicitReceive	gLockCtrl_u8FRElecRelRe- setMotorOutput	Val	uint8
gLockCtrl_u8RLDoorElec- RelMotorOutput	ExplicitReceive	gLockCtrl_u8RLDoorElec- RelMotorOutput	Val	uint8
gLockCtrl_u8RLDoorElec- RelPWMMotorOutput	ExplicitReceive	gLockCtrl_u8RLDoorElec- RelPWMMotorOutput	Val	uint8
gLockCtrl_u8RRDoorElec- RelMotorOutput	ExplicitReceive	gLockCtrl_u8RRDoorElec- RelMotorOutput	Val	uint8
gLockCtrl_u8RRDoorElec- RelPWMMotorOutput	ExplicitReceive	gLockCtrl_u8RRDoorElec- RelPWMMotorOutput	Val	uint8
gLockCtrl_u8FLDoorLock- MotorOutput	ExplicitReceive	gLockCtrl_u8FLDoorLock- MotorOutput	Val	uint8
gLock_u8DiagEnbCnd	ExplicitReceive	gLock_u8DiagEnbCnd	Val	uint8
gLock_bDrvChrgCapUn- lockSwt	ExplicitSend	gLock_bDrvChrgCapUn- lockSwt	Val	boolean
gLock_bExtTrunkUnlock- Swt	ExplicitSend	gLock_bExtTrunkUnlock- Swt	Val	boolean
gLock_bFLDoorLockFbSt	ExplicitSend	gLock_bFLDoorLockFbSt	Val	boolean
gLock_bFRDoorLockFbSt	ExplicitSend	gLock_bFRDoorLockFbSt	Val	boolean
gLock_bHoodUnlockSwt	ExplicitSend	gLock_bHoodUnlockSwt	Val	boolean
gLock_bIntTrunkUnlock- Swt	ExplicitSend	gLock_bIntTrunkUnlock- Swt	Val	boolean
gLock_bMasterDrvLock- Swt	ExplicitSend	gLock_bMasterDrvLock- Swt	Val	boolean
gLock_bMasterLockSwt	ExplicitSend	gLock_bMasterLockSwt	Val	boolean
gLock_bMasterPsgLock- Swt	ExplicitSend	gLock_bMasterPsgLock- Swt	Val	boolean
gLock_bMasterUnlockSwt	ExplicitSend	gLock_bMasterUnlockSwt	Val	boolean
gLock_bMechKeyLockSwt	ExplicitSend	gLock_bMechKeyLockSwt	Val	boolean
gLock_bMechKeyUnlock- Swt	ExplicitSend	gLock_bMechKeyUnlock- Swt	Val	boolean



Port	AccessMode	Interface	DE	Datatype
gLock bRLChildLockFbSt	ExplicitSend	gLock bRLChildLockFbSt	Val	boolean
gLock bRLDoorLockFbSt	ExplicitSend	gLock bRLDoorLockFbSt	Val	boolean
gLock_bRRChildLockFbSt	ExplicitSend	gLock_bRRChildLockFbSt	Val	boolean
gLock_bRRDoorLockFbSt	ExplicitSend	gLock_bRRDoorLockFbSt	Val	boolean
gLock_bChrgCapSwt	ExplicitSend	gLock_bChrgCapSwt	Val	boolean
gLock_u16ChrgCapPos- Volt	ExplicitSend	gLock_u16ChrgCapPos- Volt	Val	uint16
gLock_bFLPwrRelsMotor- MinusSt	ExplicitSend	gLock_bFLPwrRelsMotor- MinusSt	Val	boolean
gLock_bFLPwrRelsMotor- PlusSt	ExplicitSend	gLock_bFLPwrRelsMotor- PlusSt	Val	boolean
gLock_bFRPwrRelsMotor- MinusSt	ExplicitSend	gLock_bFRPwrRelsMotor- MinusSt	Val	boolean
gLock_bFRPwrRelsMotor- PlusSt	ExplicitSend	gLock_bFRPwrRelsMotor- PlusSt	Val	boolean
gLock_bRLPwrRelsMotor- MinusSt	ExplicitSend	gLock_bRLPwrRelsMotor- MinusSt	Val	boolean
gLock_bRLPwrRelsMotor- PlusSt	ExplicitSend	gLock_bRLPwrRelsMotor- PlusSt	Val	boolean
gLock_bRRPwrRelsMotor- MinusSt	ExplicitSend	gLock_bRRPwrRelsMotor- MinusSt	Val	boolean
gLock_bRRPwrRelsMotor- PlusSt	ExplicitSend	gLock_bRRPwrRelsMotor- PlusSt	Val	boolean
gLock_u8BootLockMotor- Ps	ExplicitSend	gLock_u8BootLockMotor- Ps	Val	uint8
gLock_u8ChrgCapLockMo- torPs	ExplicitSend	gLock_u8ChrgCapLockMo- torPs	Val	uint8
gLock_u8FLDoorLockMo- torPs	ExplicitSend	gLock_u8FLDoorLockMo- torPs	Val	uint8
gLock_u8FLPwrRelsMotor- Ps	ExplicitSend	gLock_u8FLPwrRelsMotor- Ps	Val	uint8
gLock_u8FRDoorLockMo- torPs	ExplicitSend	gLock_u8FRDoorLockMo- torPs	Val	uint8
gLock_u8FRPwrRelsMo- torPs	ExplicitSend	gLock_u8FRPwrRelsMo- torPs	Val	uint8
gLock_u8GlcBoxLockMo- torPs	ExplicitSend	gLock_u8GlcBoxLockMo- torPs	Val	uint8
gLock_u8OilTankLockMo- torPs	ExplicitSend	gLock_u8OilTankLockMo- torPs	Val	uint8
gLock_u8RLChildLockMo- torPs	ExplicitSend	gLock_u8RLChildLockMo- torPs	Val	uint8
gLock_u8RLDoorLockMo- torPs	ExplicitSend	gLock_u8RLDoorLockMo- torPs	Val	uint8
gLock_u8RLPwrRelsMo- torPs	ExplicitSend	gLock_u8RLPwrRelsMo- torPs	Val	uint8
gLock_u8RRChildLockMo- torPs	ExplicitSend	gLock_u8RRChildLockMo- torPs	Val	uint8
gLock_u8RRDoorLockMo- torPs	ExplicitSend	gLock_u8RRDoorLockMo- torPs	Val	uint8
gLock_u8RRPwrRelsMo- torPs	ExplicitSend	gLock_u8RRPwrRelsMo- torPs	Val	uint8