KL15PwrSply_DD | 2022-10-17

KL15PwrSply_DD

[COMP]

I [KL15PwrSply_DD]

1 Function Definition

1.1 Purpose

Power Distribution Drive and Fault Diagnosis

1.2 Introduction

Power Distribution Drive and Power Distribution Fault Diagnosis and Sleep and Wakeup

2 Function Description

2.1 Behavior in normal mode

Figure 1 KL15PwrSply_DD [KL15PwrSply_DD]

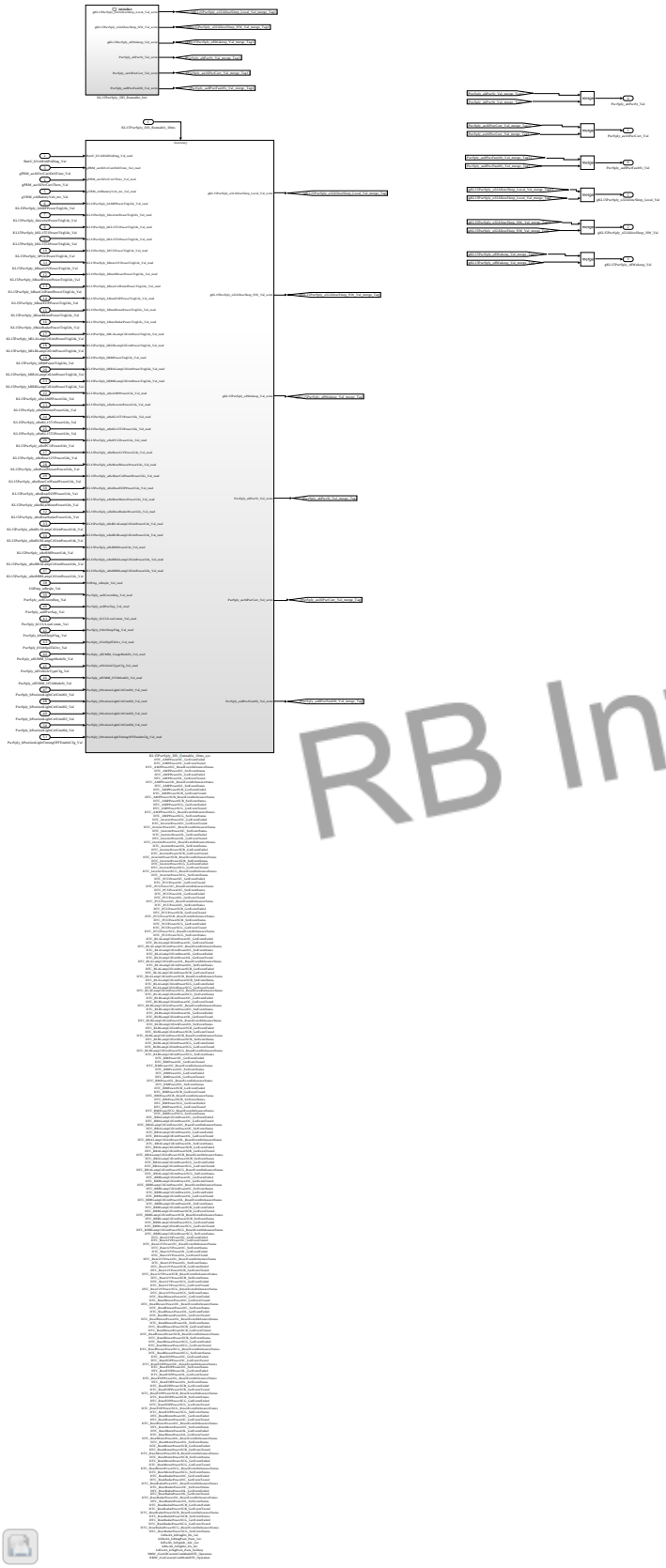

Figure 2 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys]

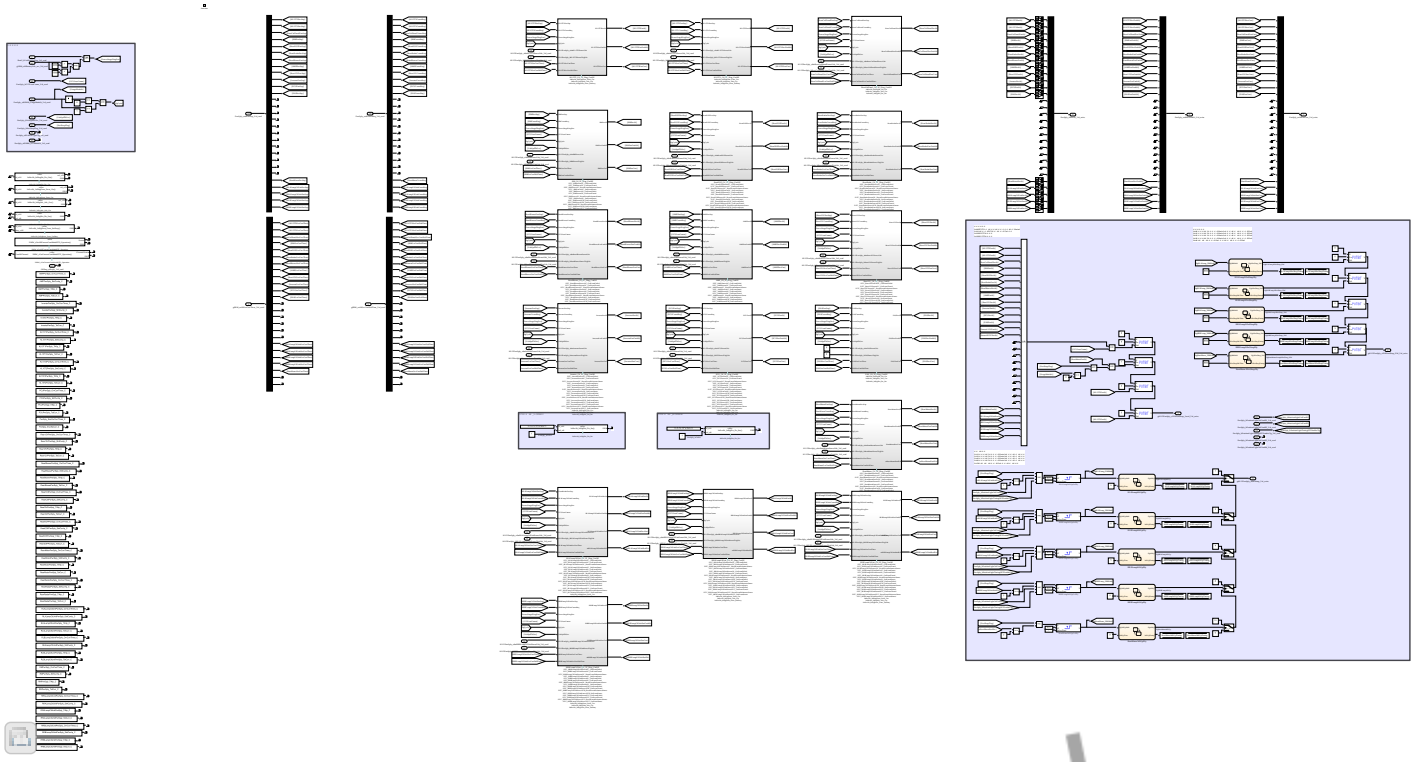

Figure 3 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff]

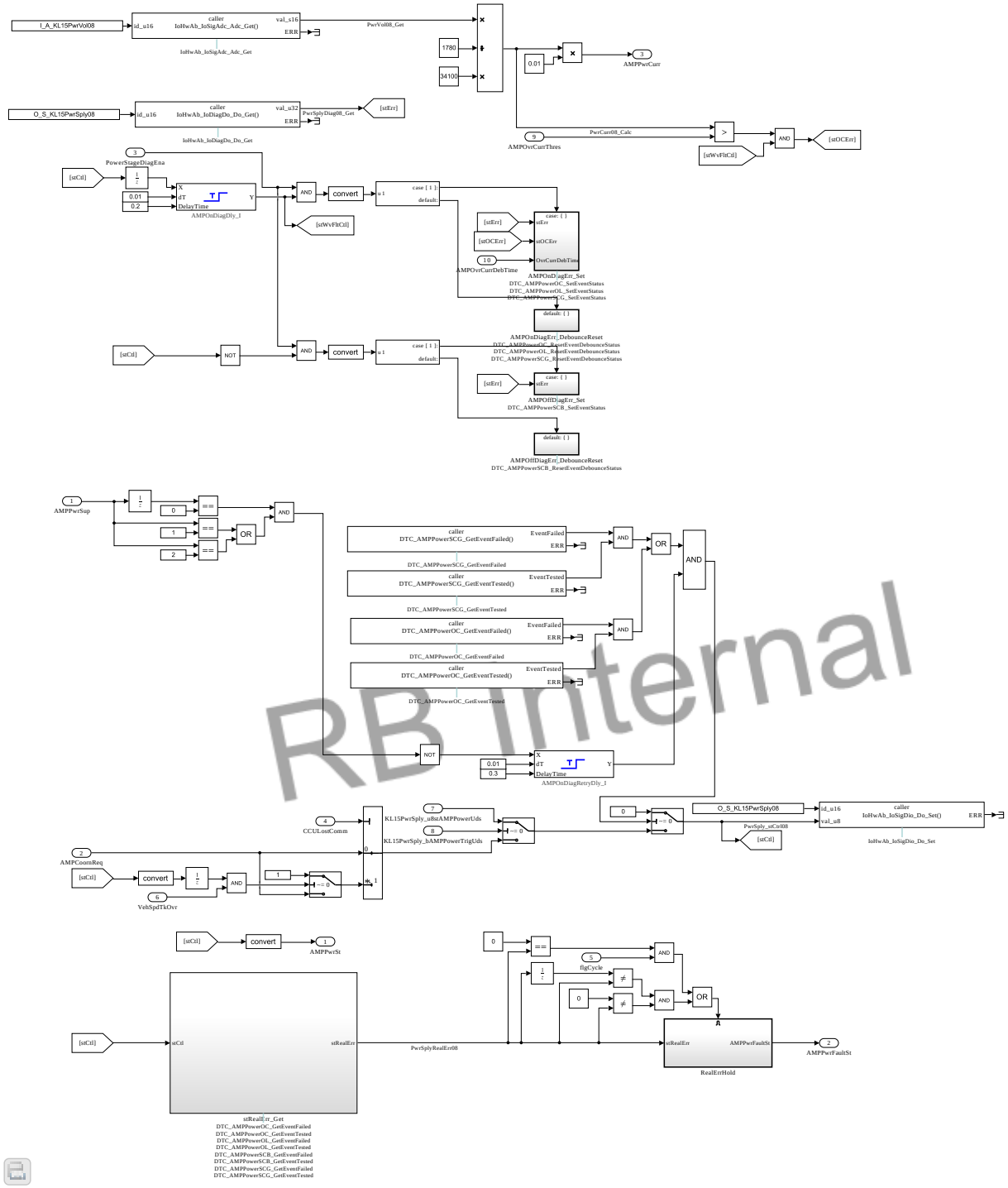

Figure 4 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOffDiagErr_DebounceReset]

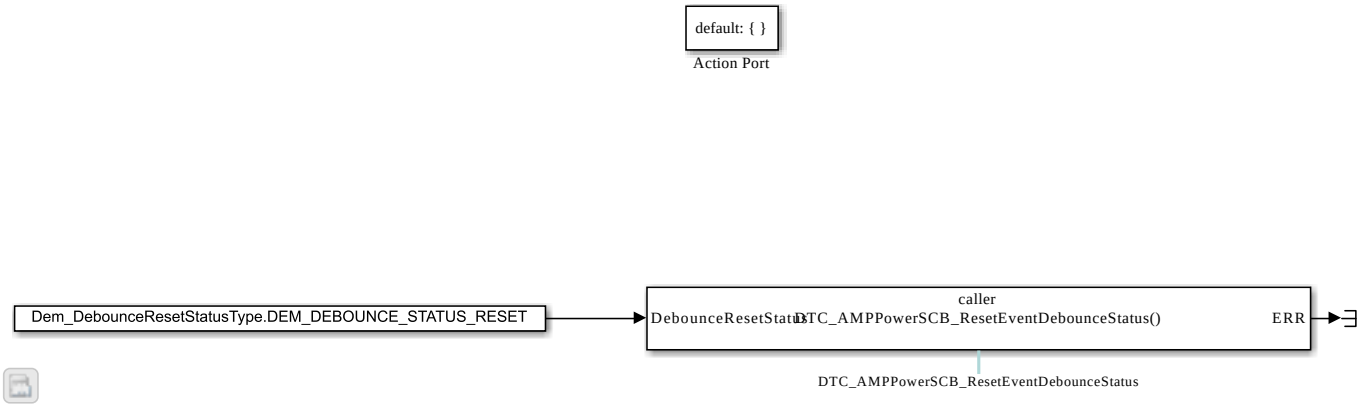

Figure 5 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOffDiagErr_Set]

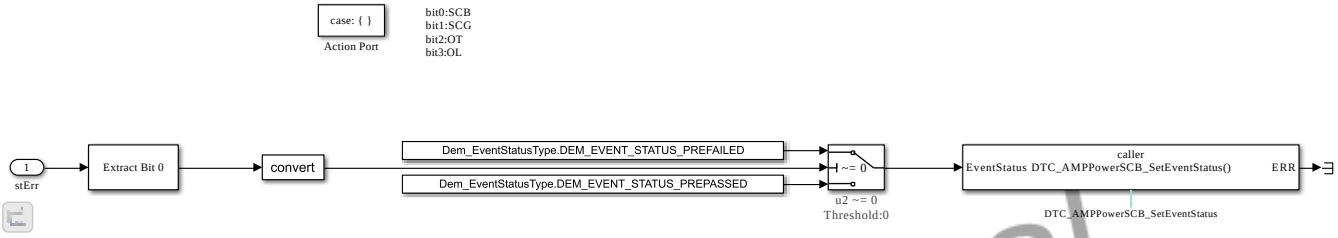

Figure 6 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOnDiagErr_DebounceReset]

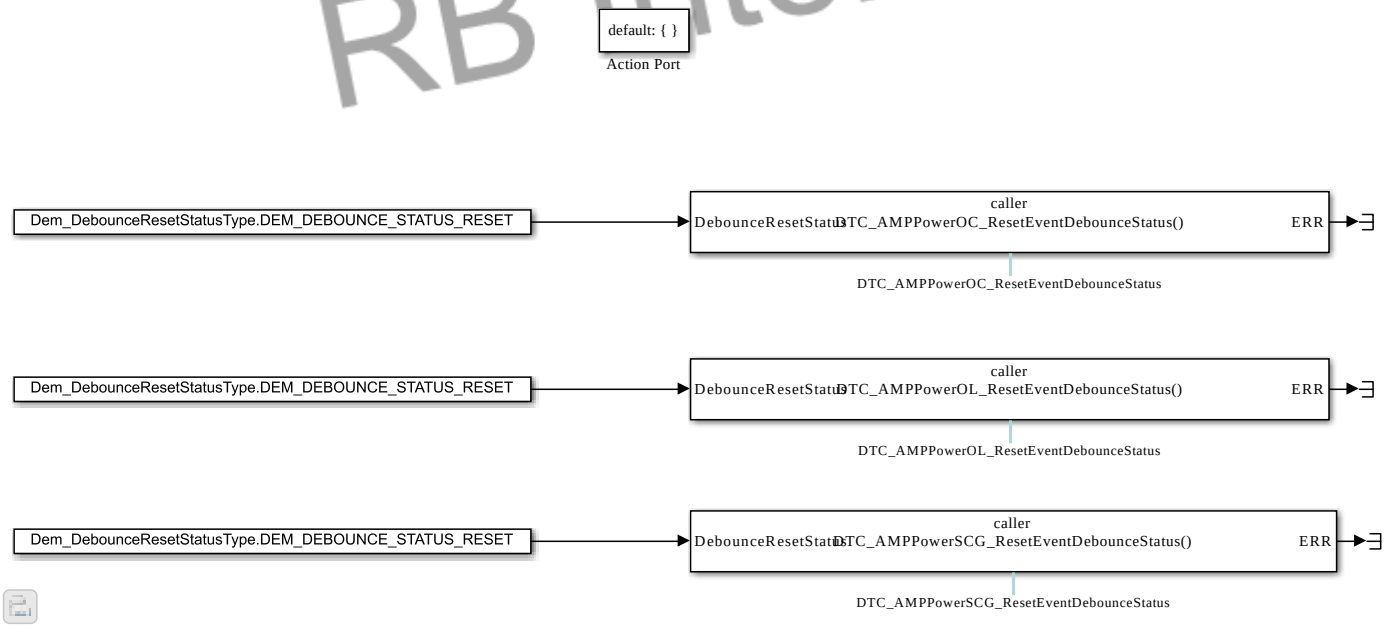

Figure 7 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_AMPOnDiagErr_Set]

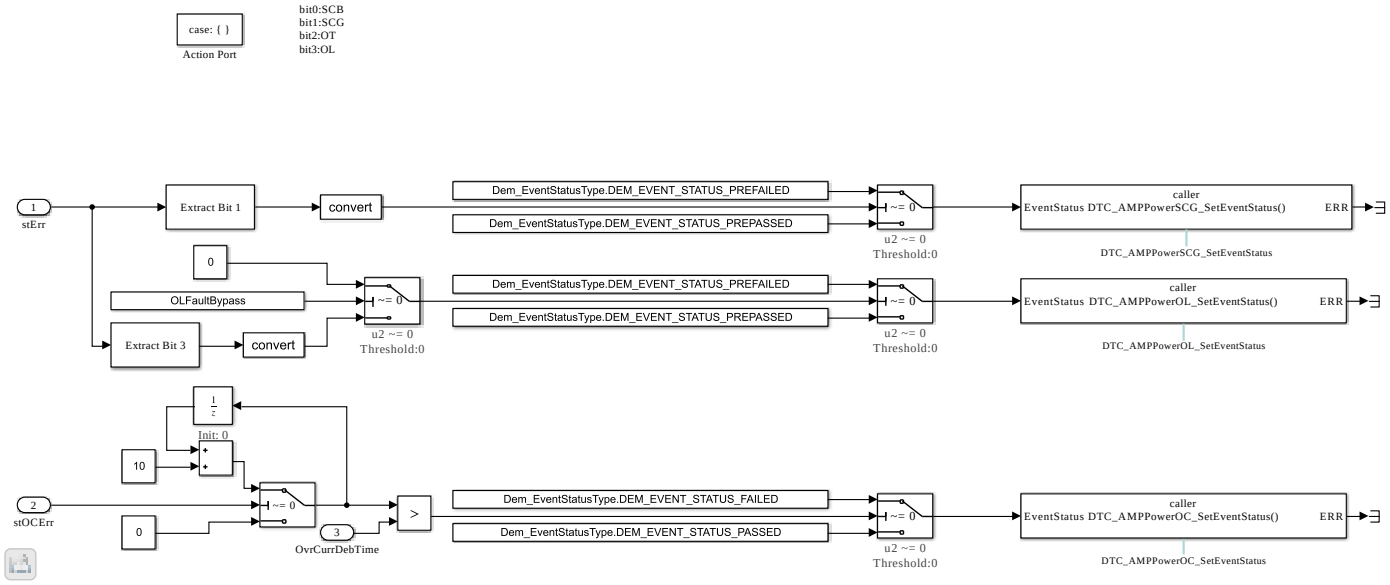

Figure 8 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_RealErrHold]

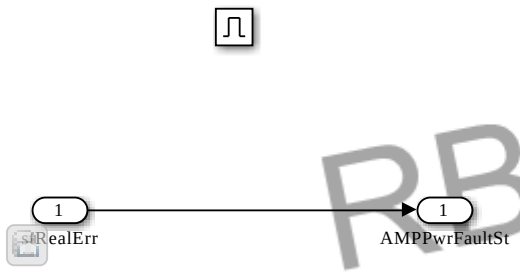

Figure 9 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_AMP_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
1=Circuit short to ground
2=Circuit short to battery
3=Open
4=Overpower
5~7=Reserve

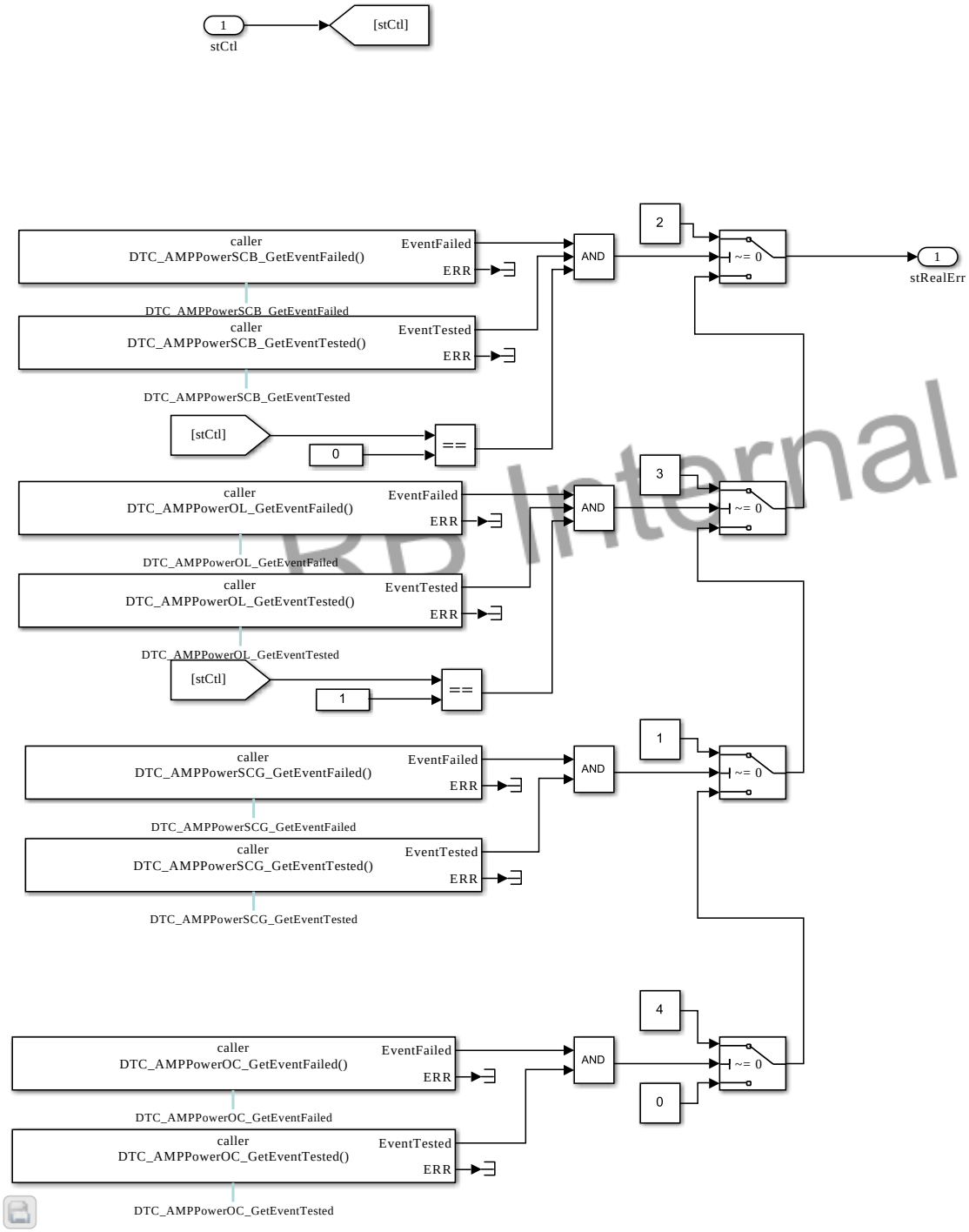

Figure 10 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff]

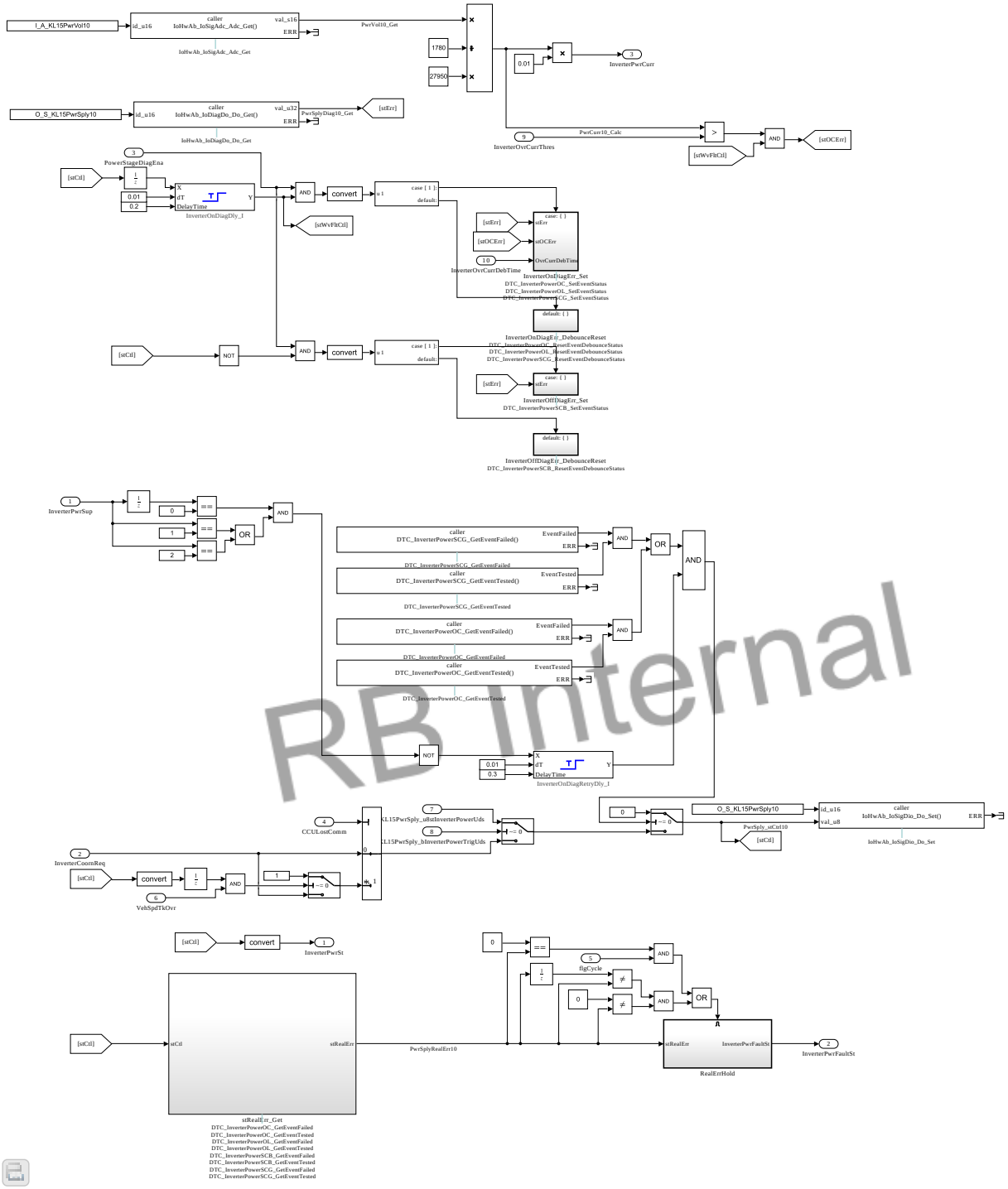

Figure 11 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOffDiagErr_DebounceReset]

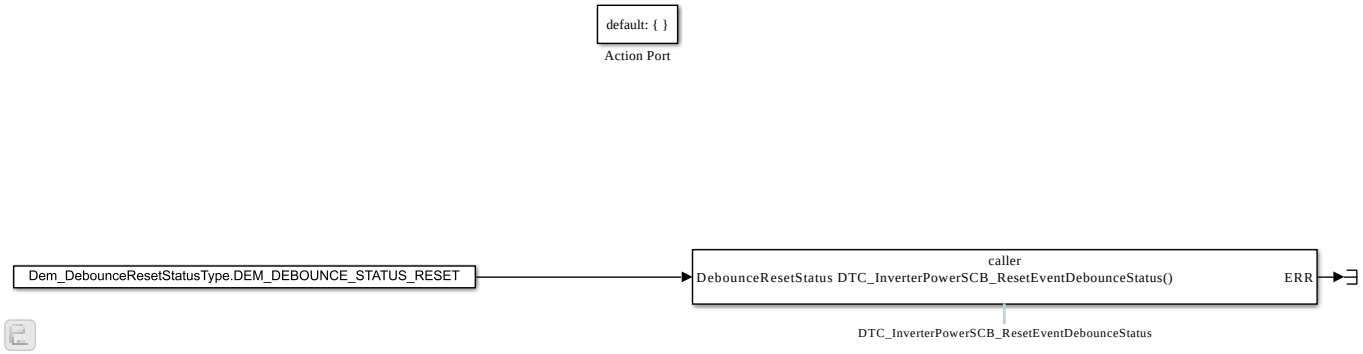

Figure 12 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOffDiagErr_Set]

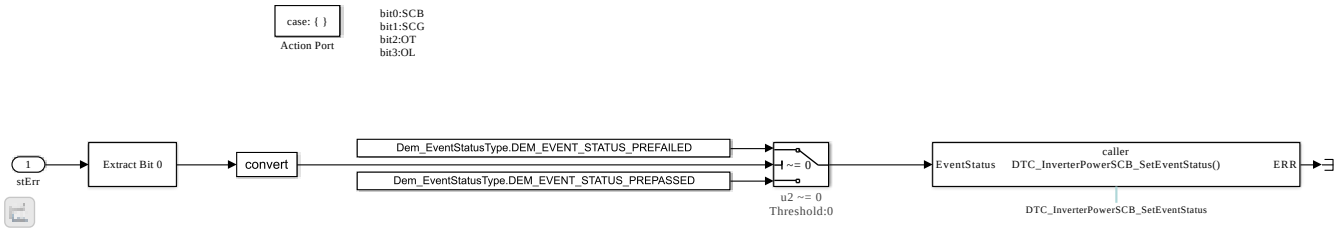

Figure 13 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOnDiagErr_DebounceReset]

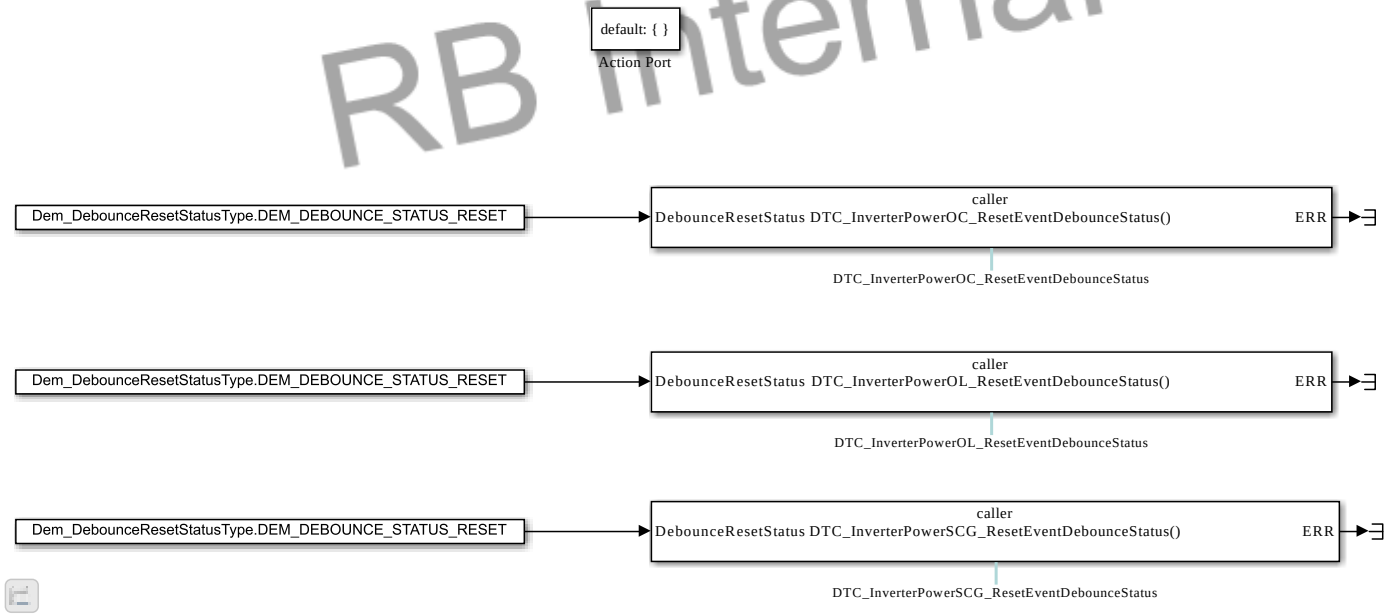

Figure 14 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_InverterOnDiagErr_Set]

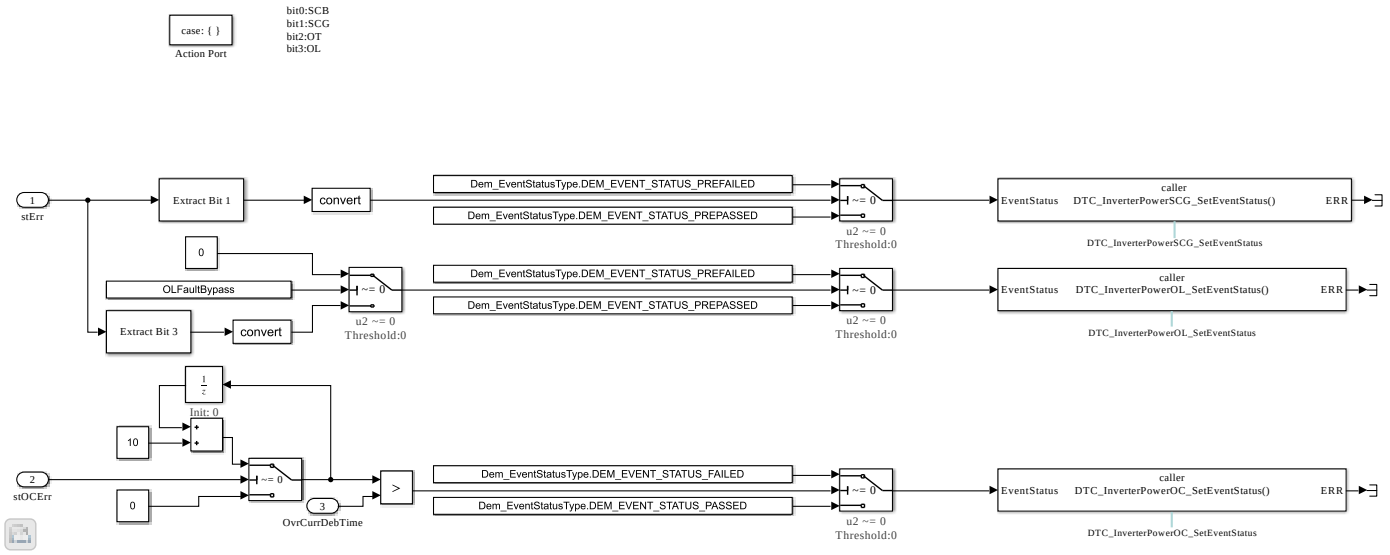

Figure 15 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_RealErrHold]

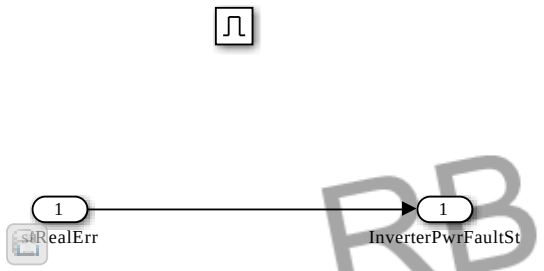

Figure 16 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Inverter_Ctl_2F_Diag_CutOff_stRealErr_Get]

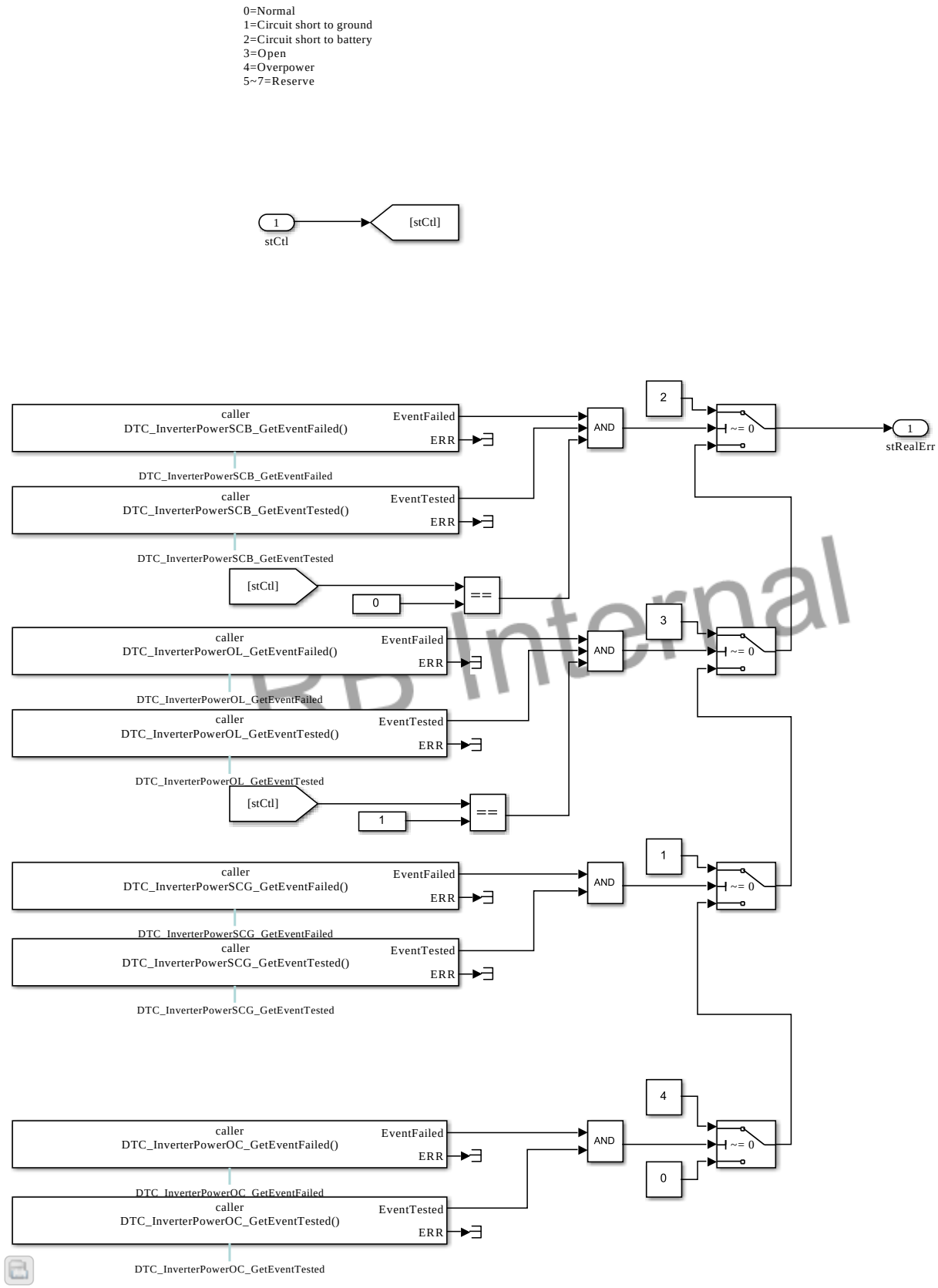

Figure 17 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff]

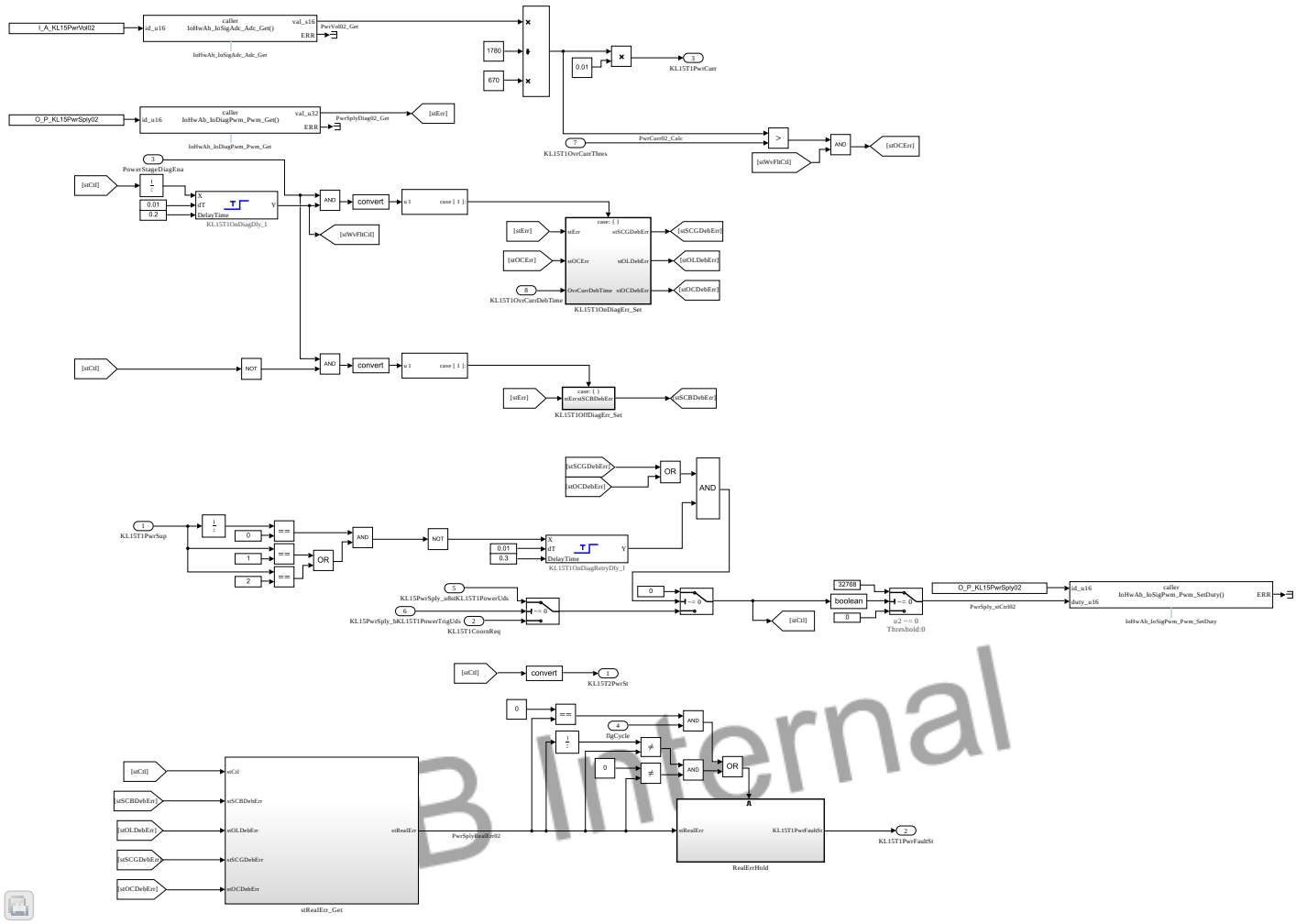

Figure 18 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_KL15T1OffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_KL15T1OffDiagErr_Set]

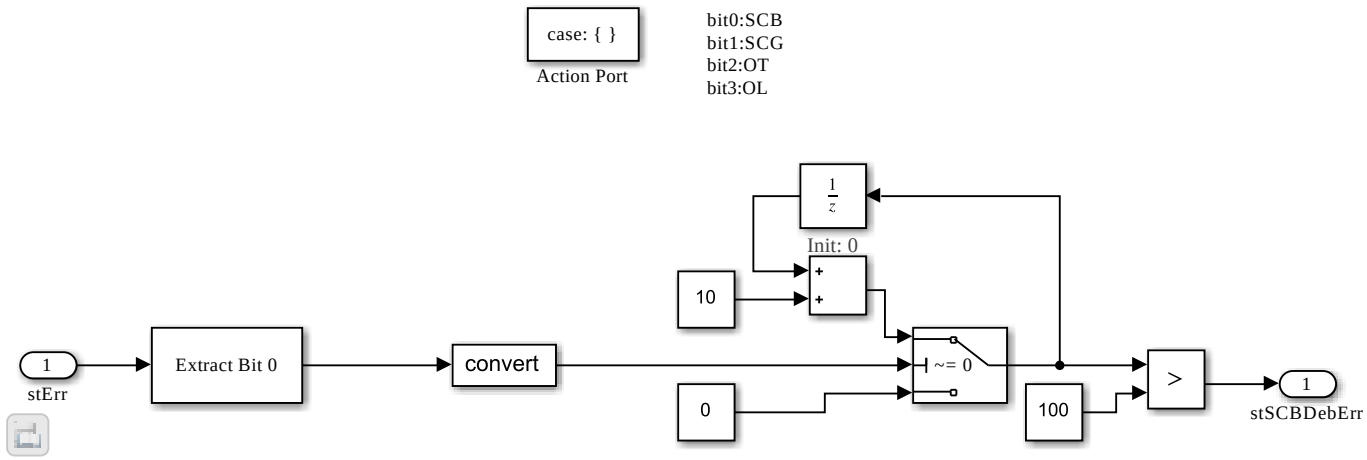

Figure 19 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_KL15T1OnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_KL15T1OnDiagErr_Set]

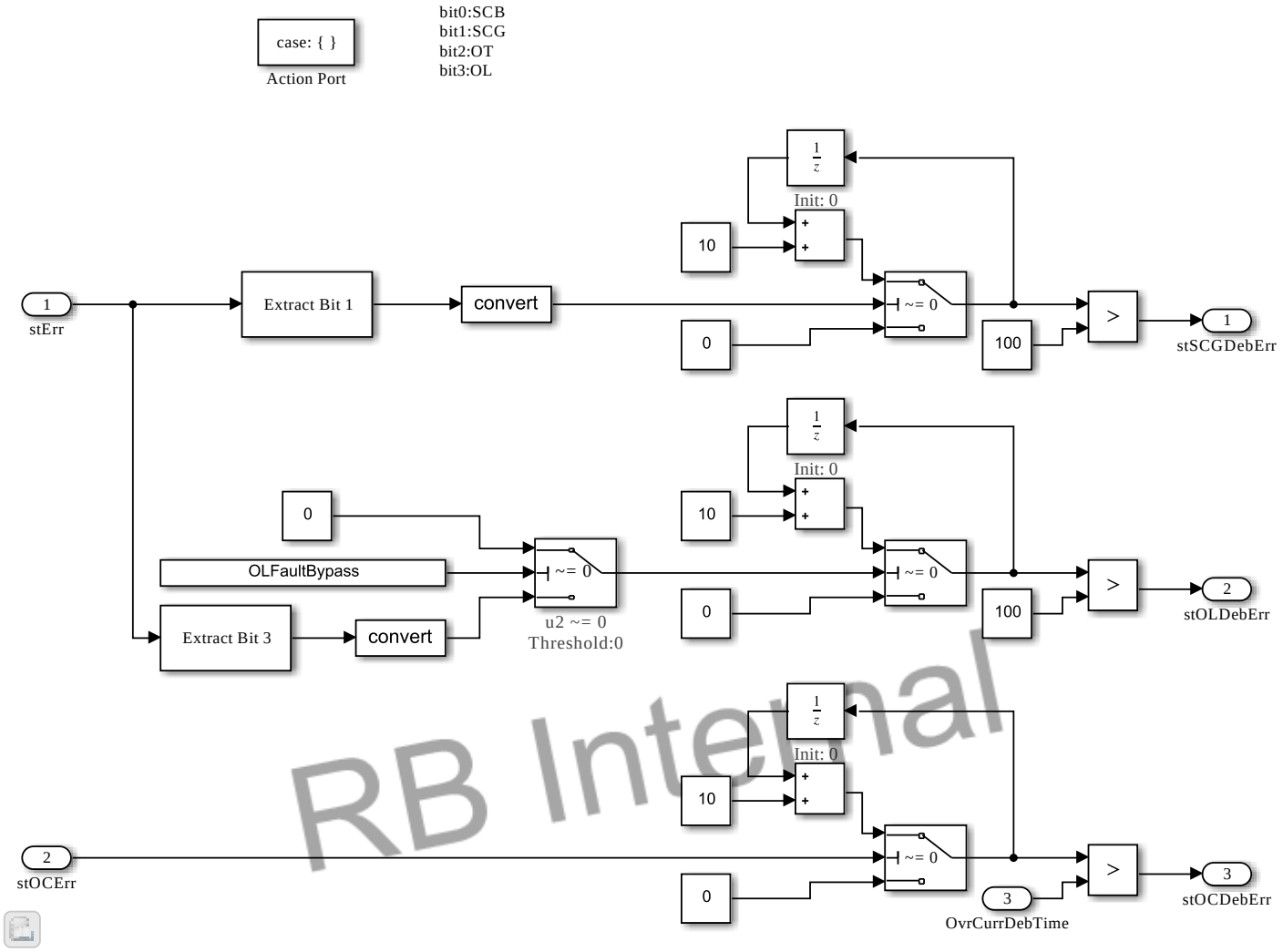

Figure 20 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_RealErrHold]

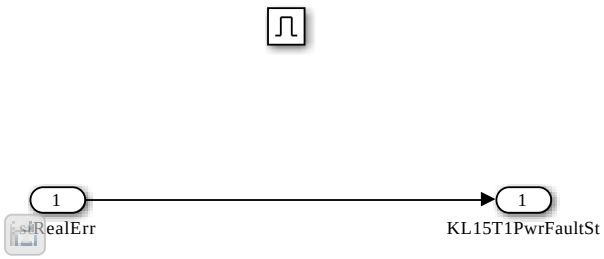

Figure 21 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T1_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
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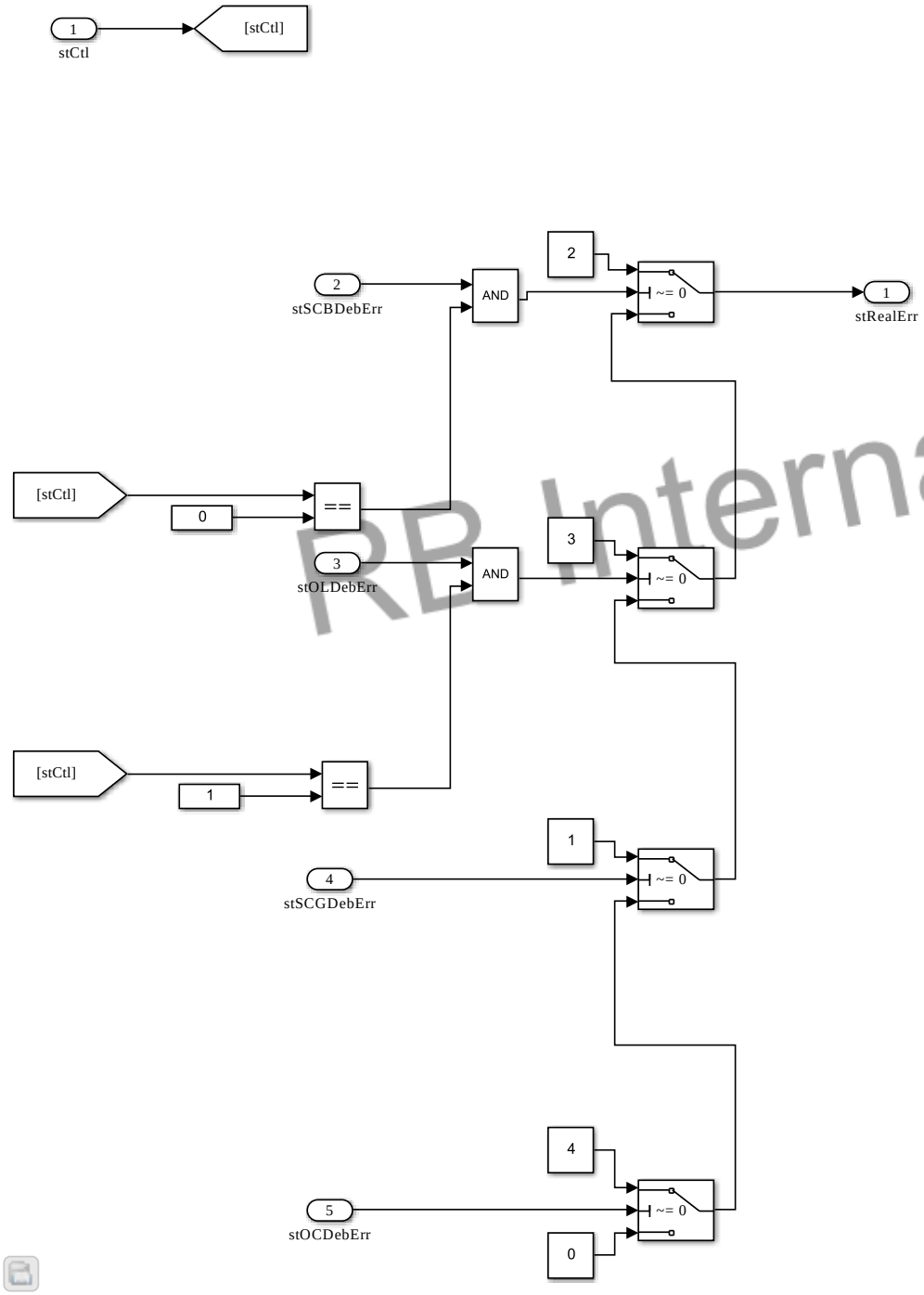

Figure 22 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff]

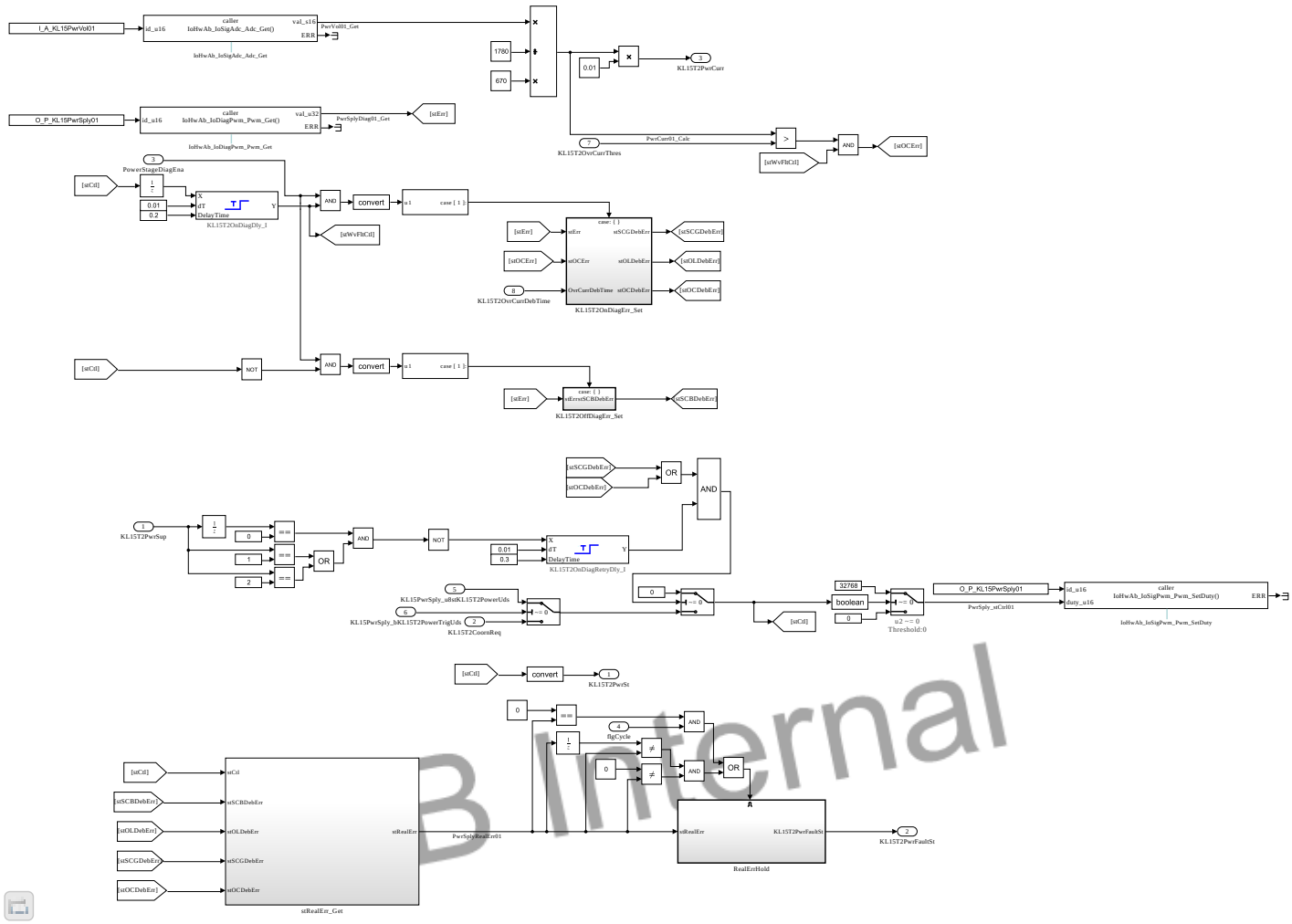

Figure 23 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_KL15T2OffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_KL15T2OffDiagErr_Set]

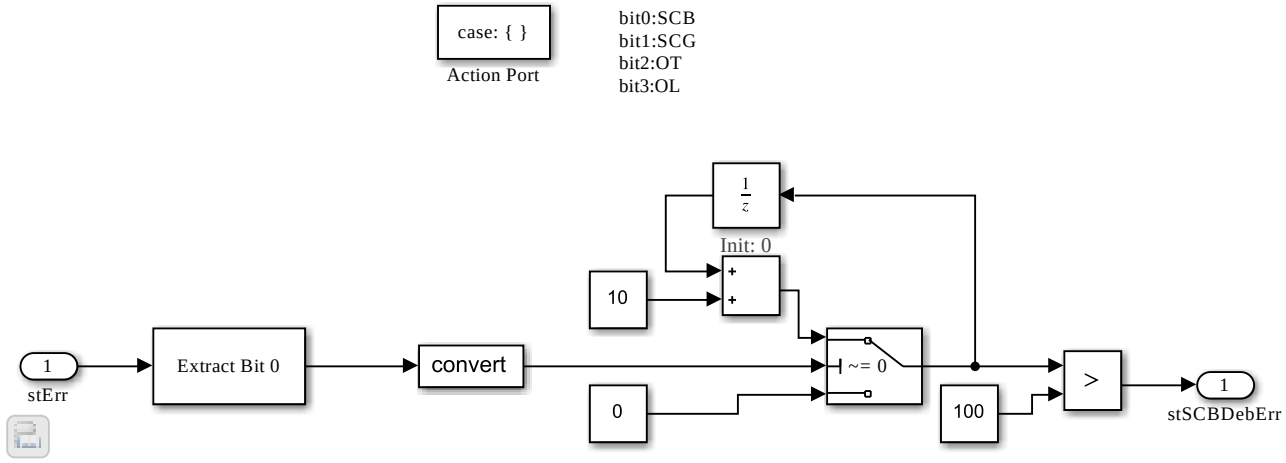

Figure 24 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_KL15T2OnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_KL15T2OnDiagErr_Set]

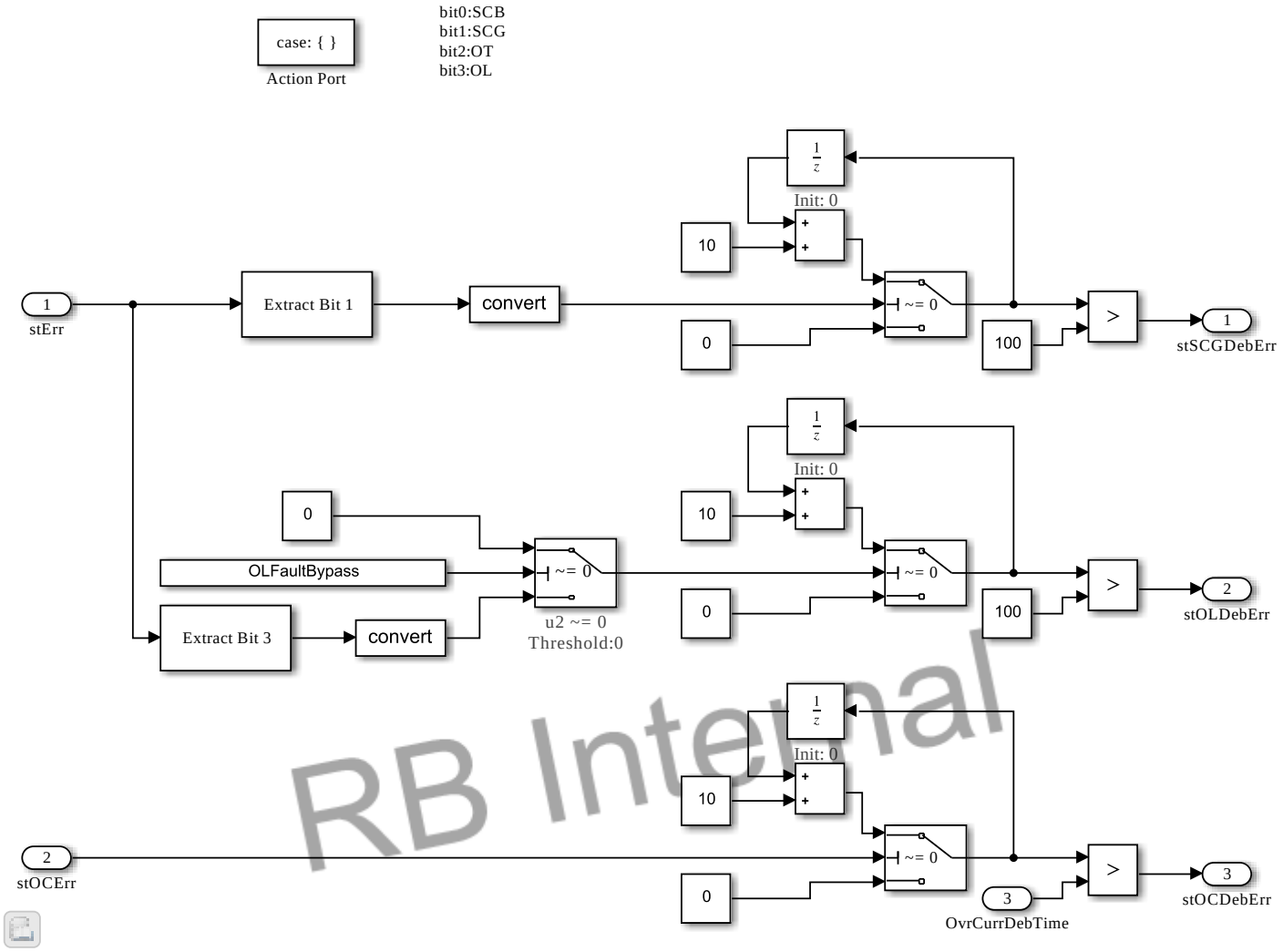

Figure 25 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_RealErrHold]

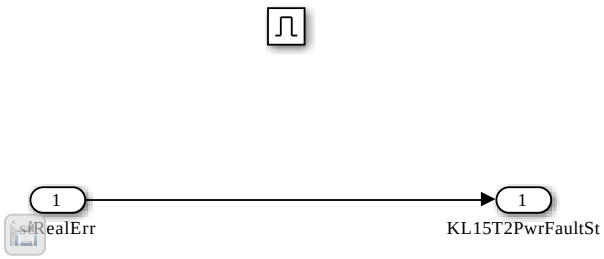

Figure 26 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_KL15T2_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
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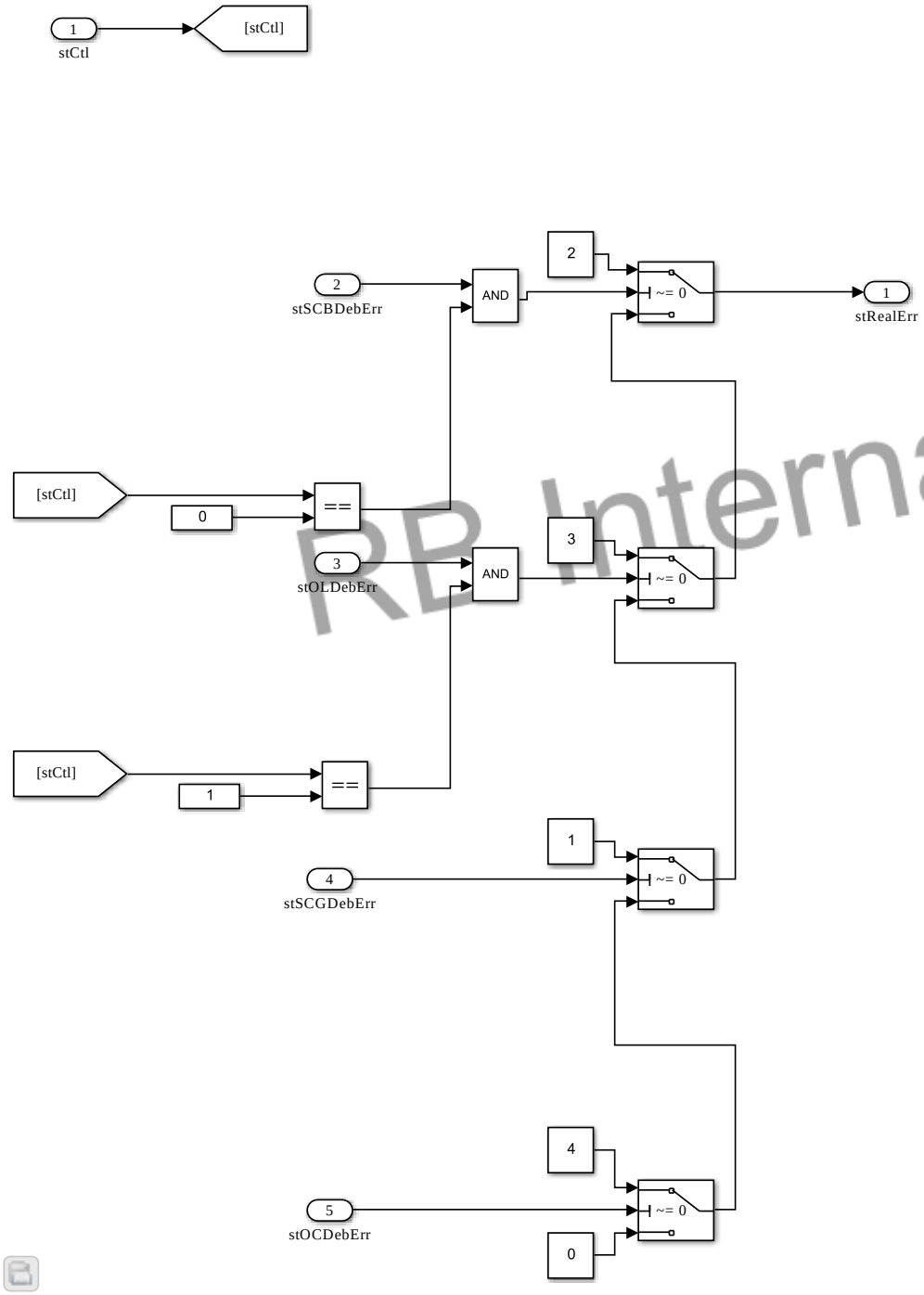

Figure 27 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff]

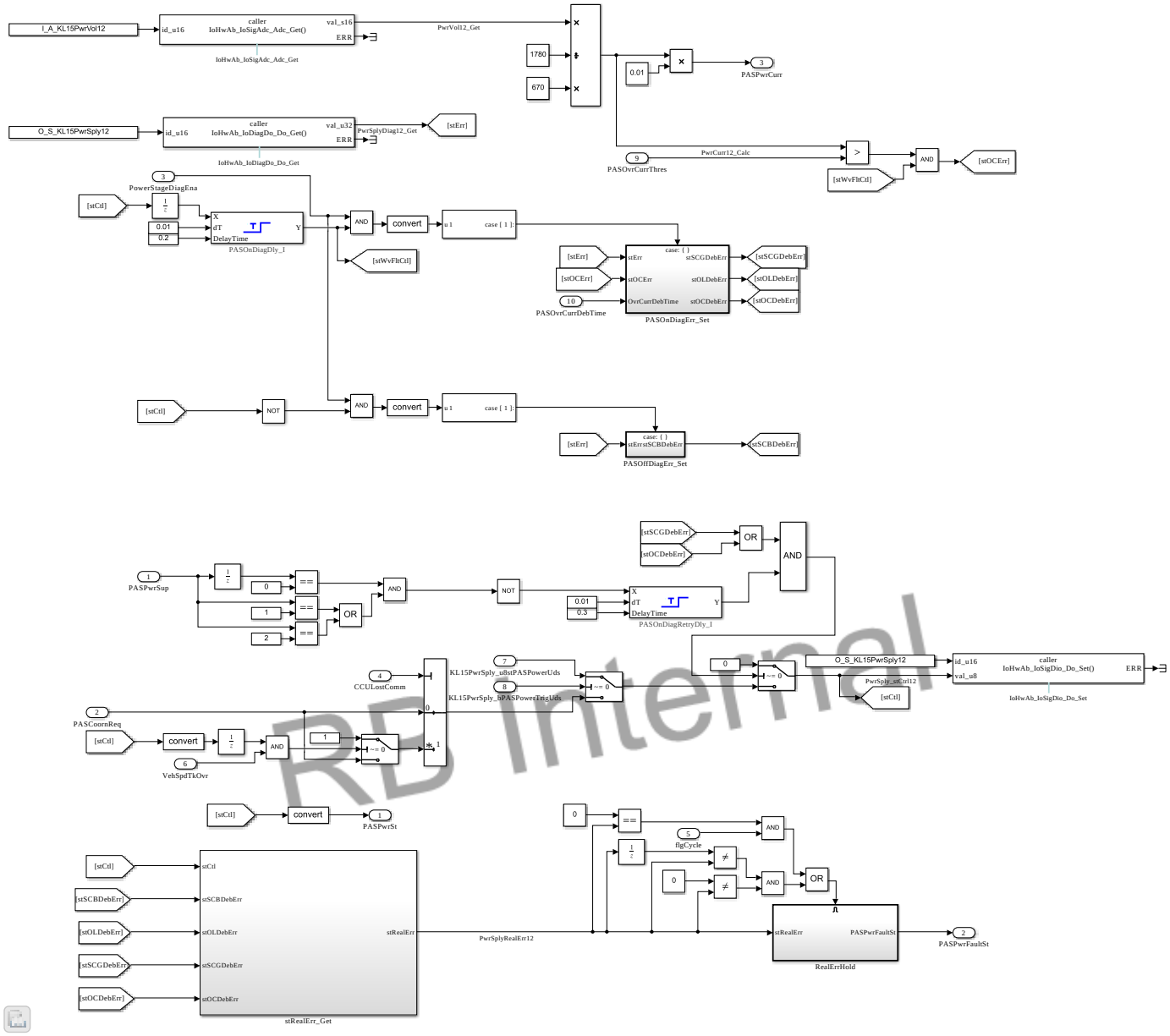

Figure 28 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_PASOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_PASOffDiagErr_Set]

bit0:SCB
bit1:SCG
bit2:OT
bit3:OL

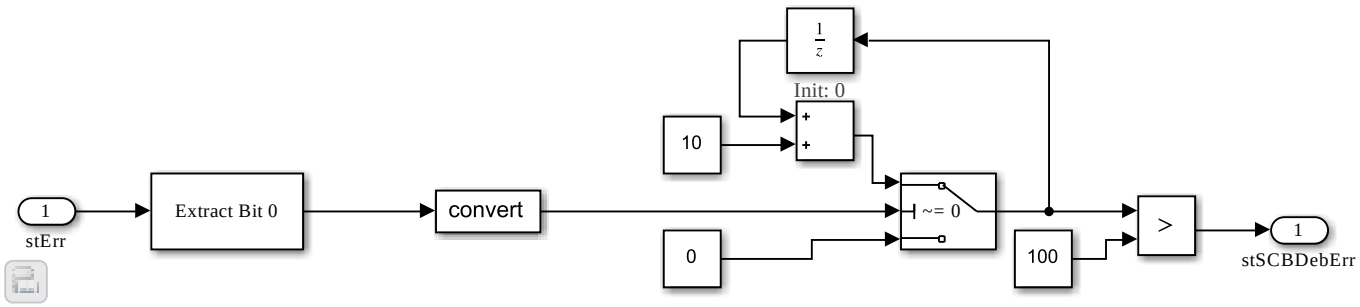

Figure 29 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_PASOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_PASOnDiagErr_Set]

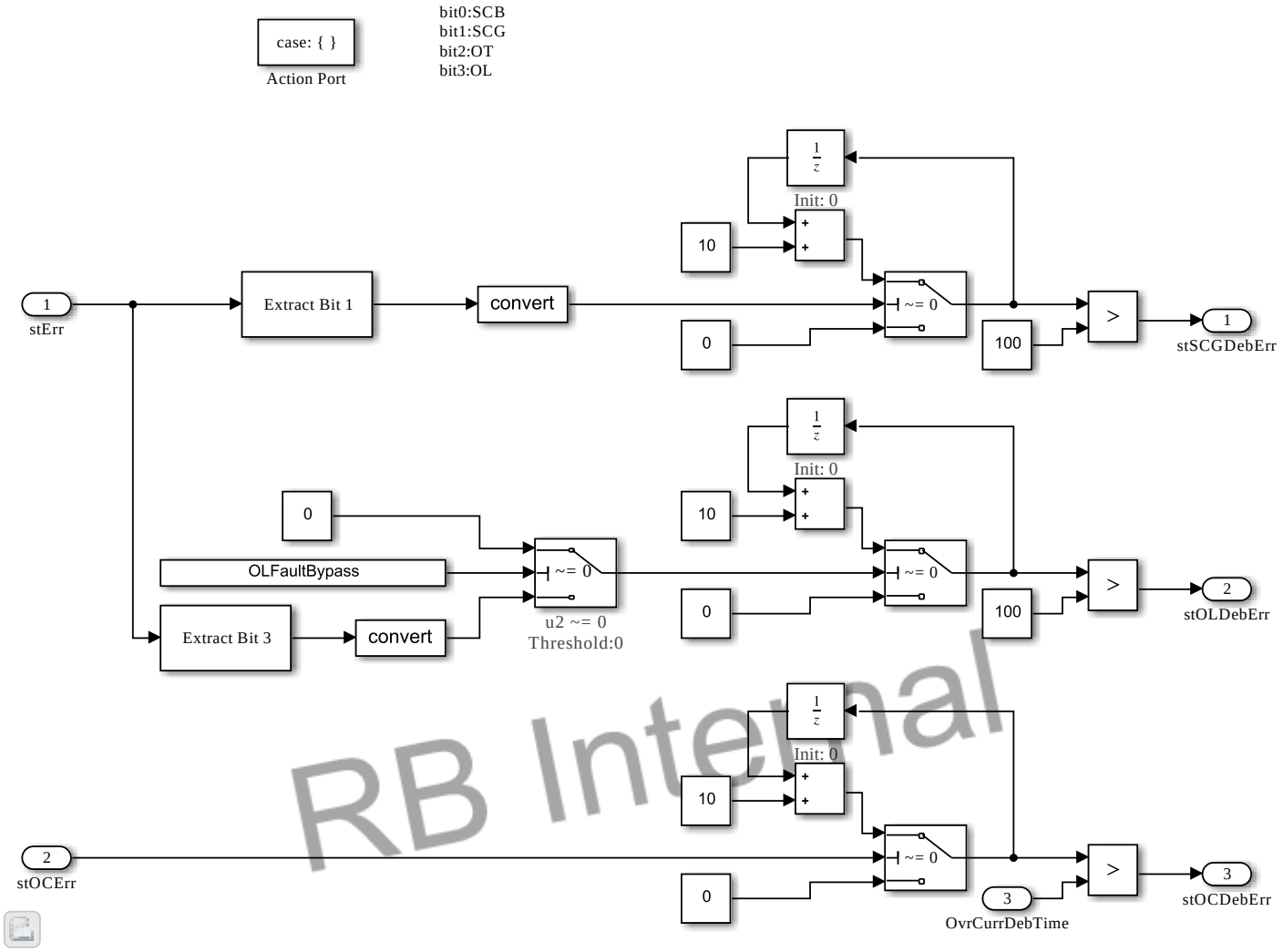

Figure 30 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_RealErrHold]

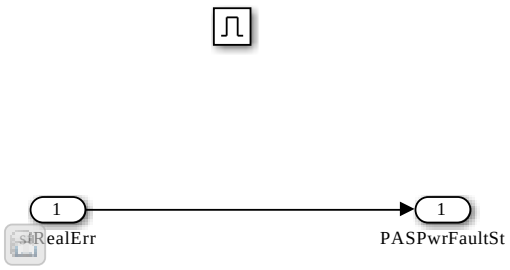

Figure 31 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PAS_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
1=Circuit short to ground
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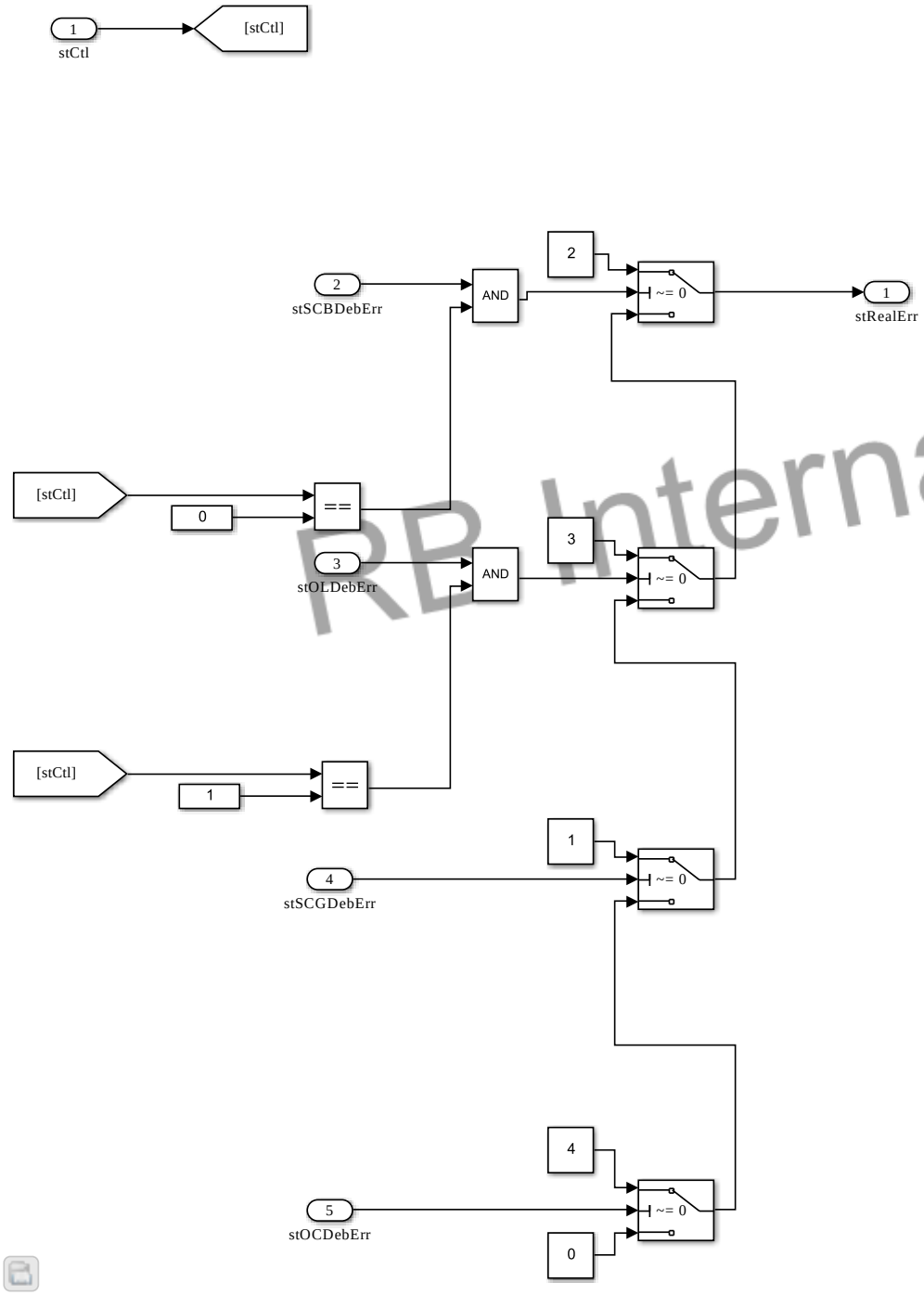

Figure 32 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff]

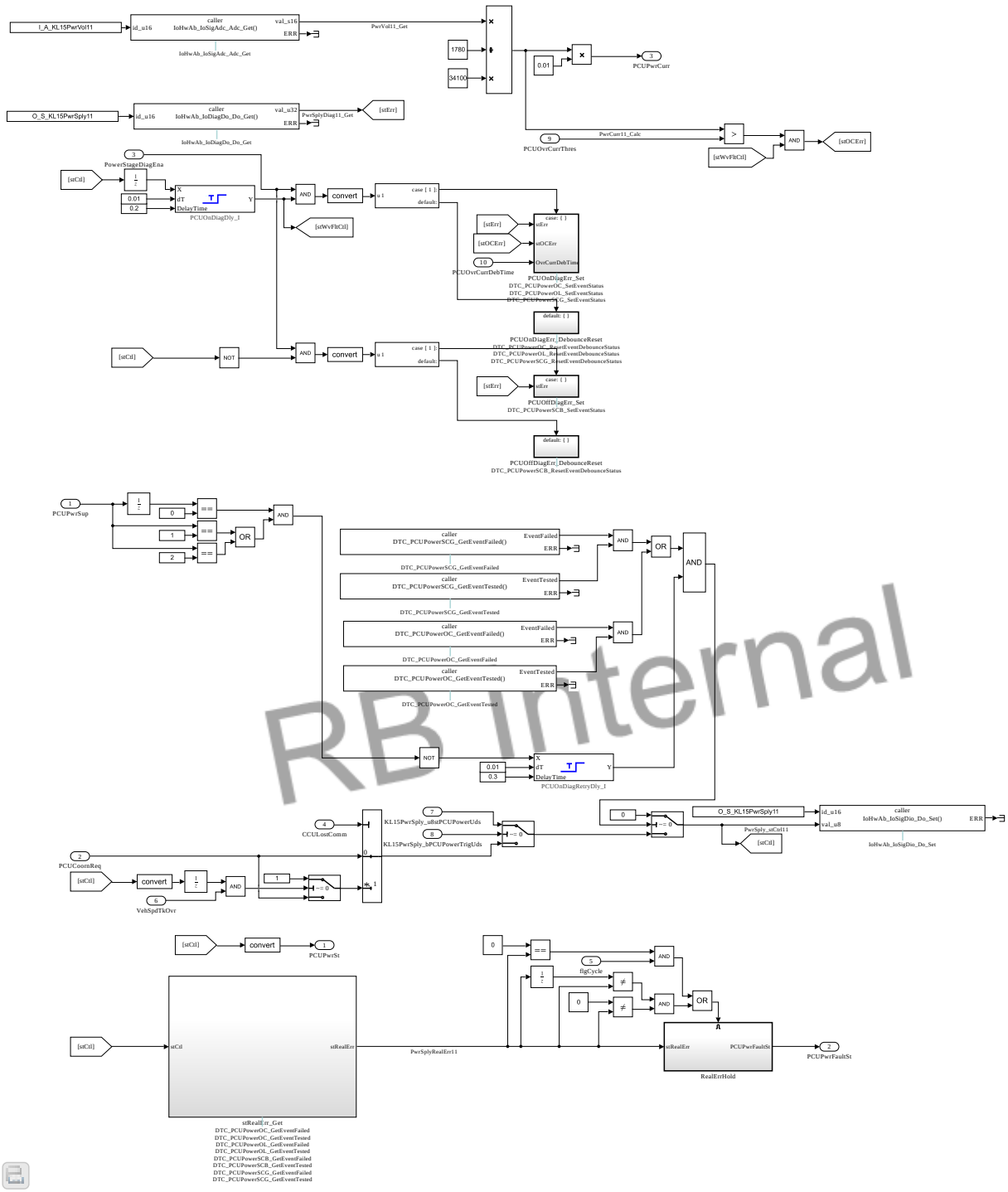

Figure 33 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOffDiagErr_DebounceReset]

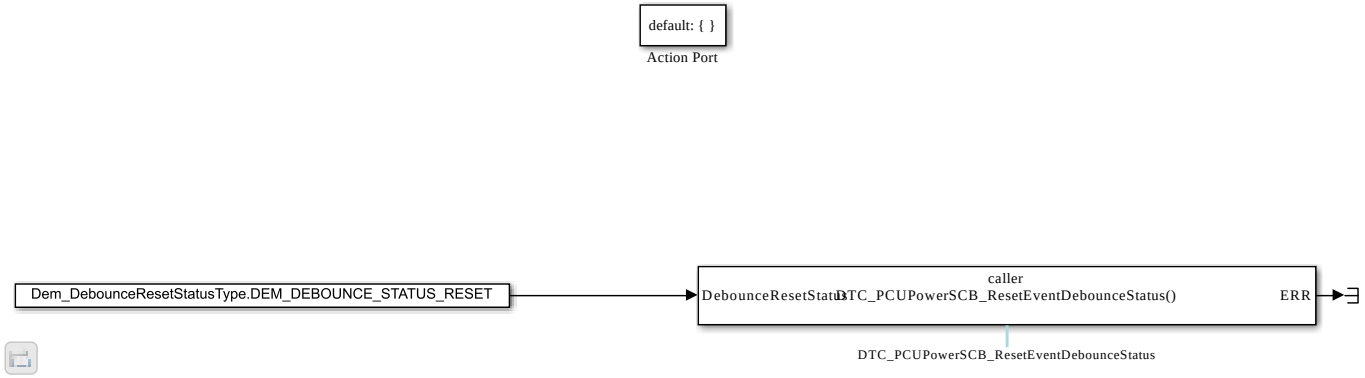

Figure 34 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOffDiagErr_Set]

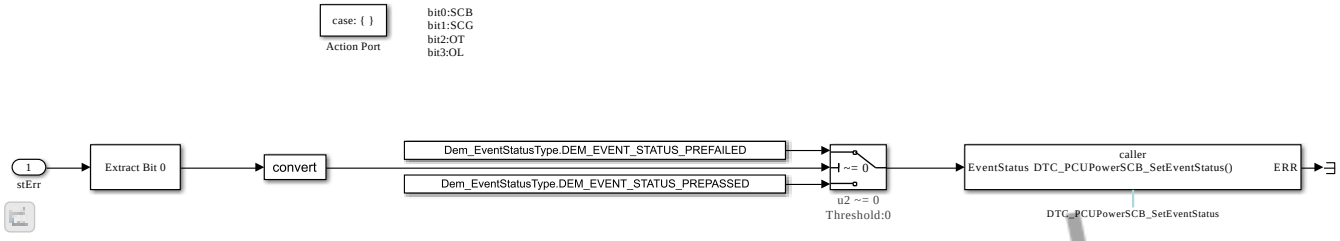

Figure 35 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOnDiagErr_DebounceReset]

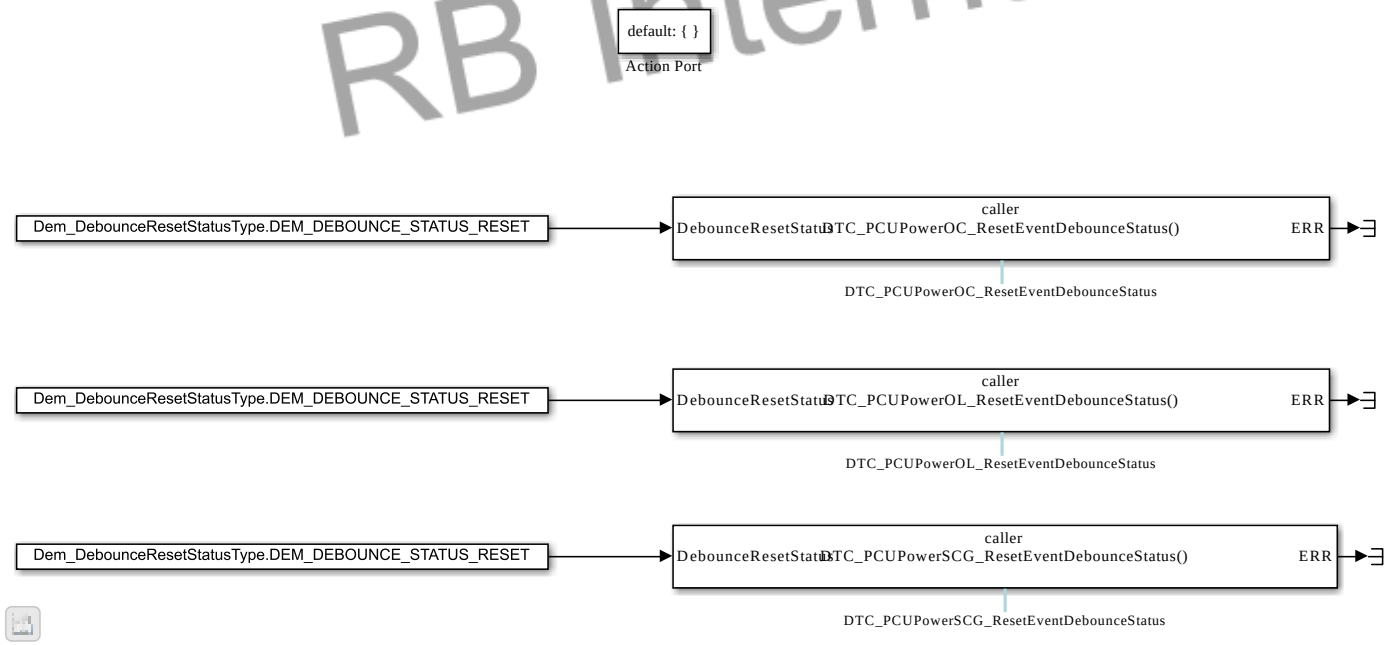

Figure 36 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_PCUOnDiagErr_Set]

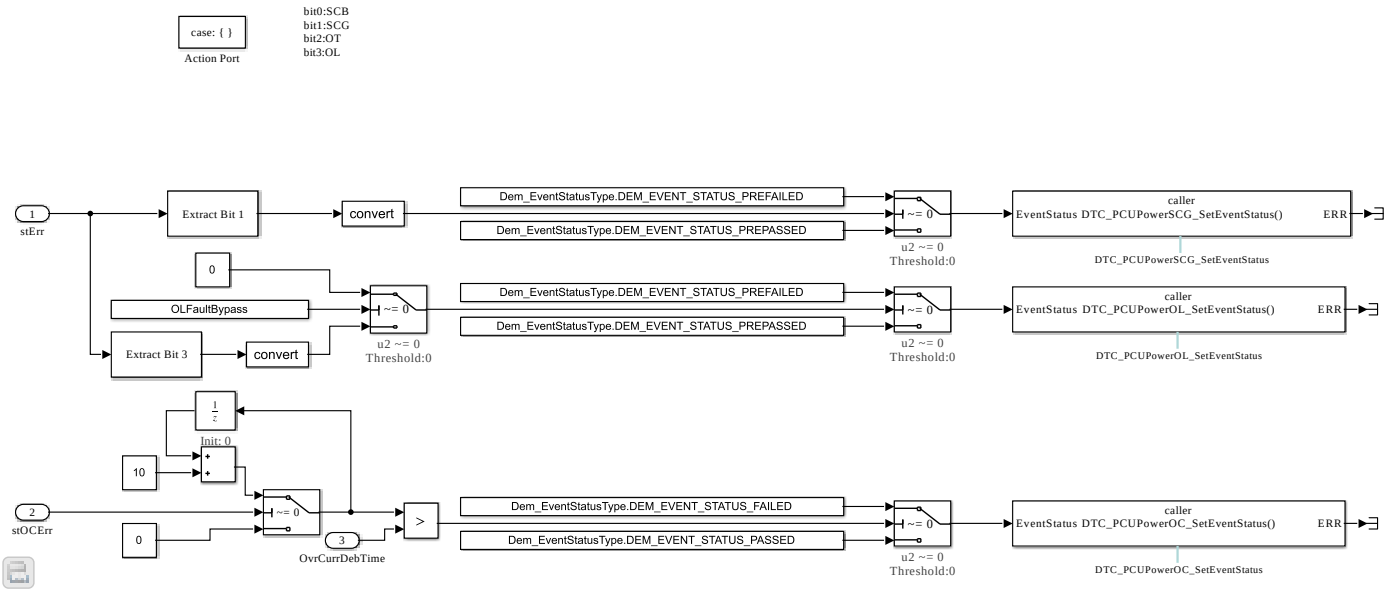

Figure 37 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_RealErrHold]

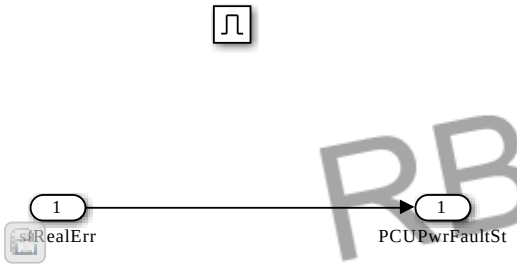

Figure 38 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_PCU_Ctl_2F_Diag_CutOff_stRealErr_Get]

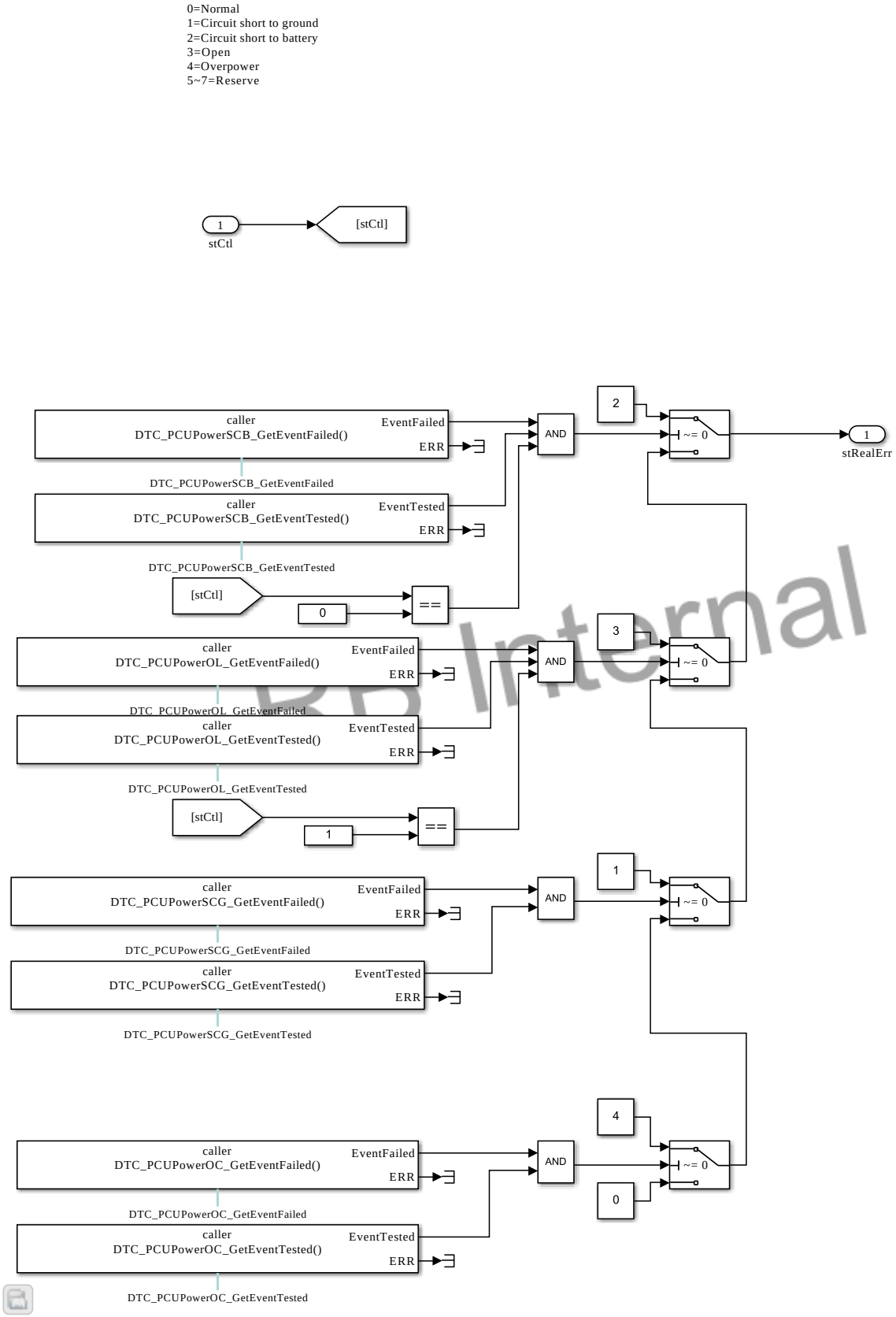

Figure 39 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampAllwSleepDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampAllwSleepDly]

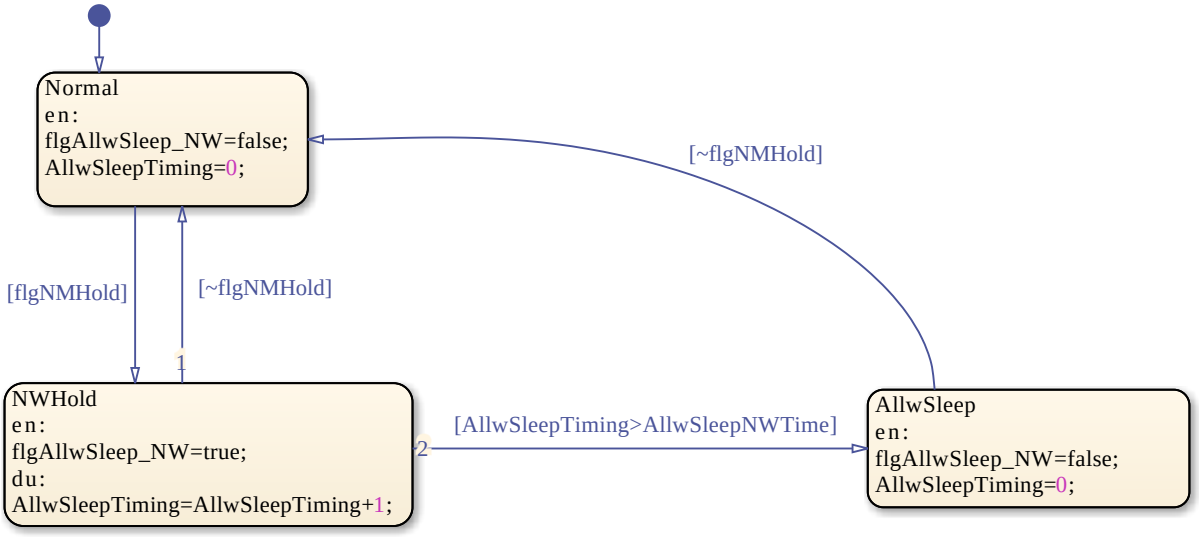

Figure 40 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff]

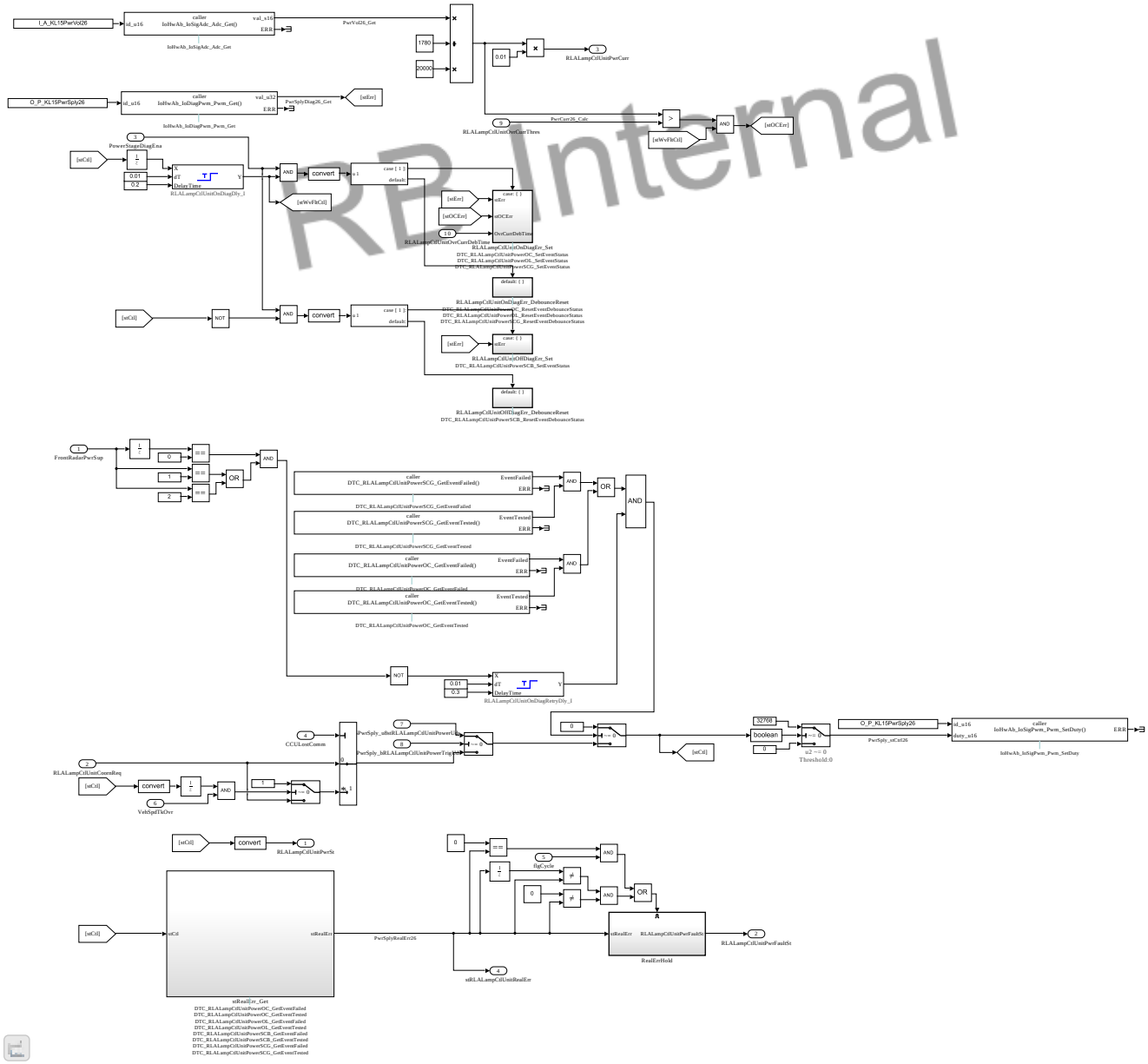

Figure 41 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOffDiagErr_DebounceReset]

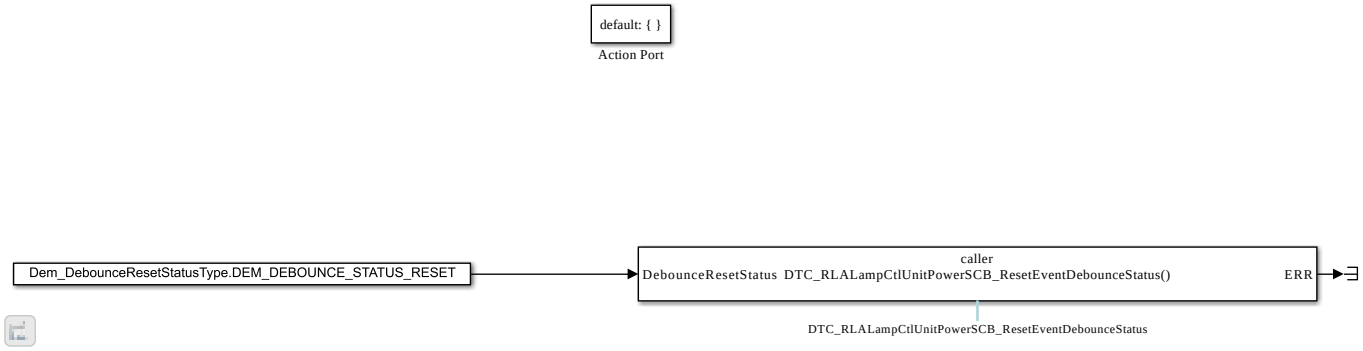

Figure 42 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOffDiagErr_Set]

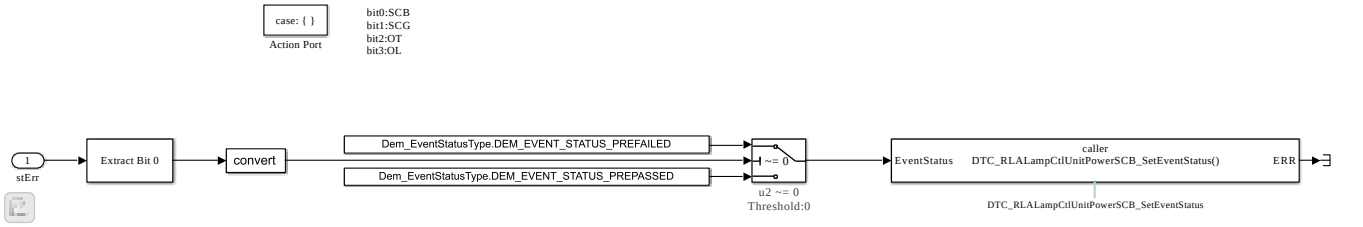

Figure 43 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOnDiagErr_DebounceReset]

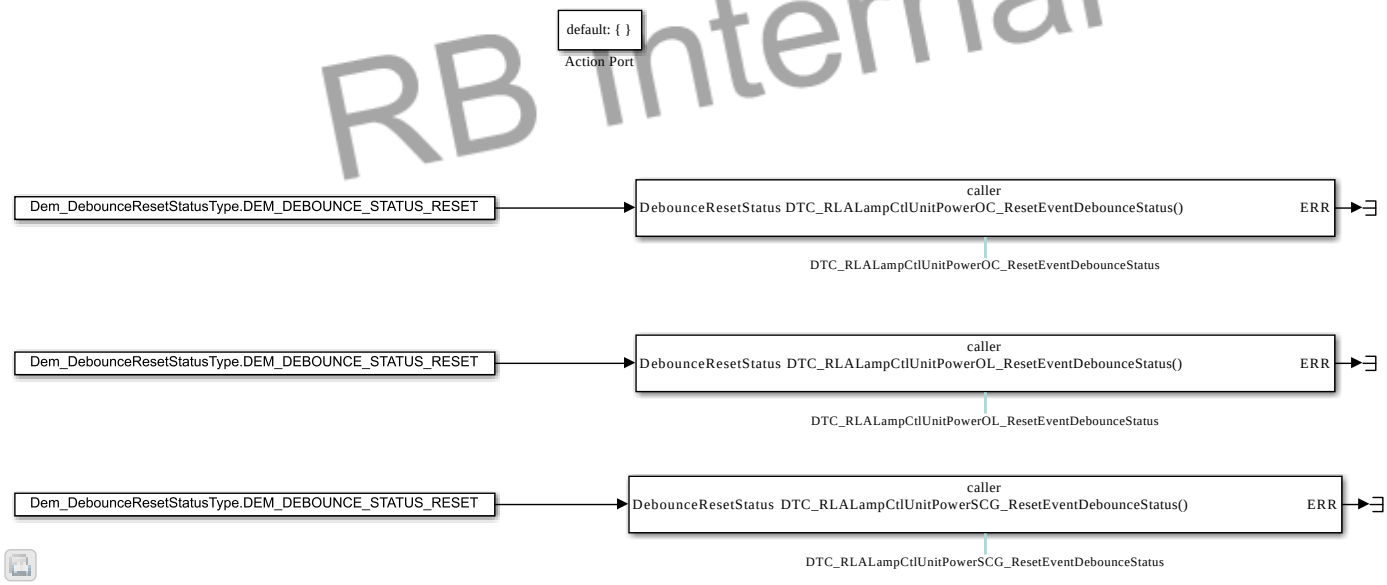

Figure 44 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RLALampCtlUnitOnDiagErr_Set]

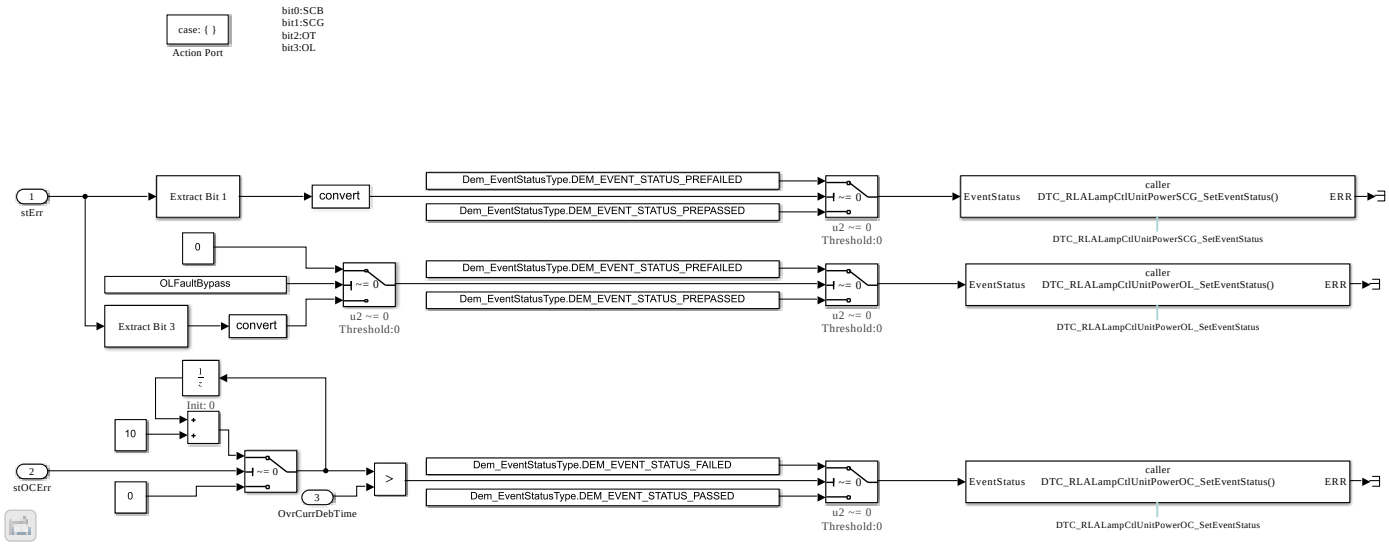

Figure 45 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold]

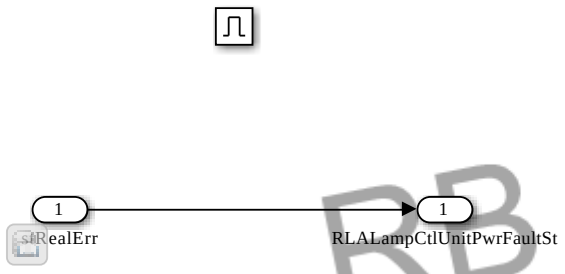

Figure 46 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get]

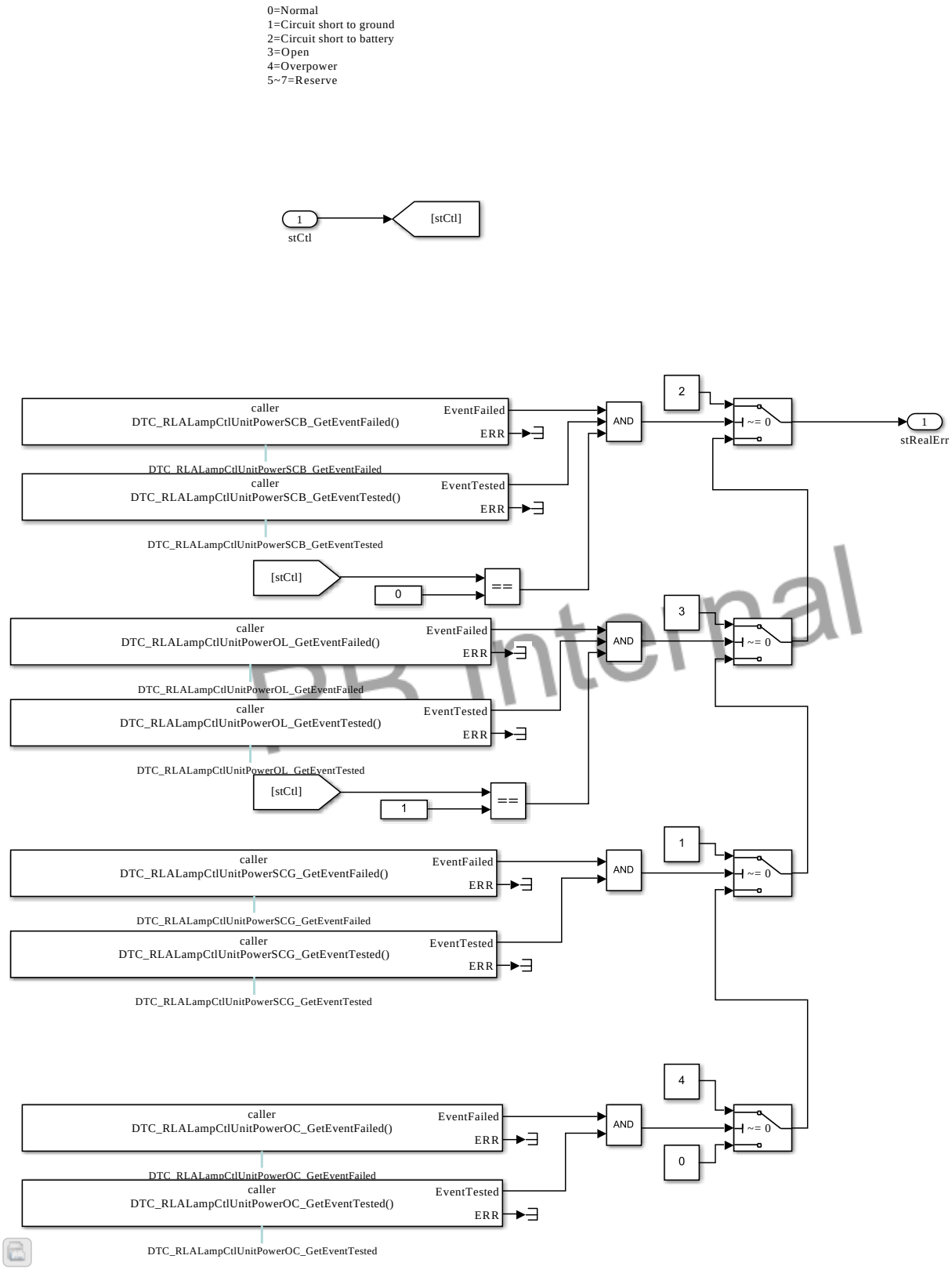

Figure 47 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLALampWkUpDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLA-LampWkUpDly]

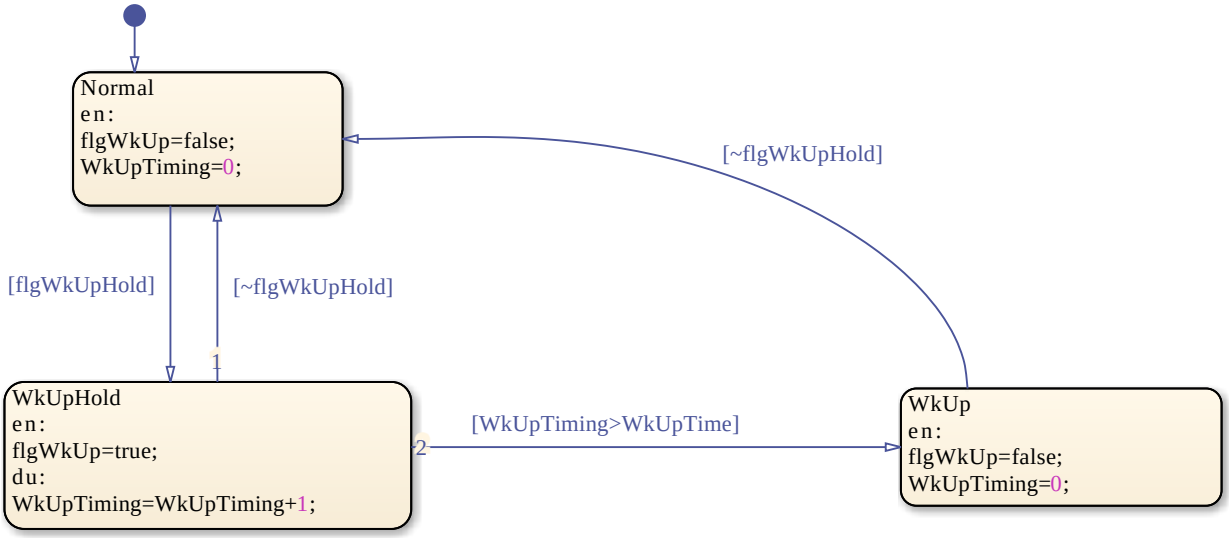

Figure 48 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampAllwSleepDly [KL15PwrSply_DD_KL15PwrSply_DD_Runna-ble_10ms_sys_RLBLampAllwSleepDly]

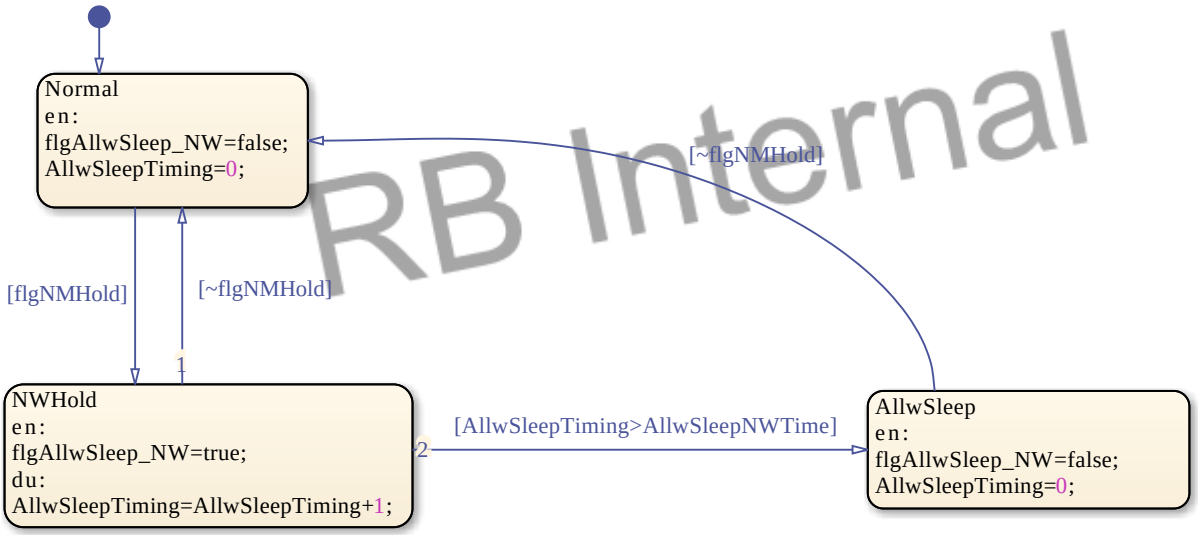

Figure 49 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff]

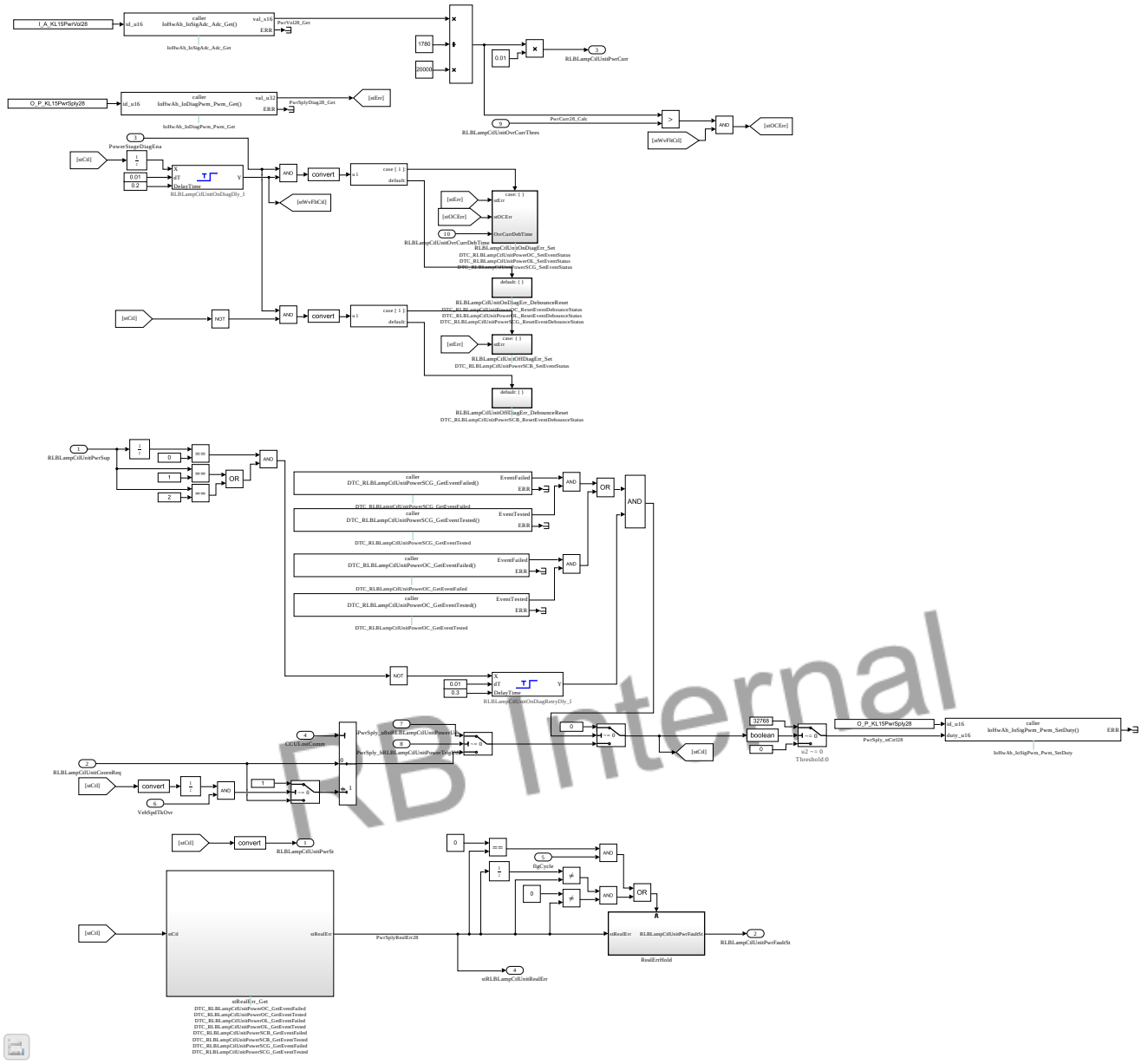

Figure 50 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOffDiagErr_DebounceReset]

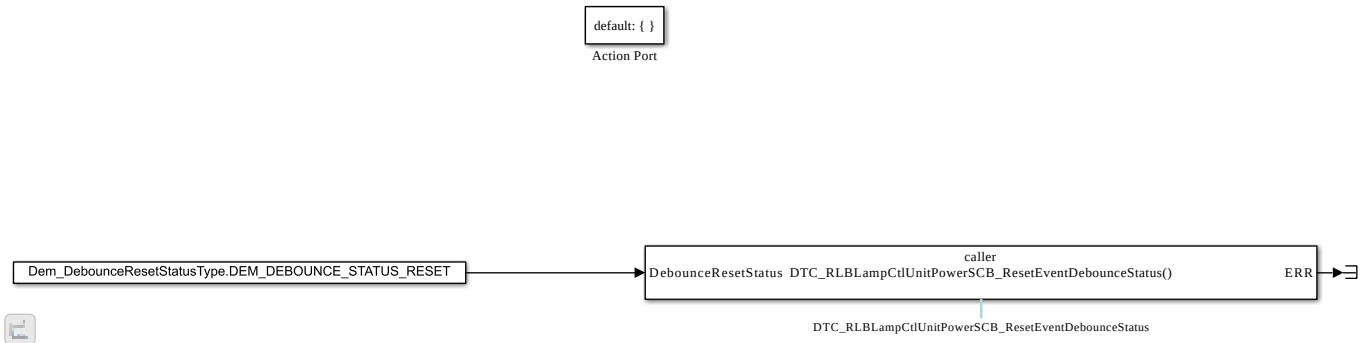

Figure 51 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOffDiagErr_Set]

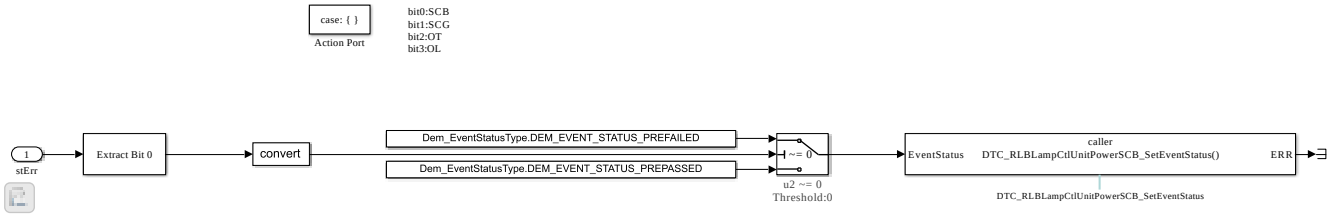

Figure 52 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOnDiagErr_DebounceReset]

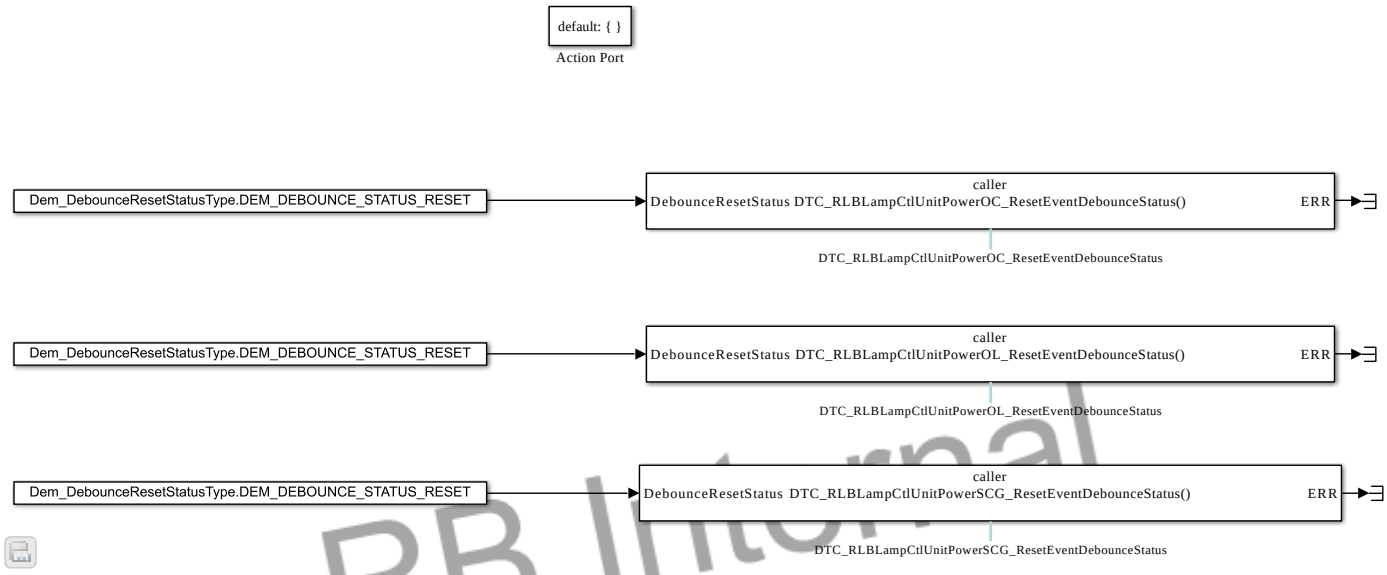

Figure 53 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RLBLampCtlUnitOnDiagErr_Set]

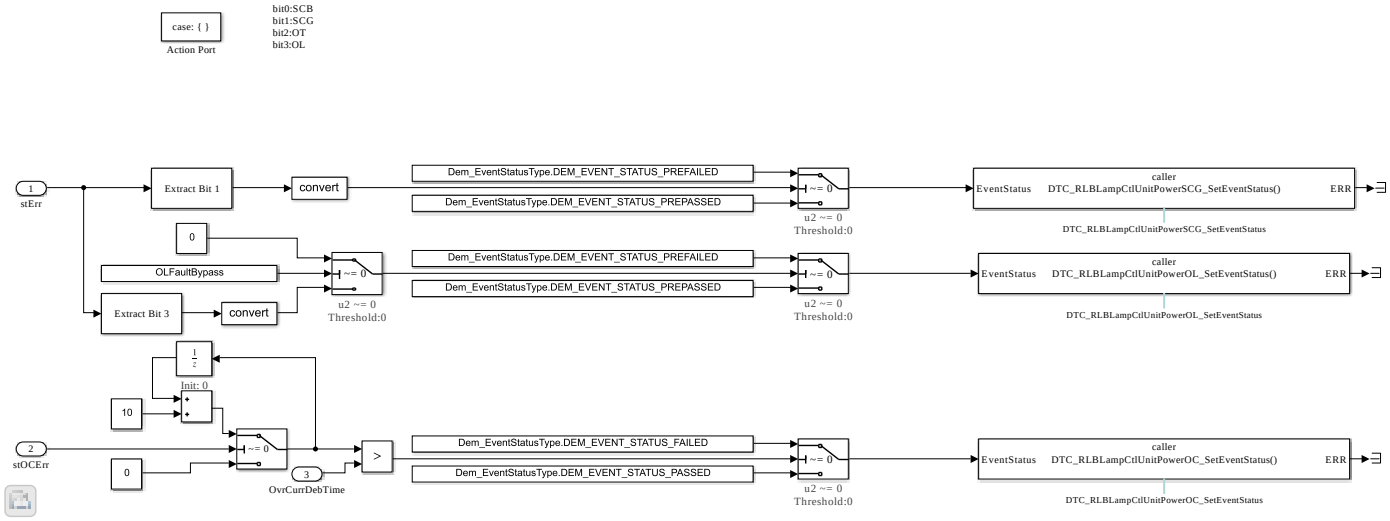

Figure 54 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold]

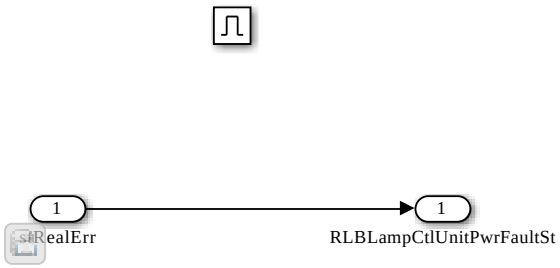

Figure 55 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get]

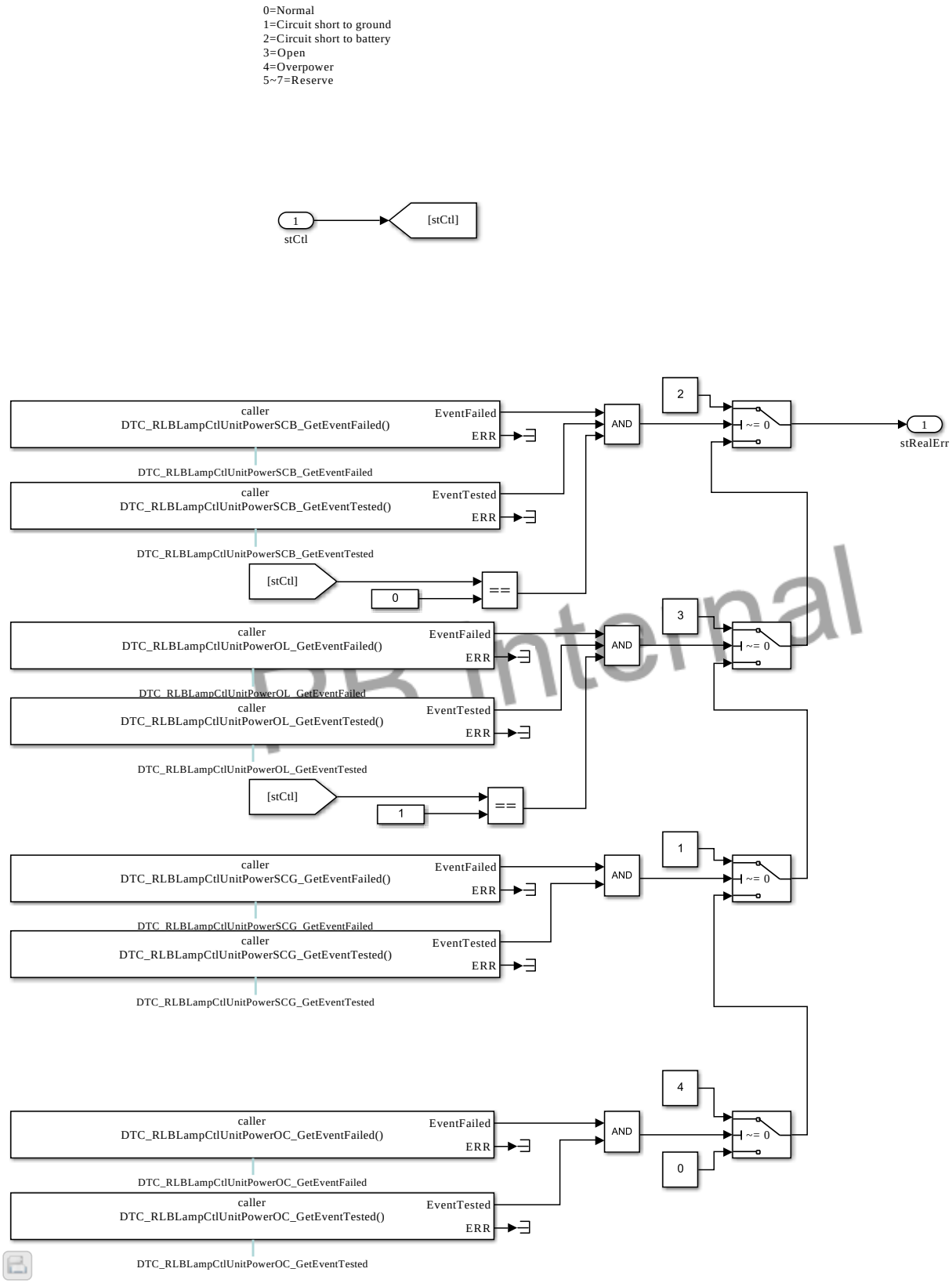

Figure 56 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RLBLampWkUpDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RL-BLampWkUpDly]

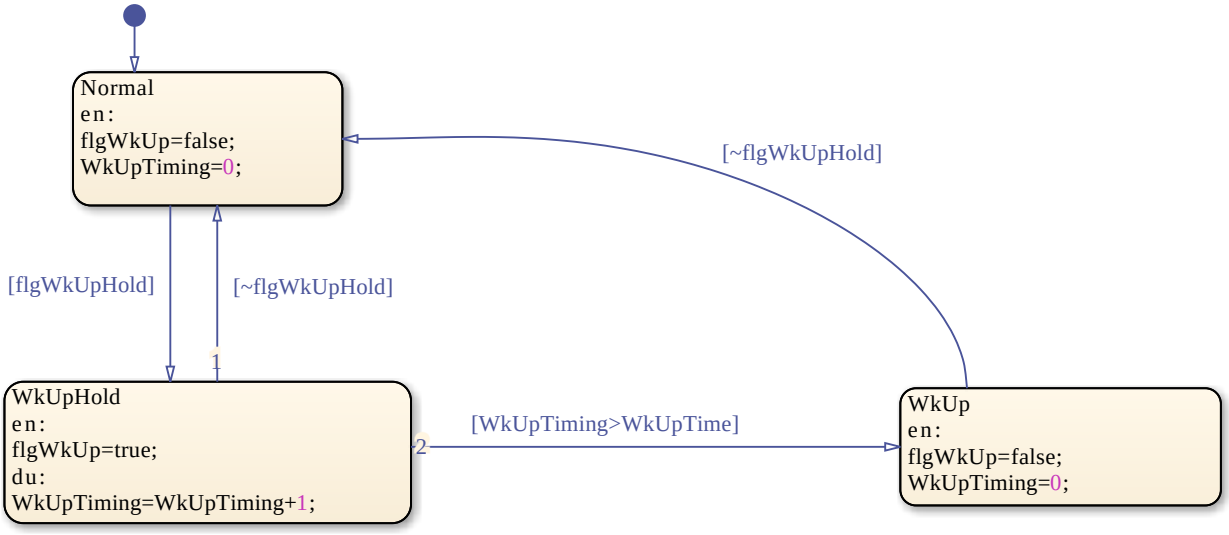

Figure 57 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff]

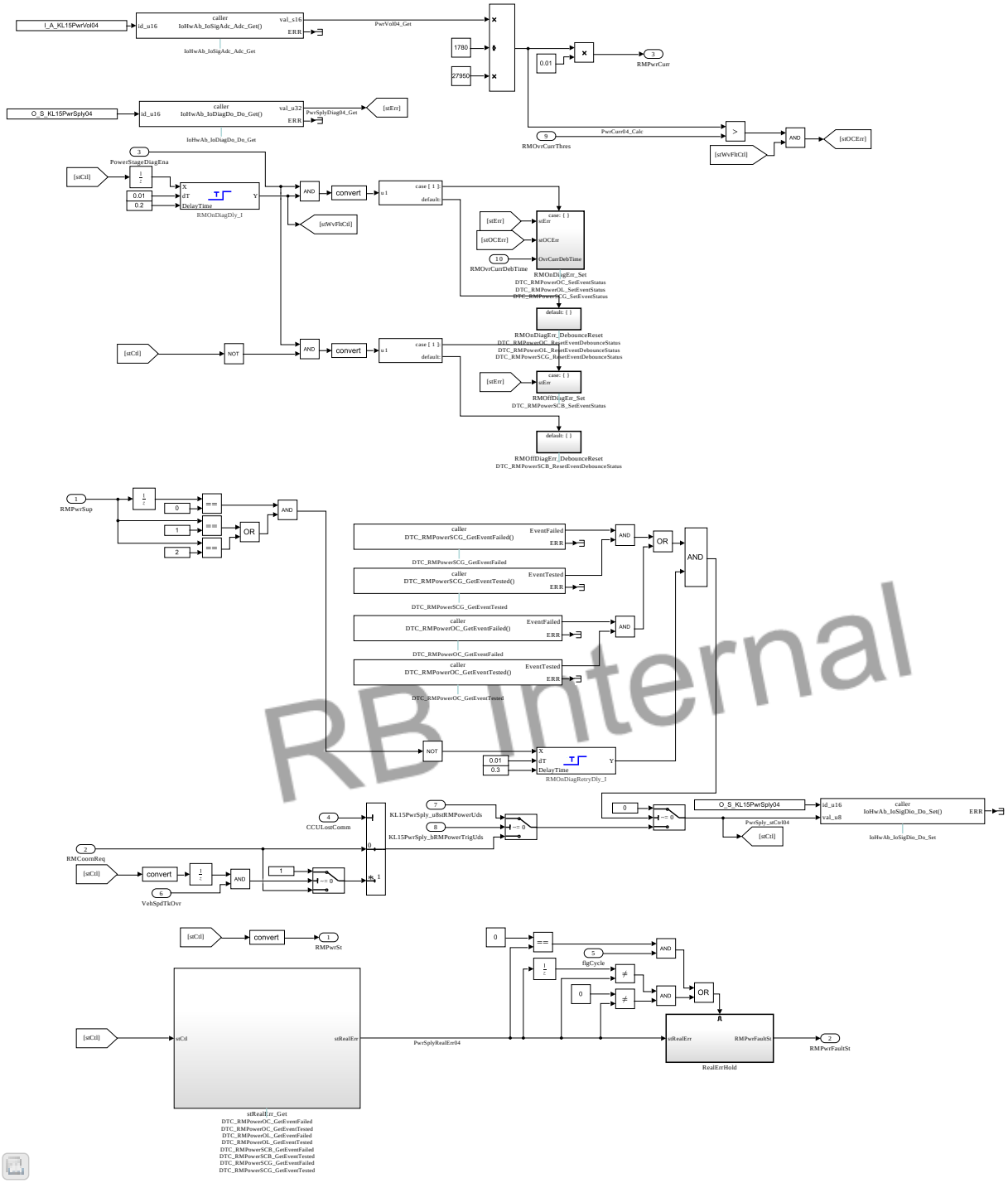

Figure 58 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOffDiagErr_DebounceReset]

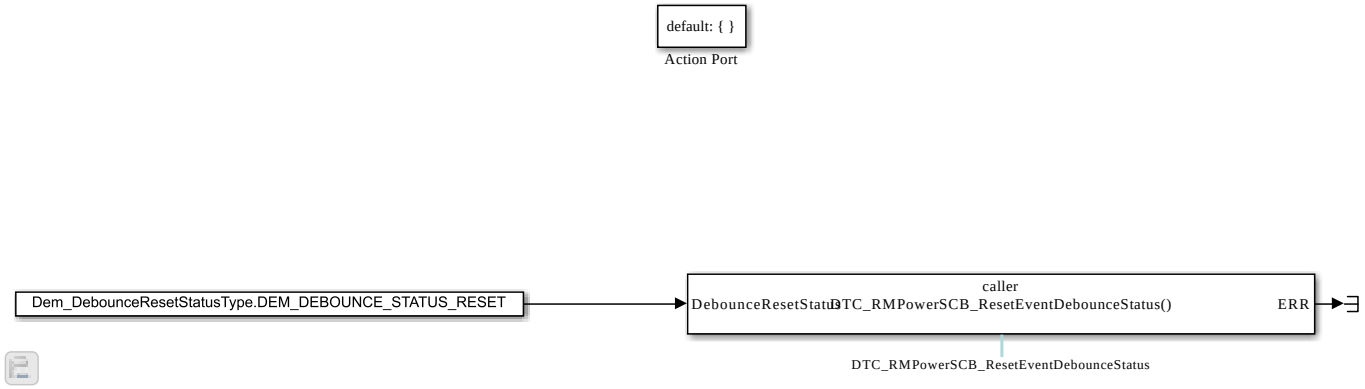

Figure 59 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOffDiagErr_Set]

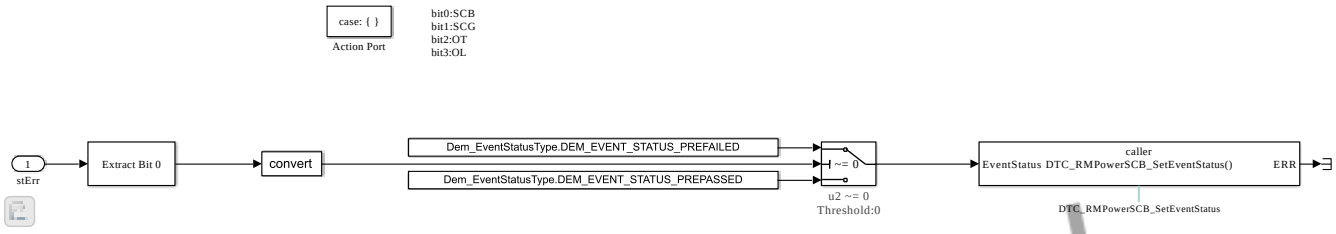

Figure 60 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOnDiagErr_DebounceReset]

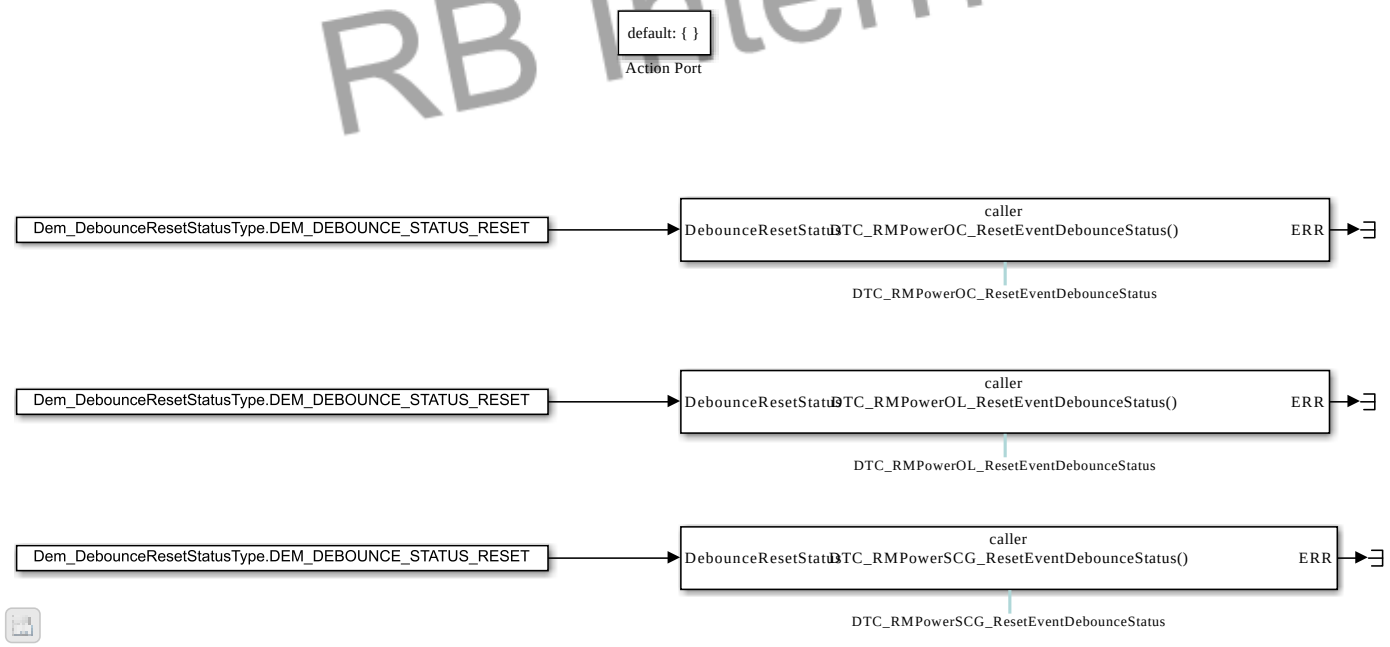

Figure 61 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RMOnDiagErr_Set]

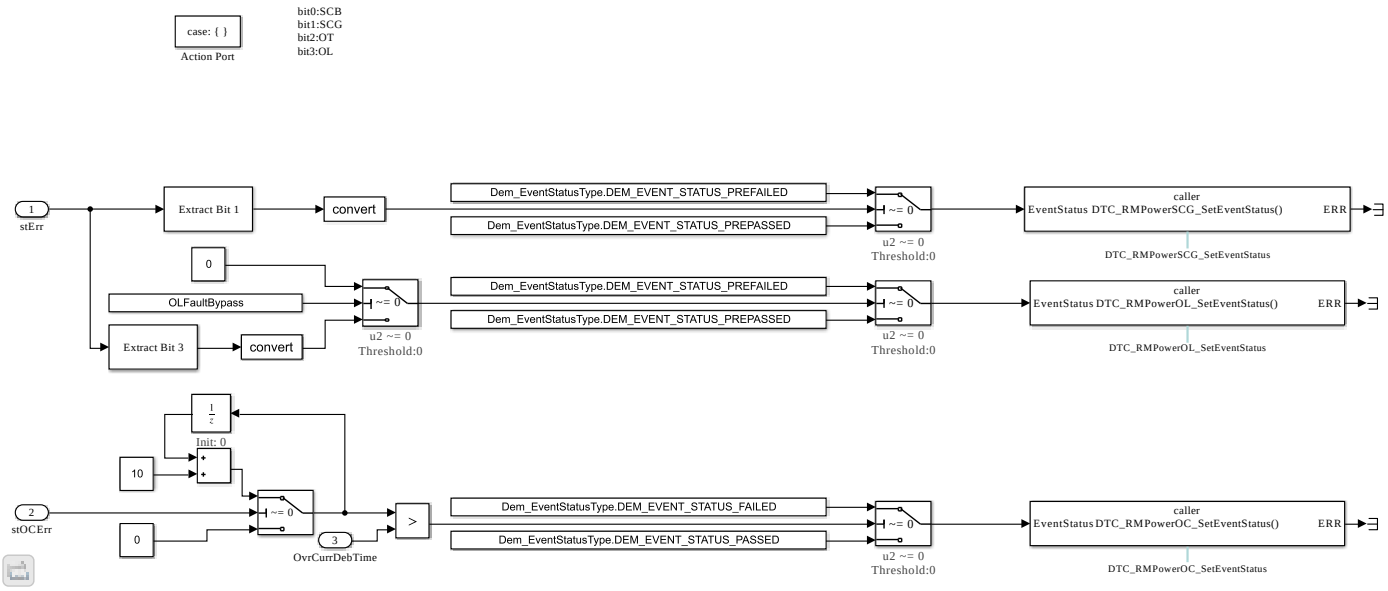

Figure 62 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_RealErrHold]

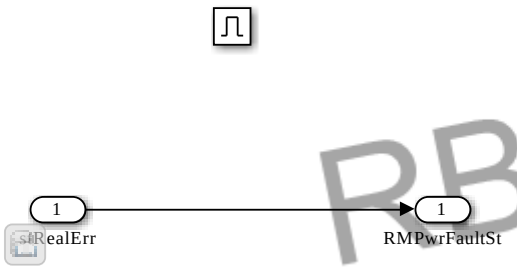

Figure 63 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RM_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
1=Circuit short to ground
2=Circuit short to battery
3=Open
4=Overpower
5~7=Reserve

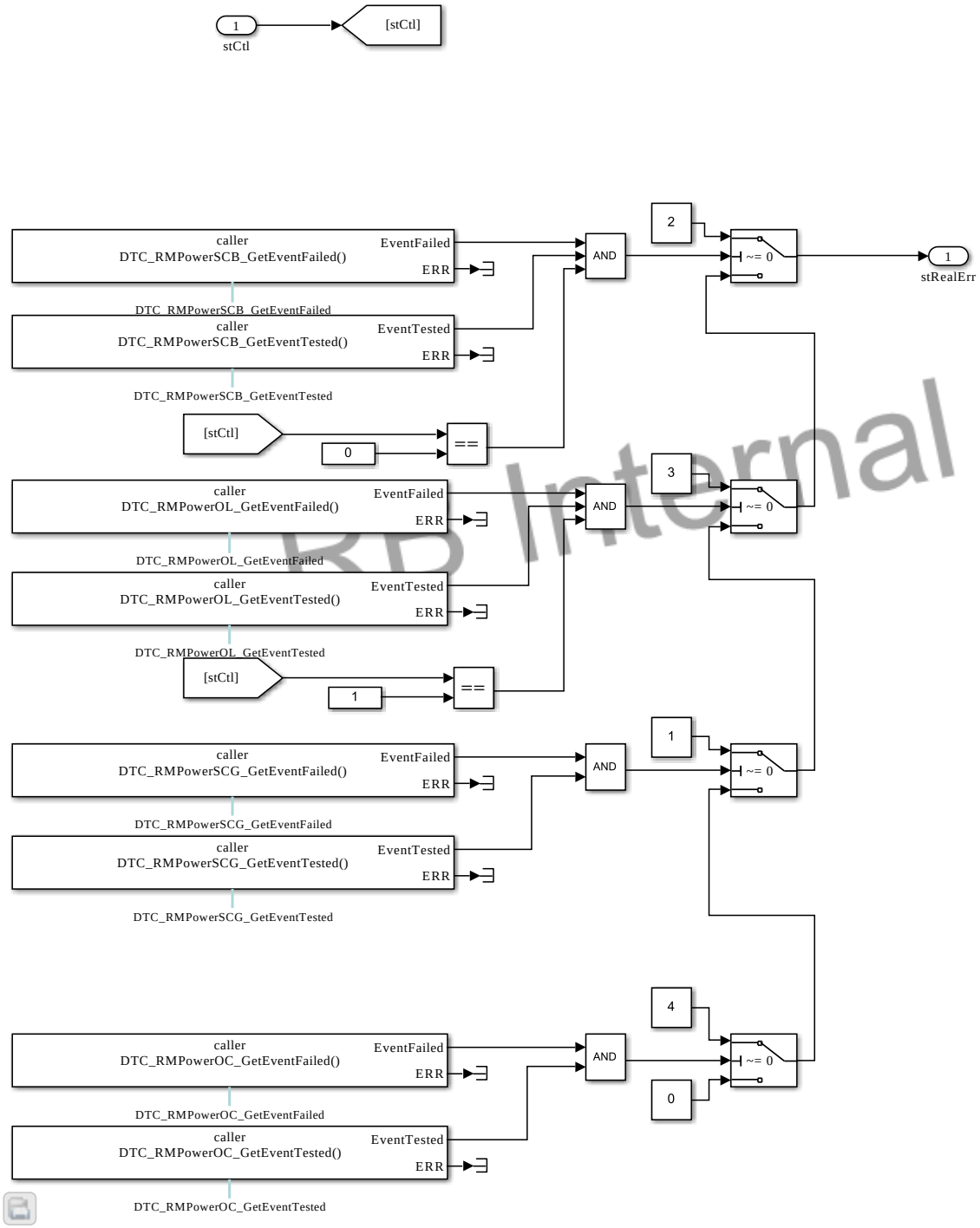

Figure 64 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampAllwSleepDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampAllwSleepDly]

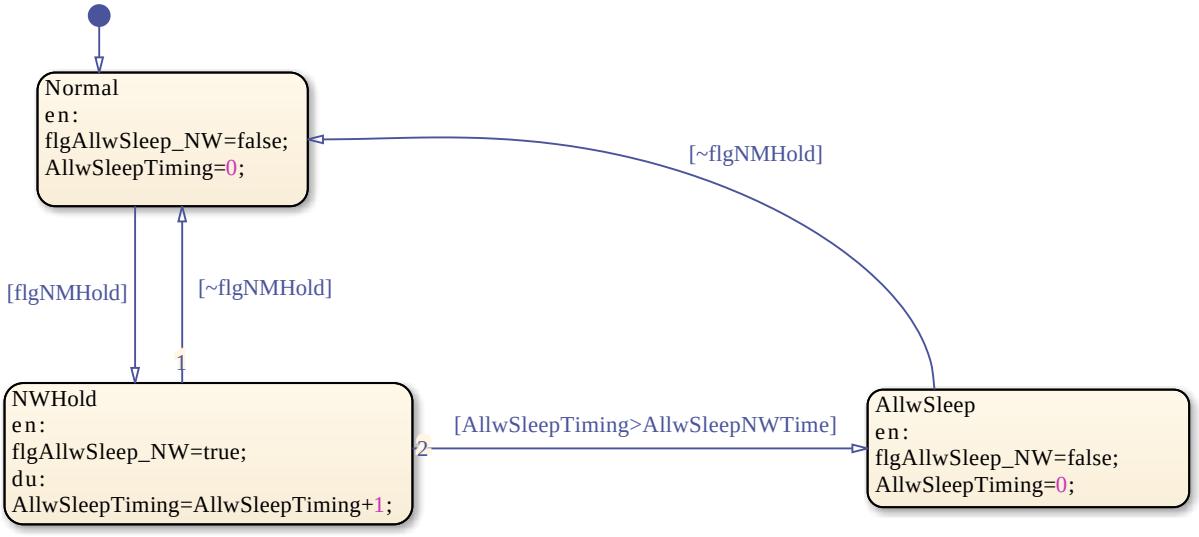

Figure 65 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff]

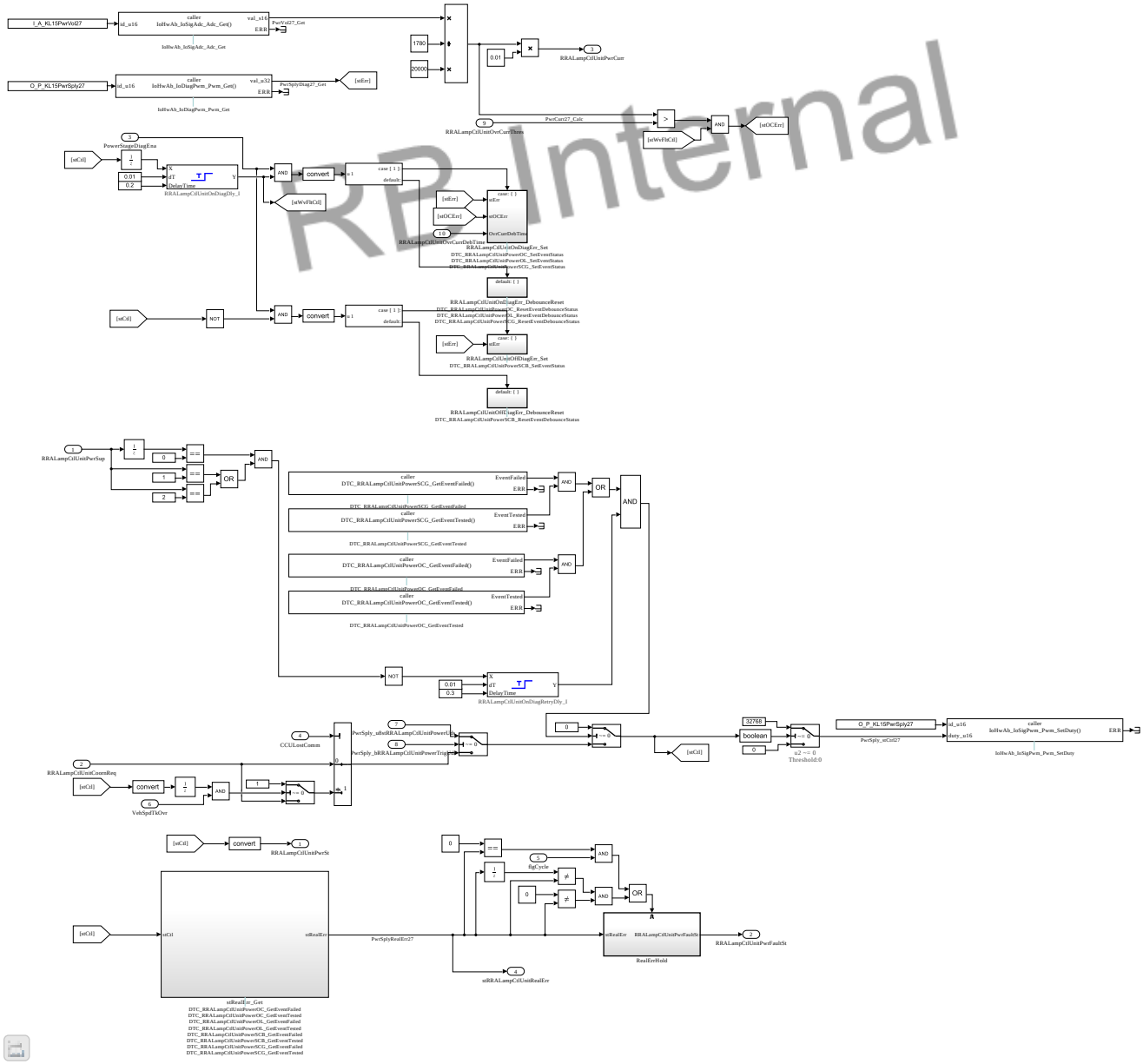

Figure 66 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOffDiagErr_DebounceReset]

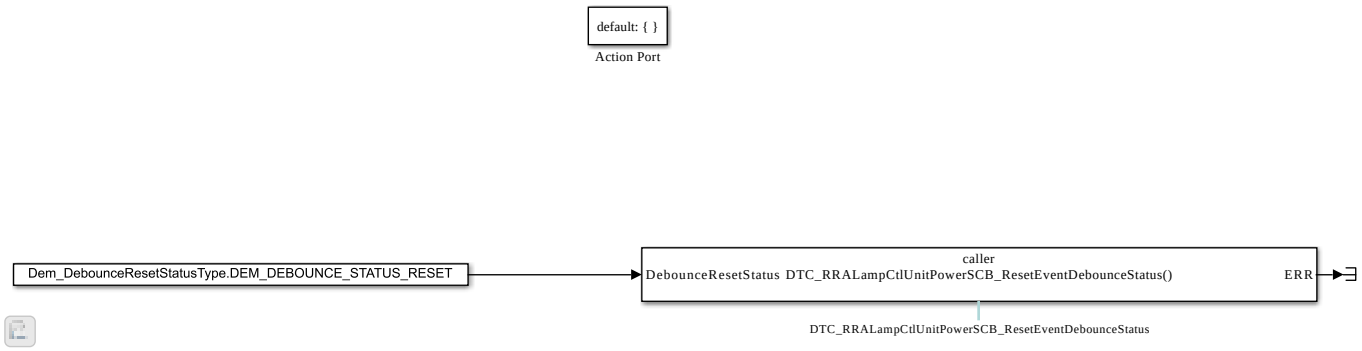

Figure 67 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOffDiagErr_Set]

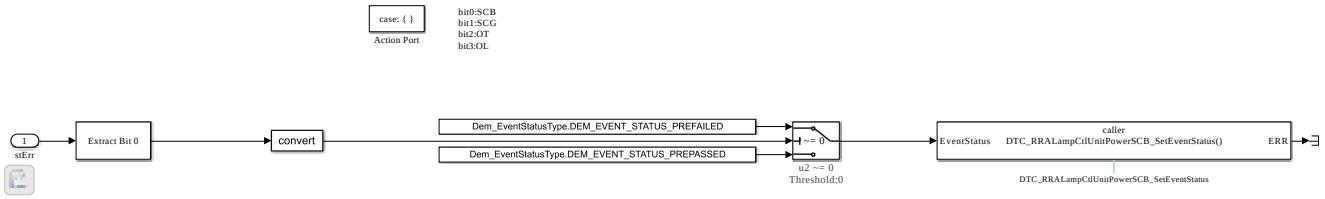

Figure 68 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOnDiagErr_DebounceReset]

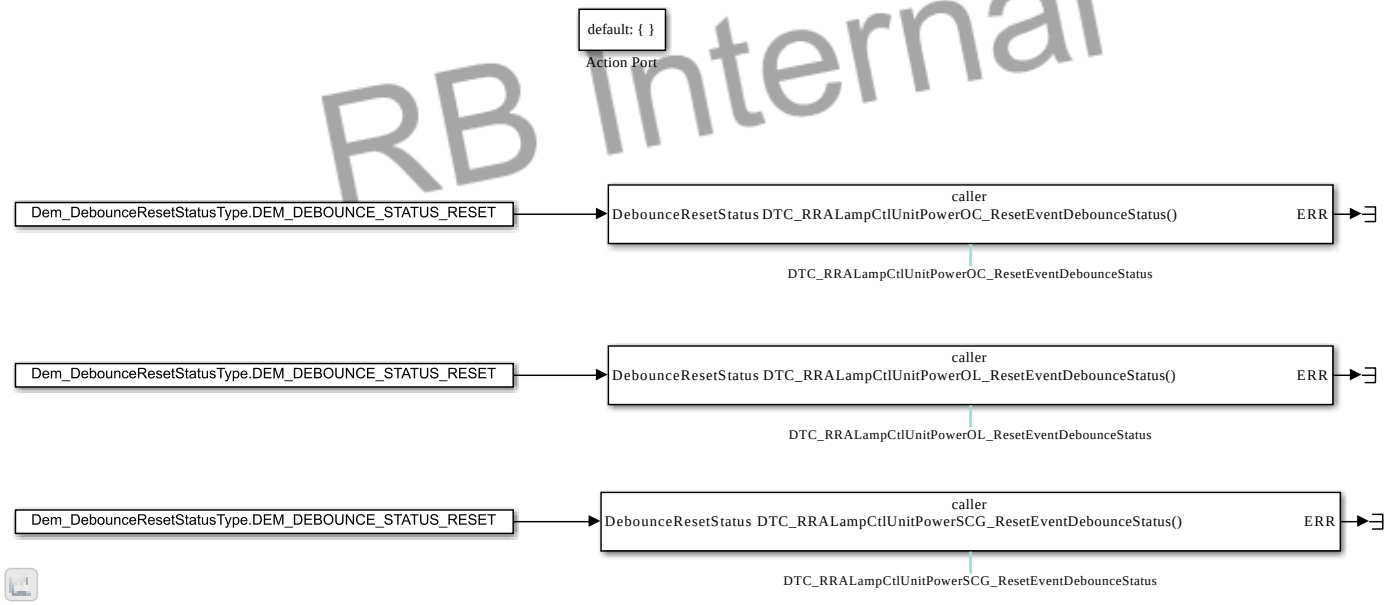

Figure 69 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RRALampCtlUnitOnDiagErr_Set]

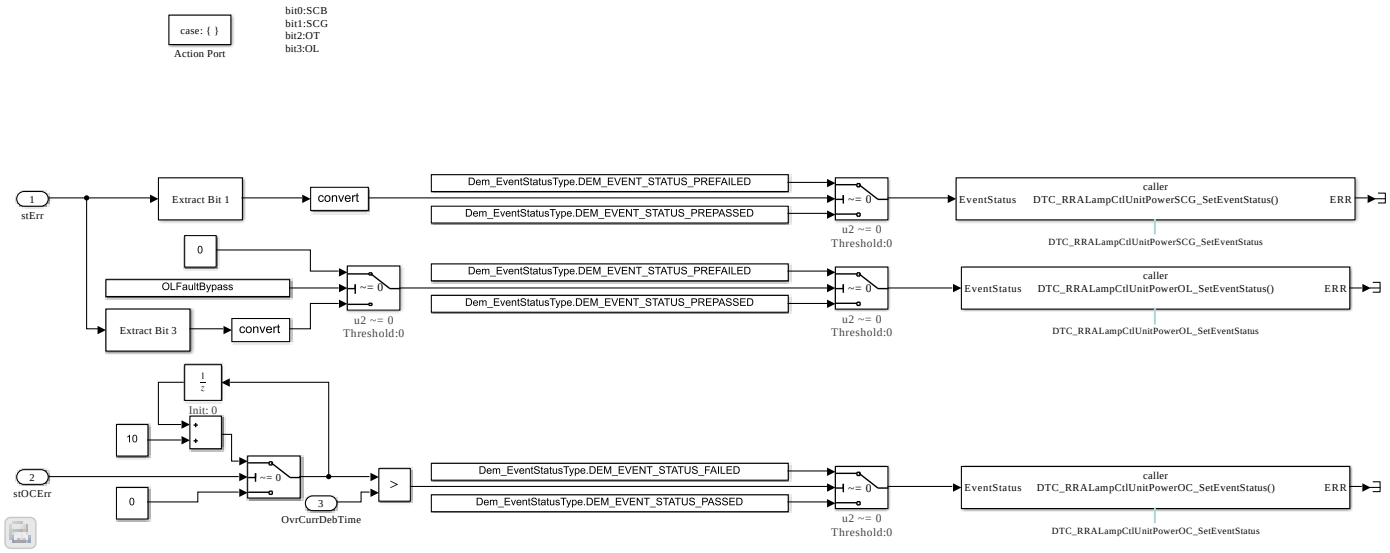

Figure 70 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold]

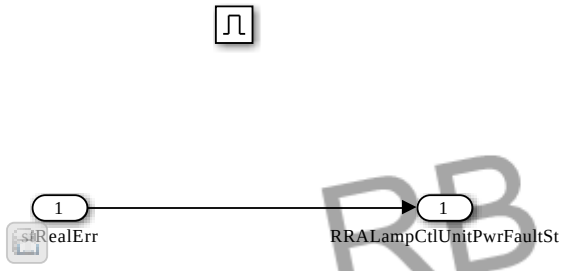

Figure 71 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get]

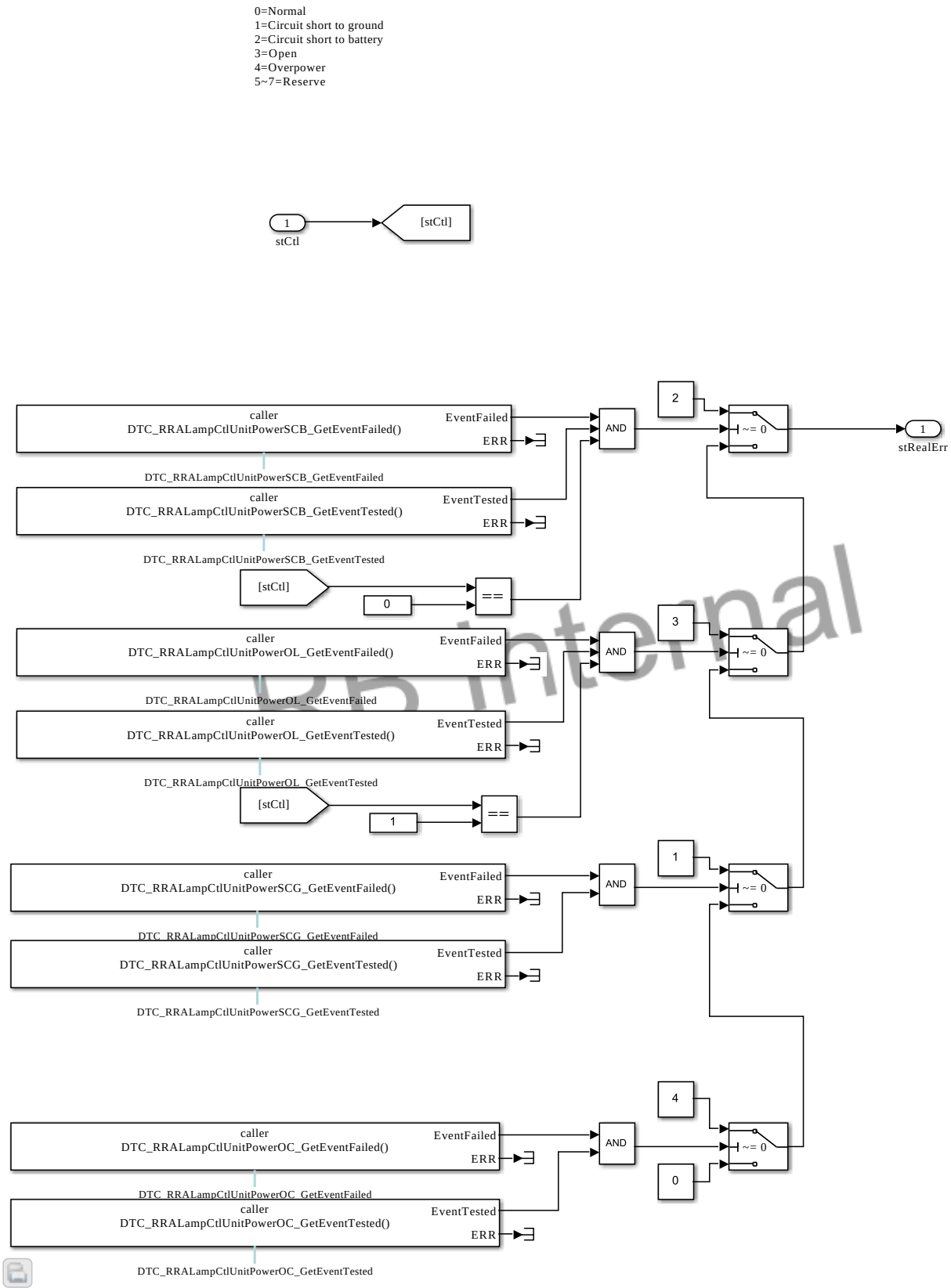

Figure 72 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRALampWkUpDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRA-LampWkUpDly]

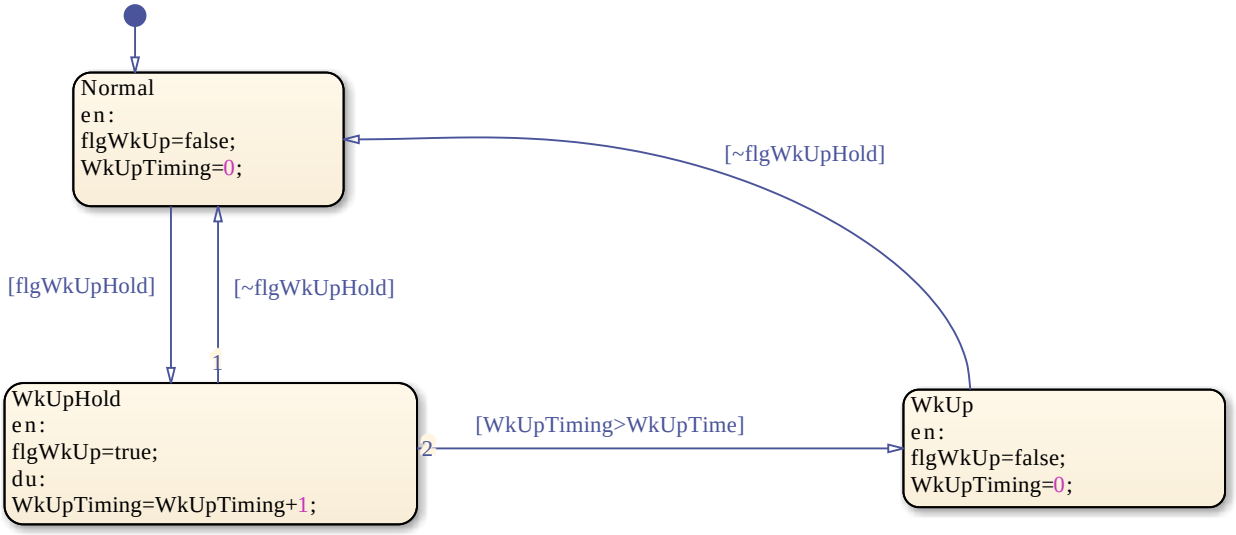

Figure 73 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampAllwSleepDly [KL15PwrSply_DD_KL15PwrSply_DD_Runna-ble_10ms_sys_RRBLampAllwSleepDly]

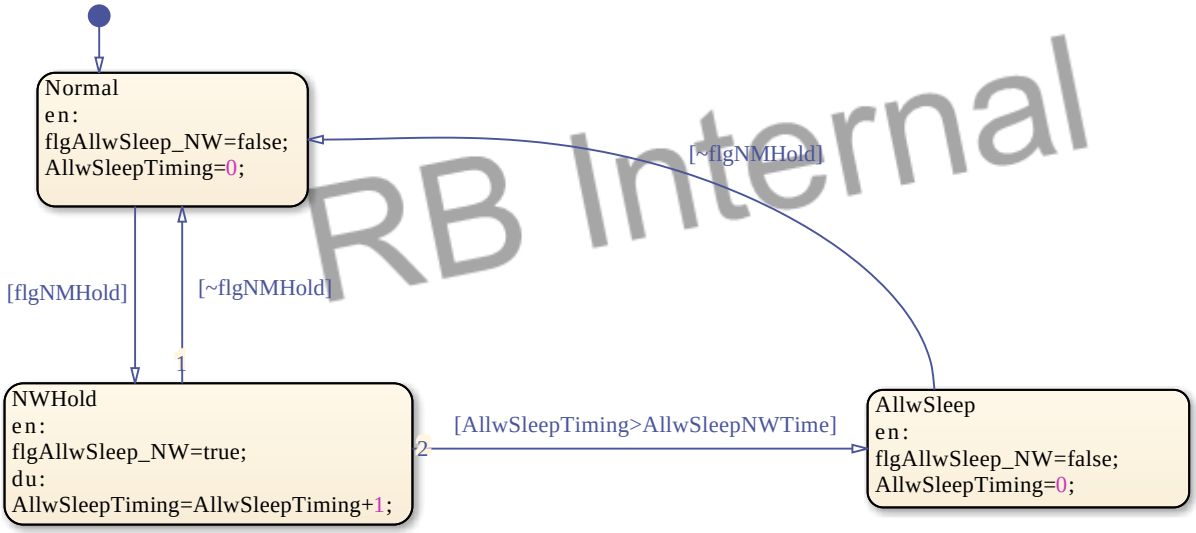

Figure 74 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff]

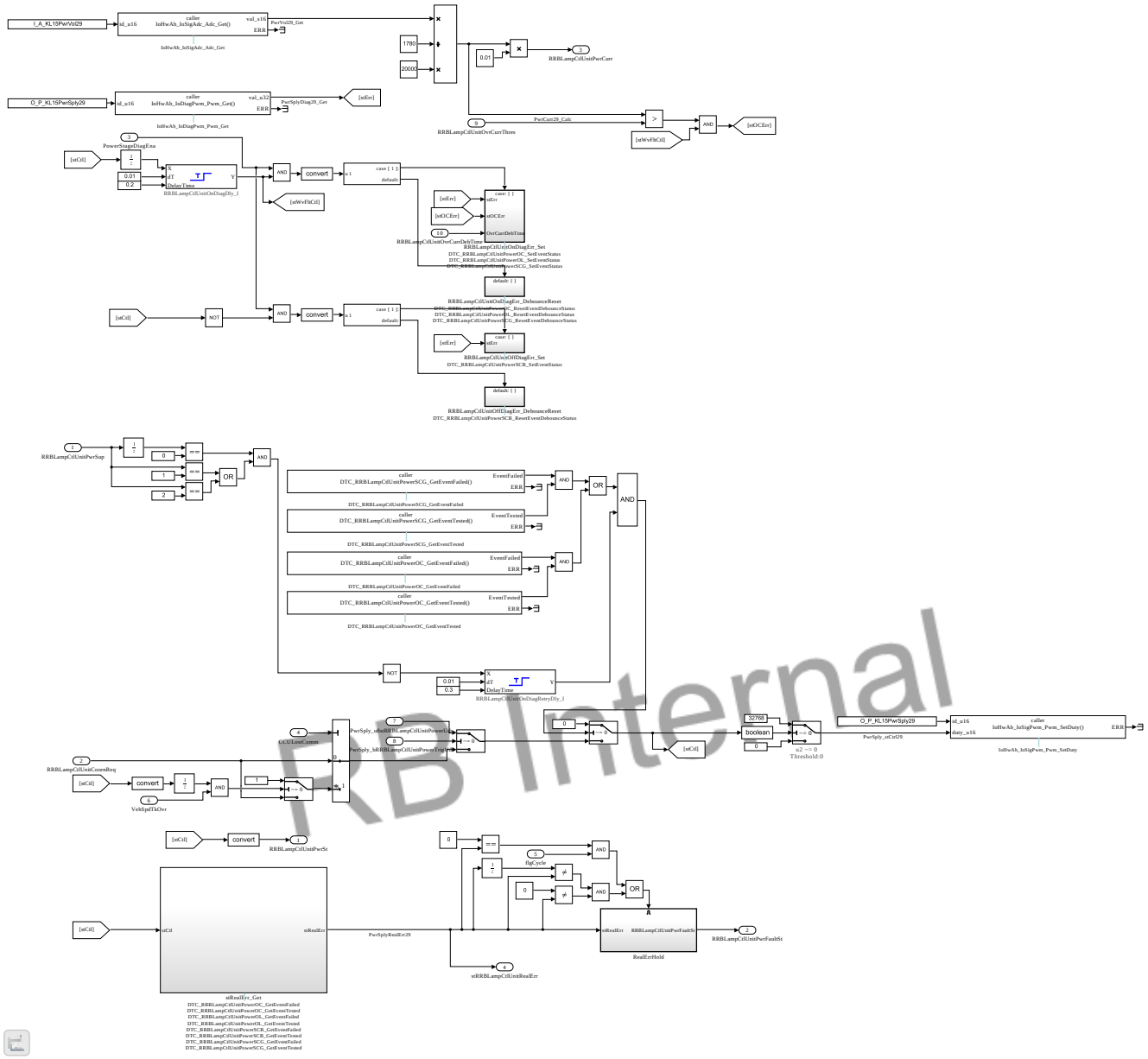

Figure 75 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOffDiagErr_DebounceReset]

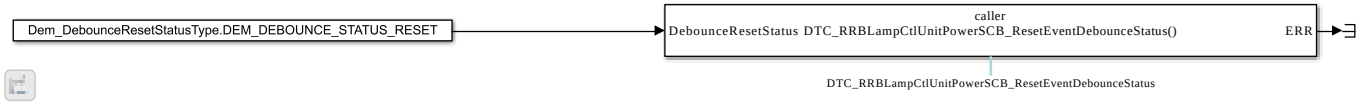

Figure 76 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOffDiagErr_Set]

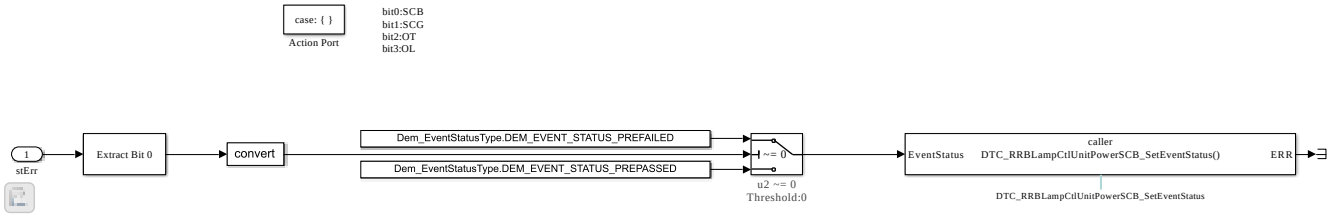

Figure 77 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOnDiagErr_DebounceReset]

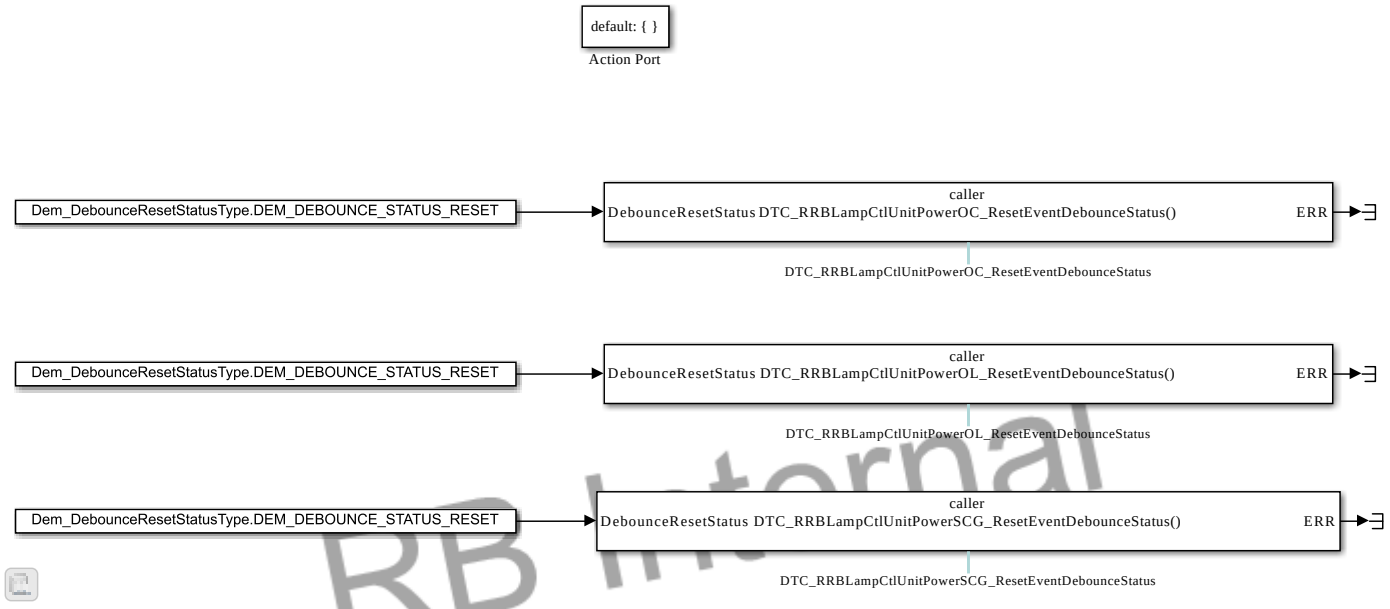

Figure 78 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RRBLampCtlUnitOnDiagErr_Set]

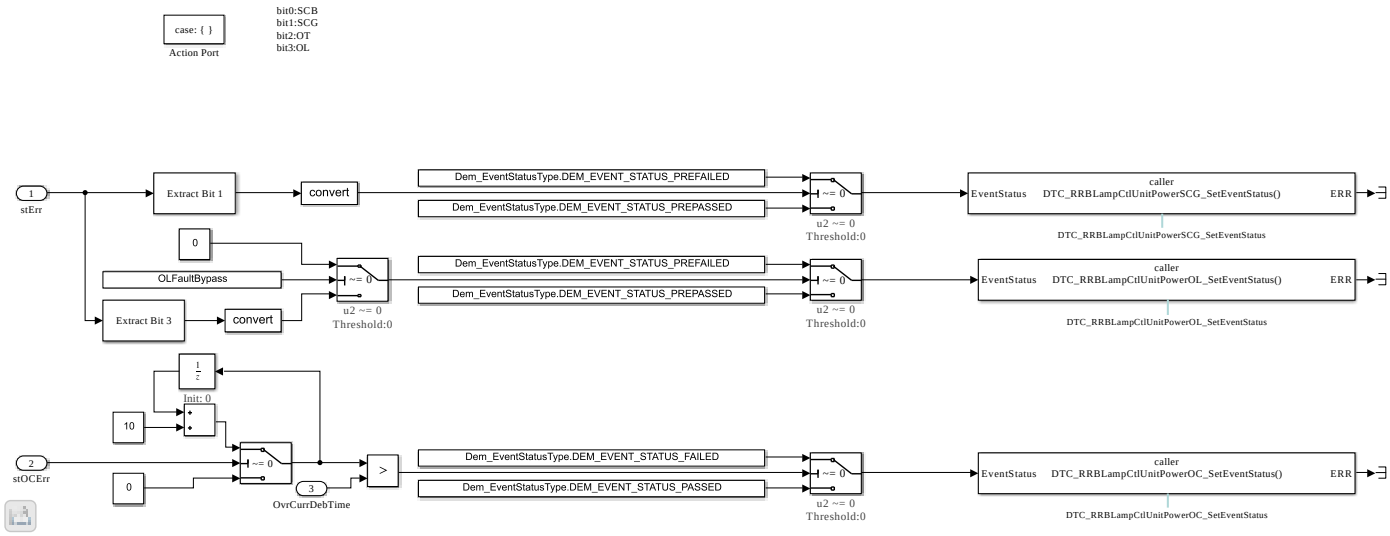

Figure 79 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_RealErrHold]

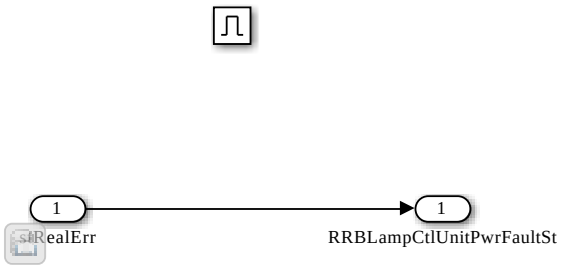

Figure 80 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampCtlUnit_Ctl_2F_Diag_CutOff_stRealErr_Get]

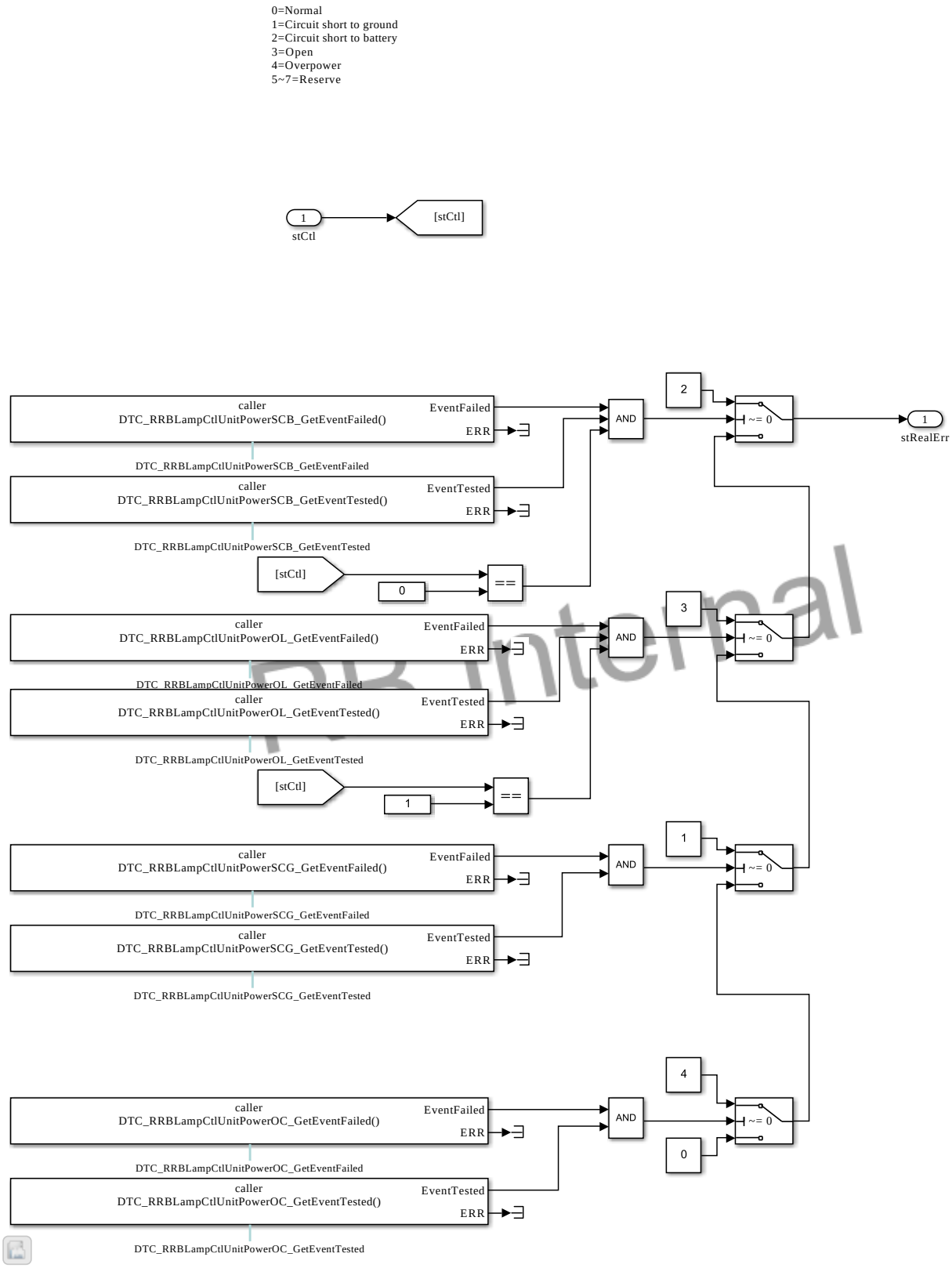

Figure 81 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampWkUpDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RRBLampWkUpDly]

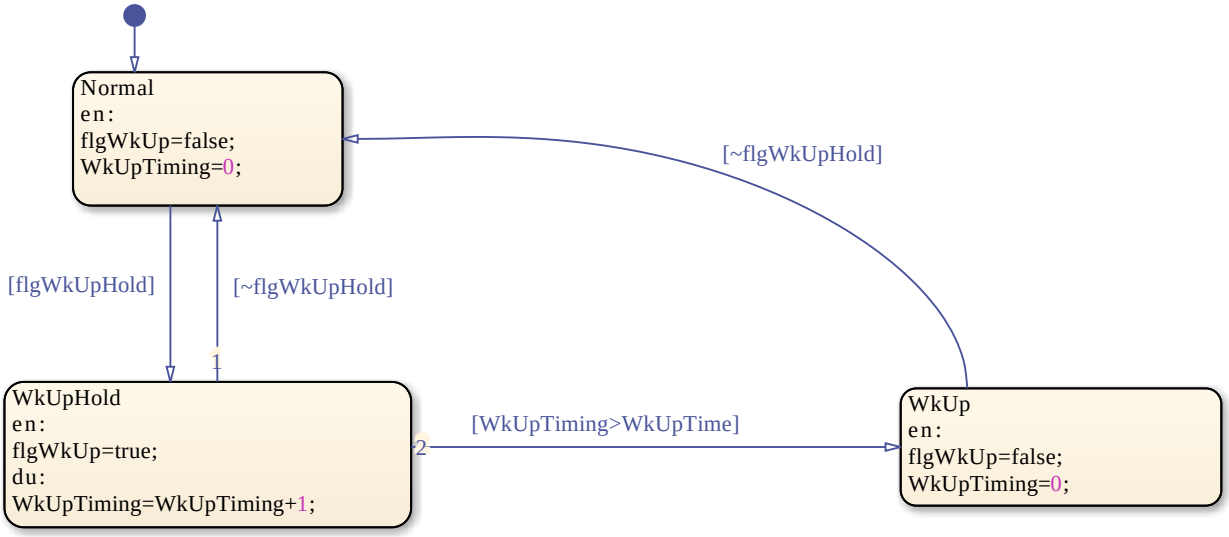

Figure 82 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff]

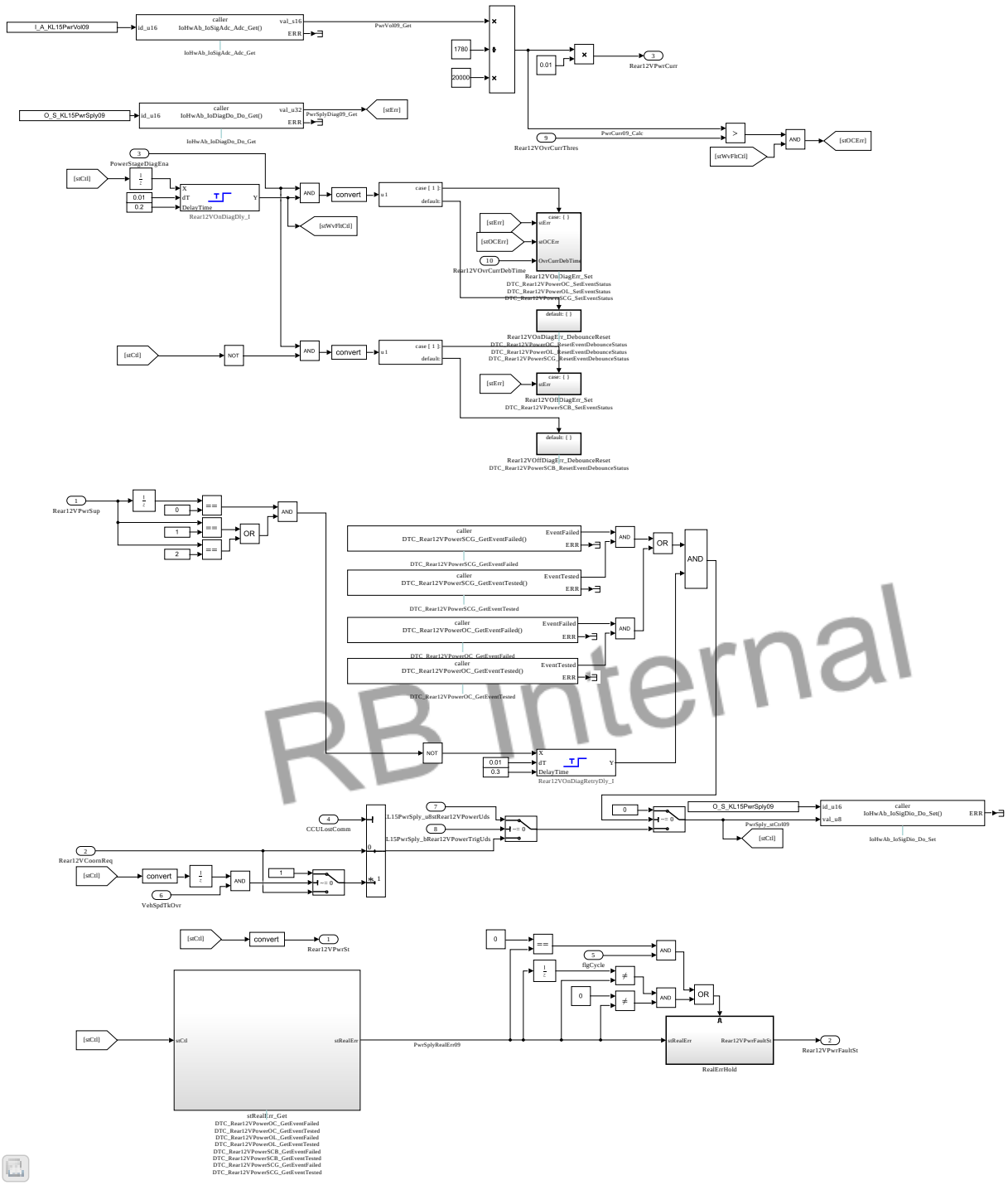

Figure 83 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_RealErrHold]

Figure 84 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOffDiagErr_DebounceReset]

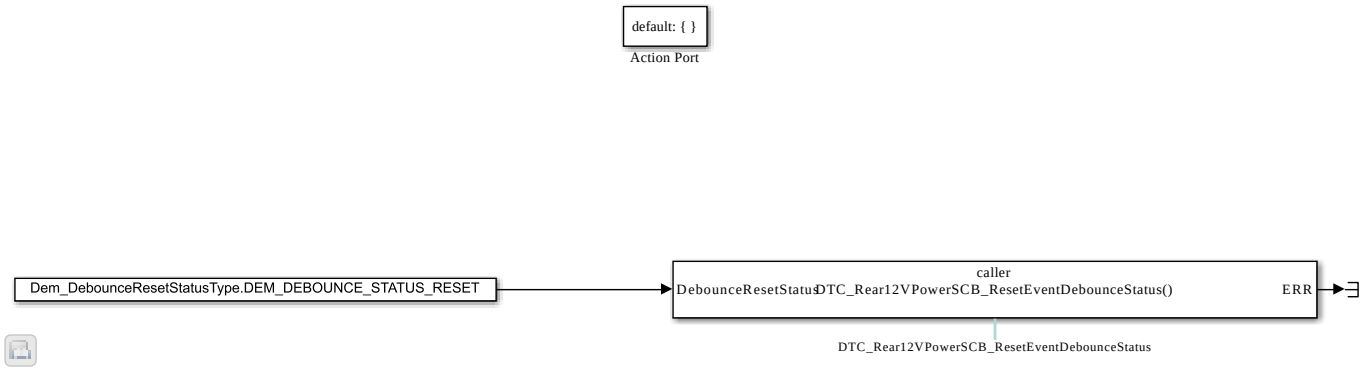

Figure 85 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOffDiagErr_Set]

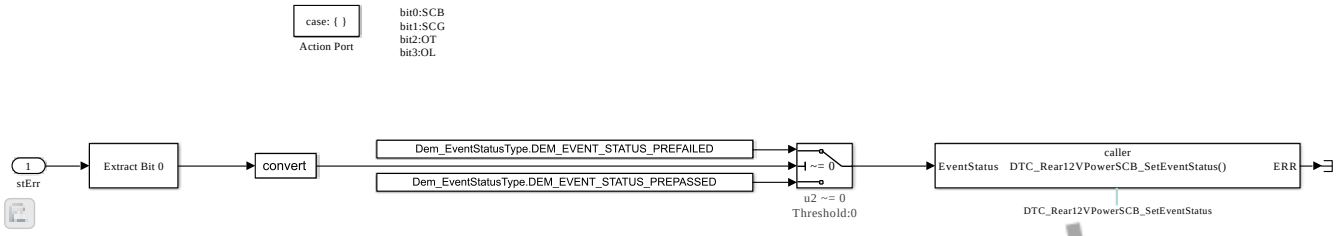

Figure 86 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOnDiagErr_DebounceReset]

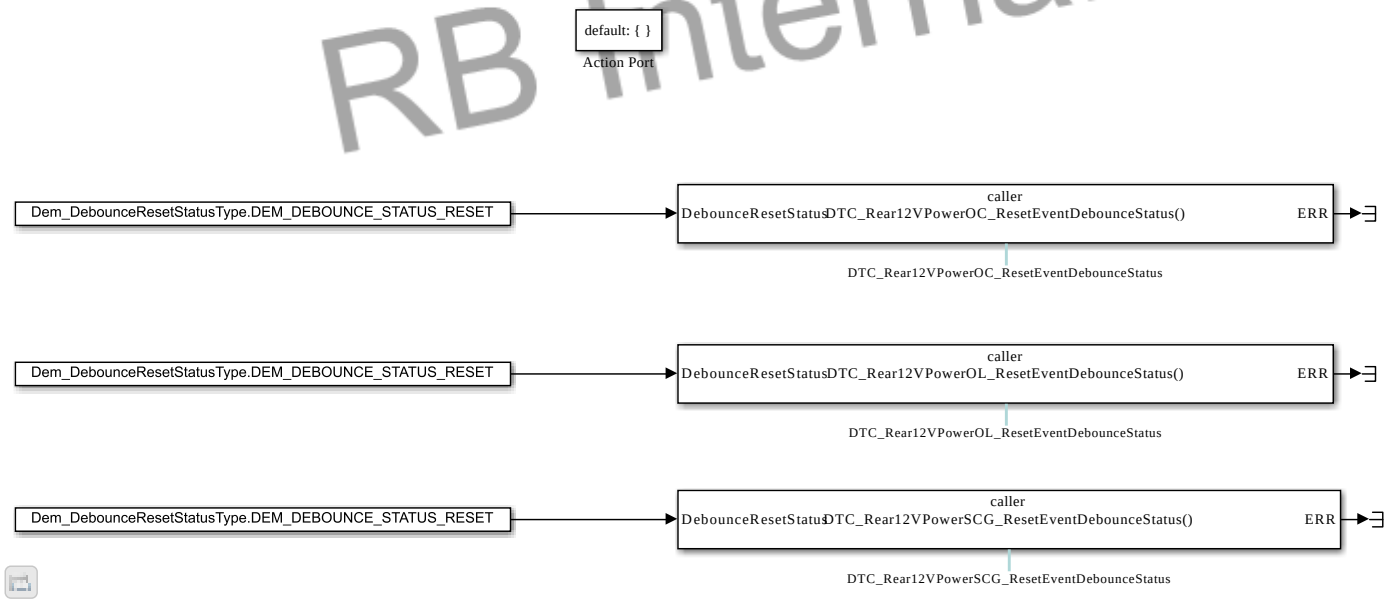

Figure 87 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_Rear12VOnDiagErr_Set]

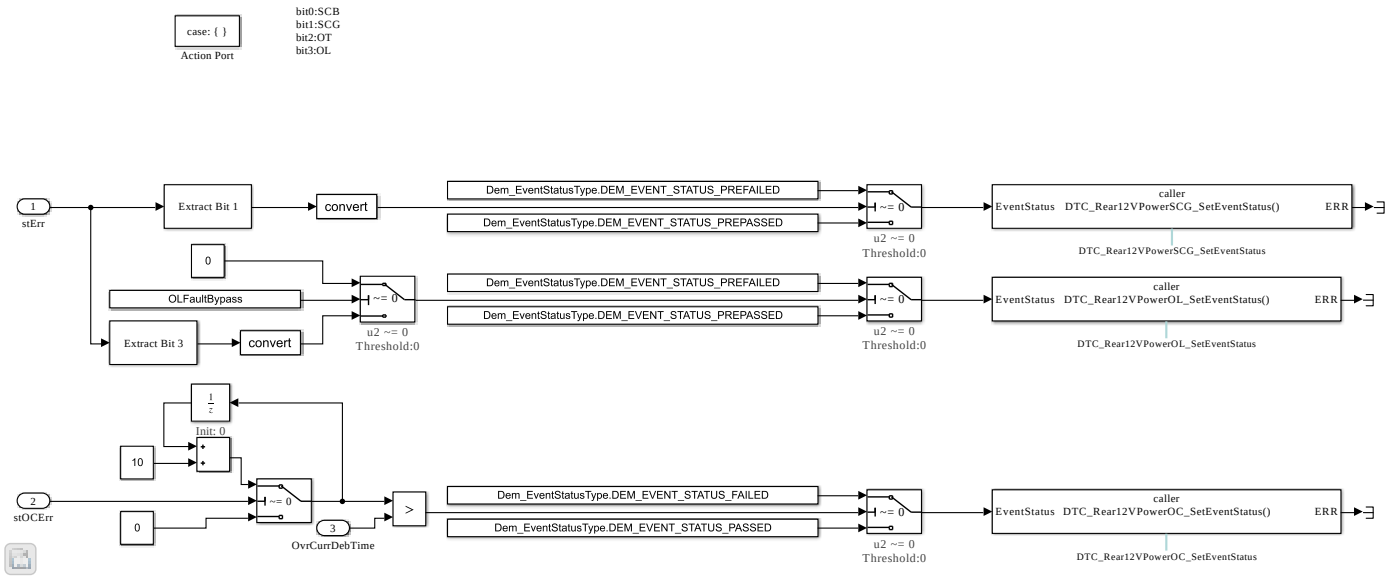

Figure 88 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_Rear12V_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
1=Circuit short to ground
2=Circuit short to battery
3=Open
4=Overpower
5~7=Reserve

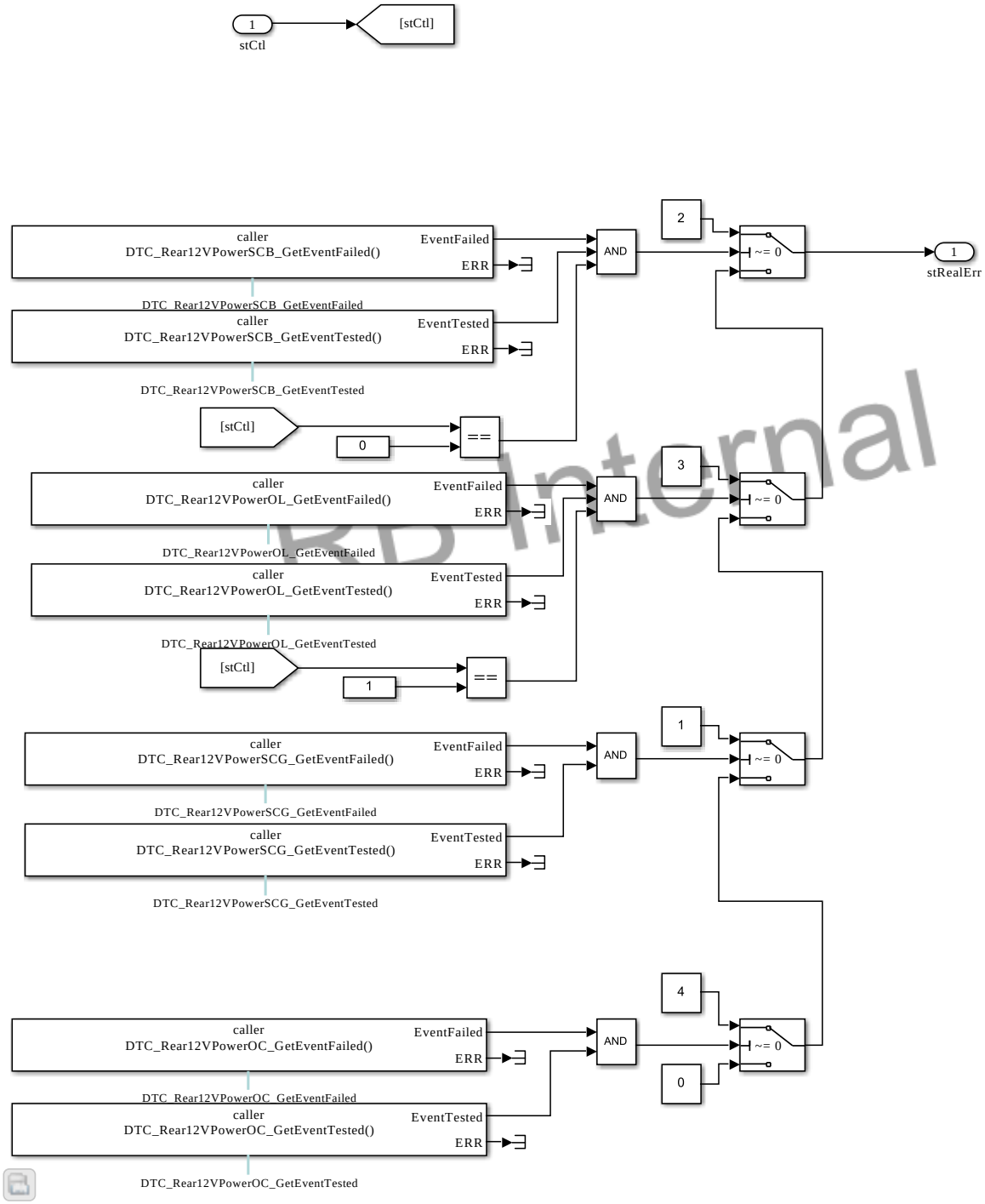

Figure 89 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff]

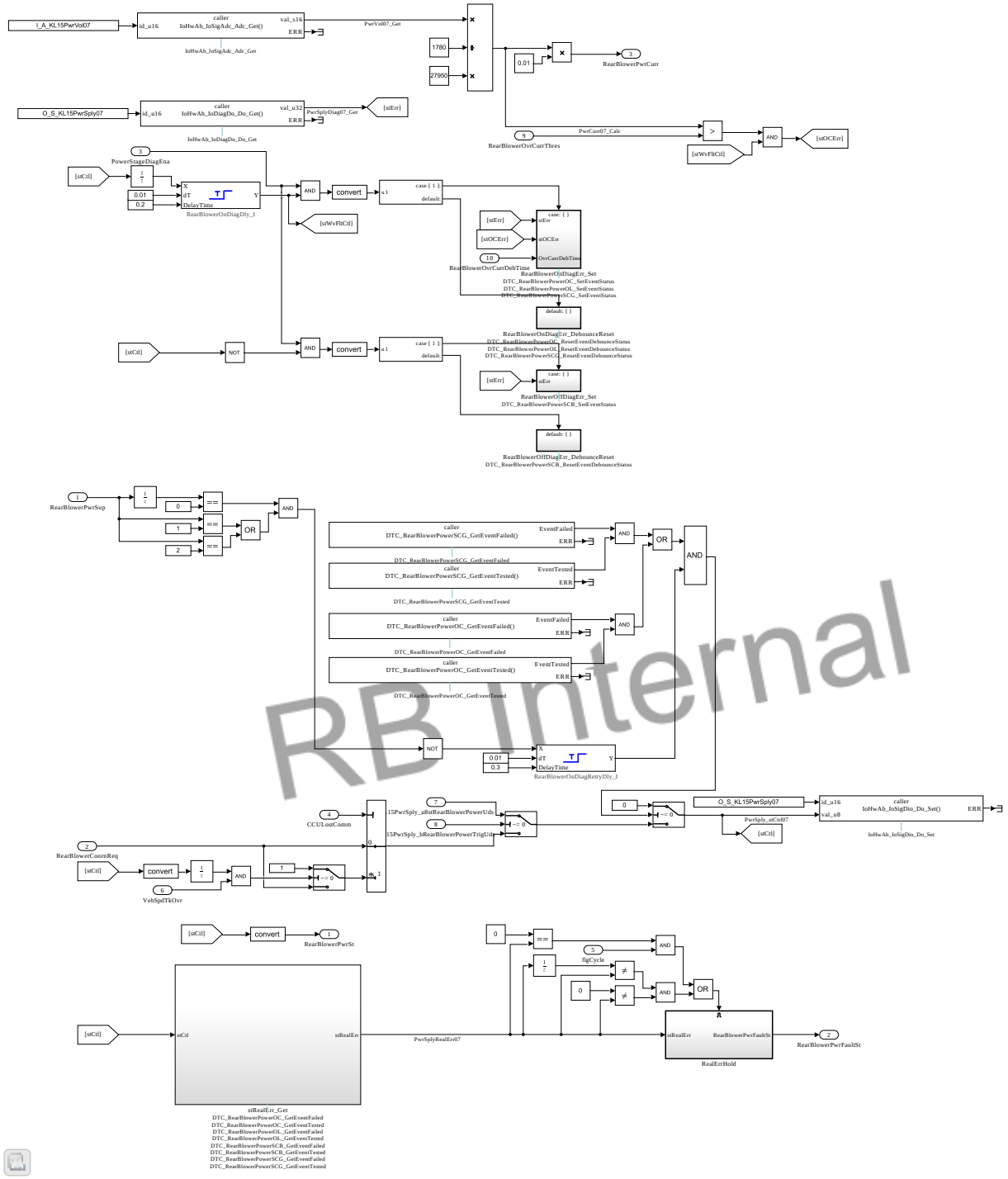

Figure 90 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RealErrHold]

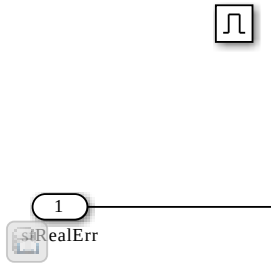

Figure 91 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOffDiagErr_DebounceReset]

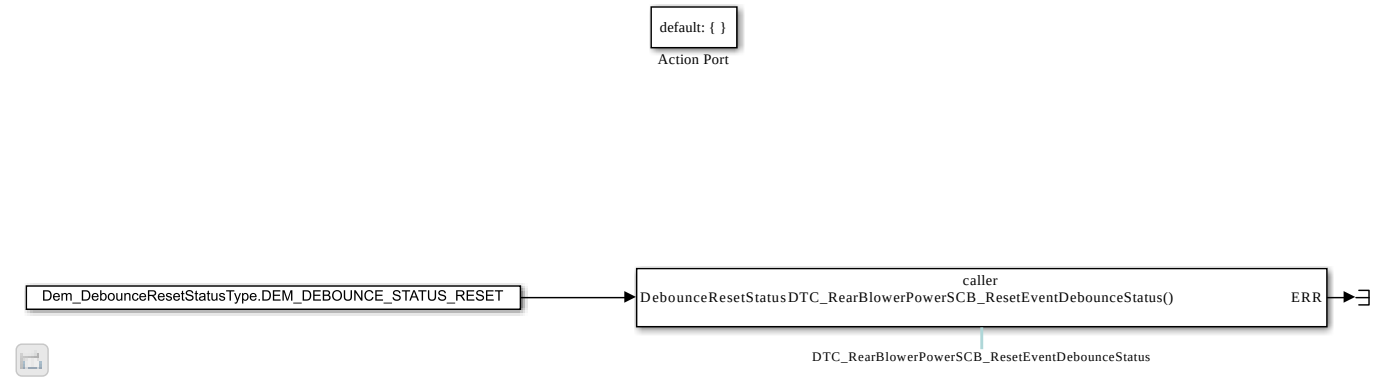

Figure 92 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOffDiagErr_Set]

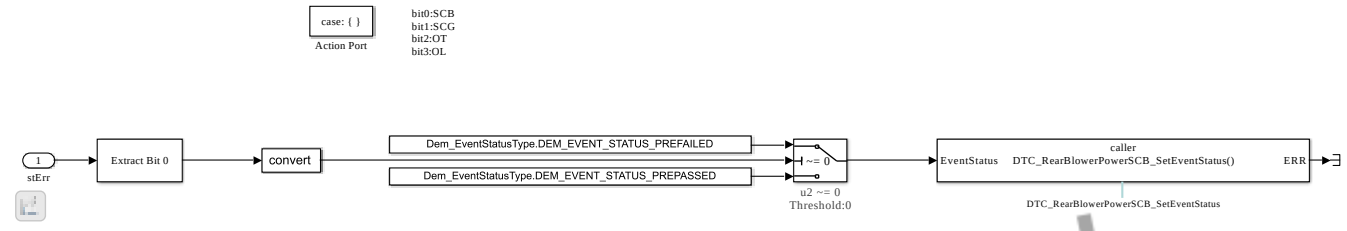

Figure 93 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOnDiagErr_DebounceReset]

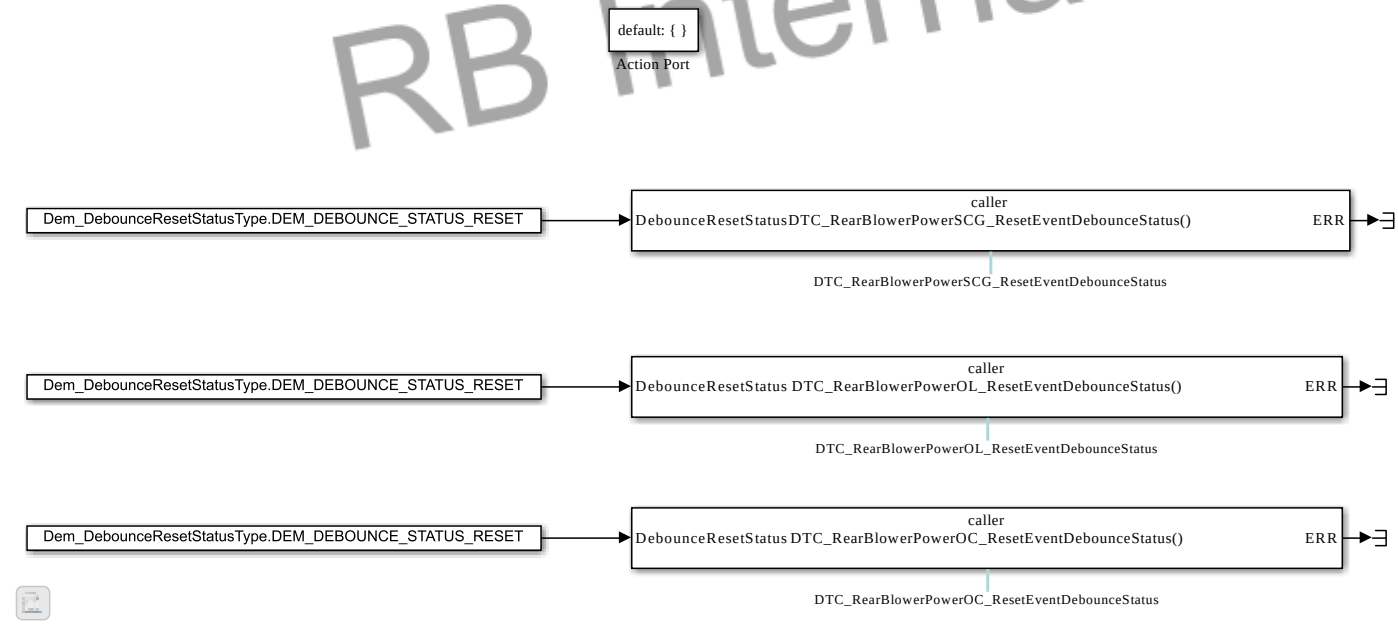

Figure 94 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_RearBlowerOnDiagErr_Set]

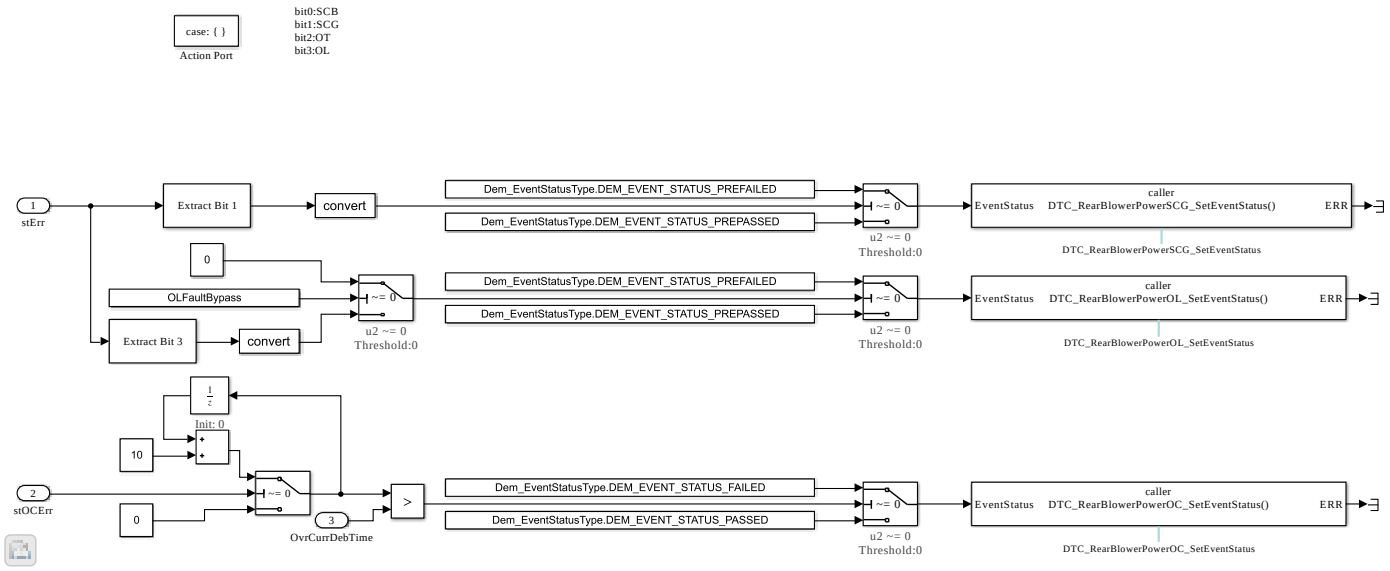

Figure 95 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearBlower_Ctl_2F_Diag_CutOff_stRealErr_Get]

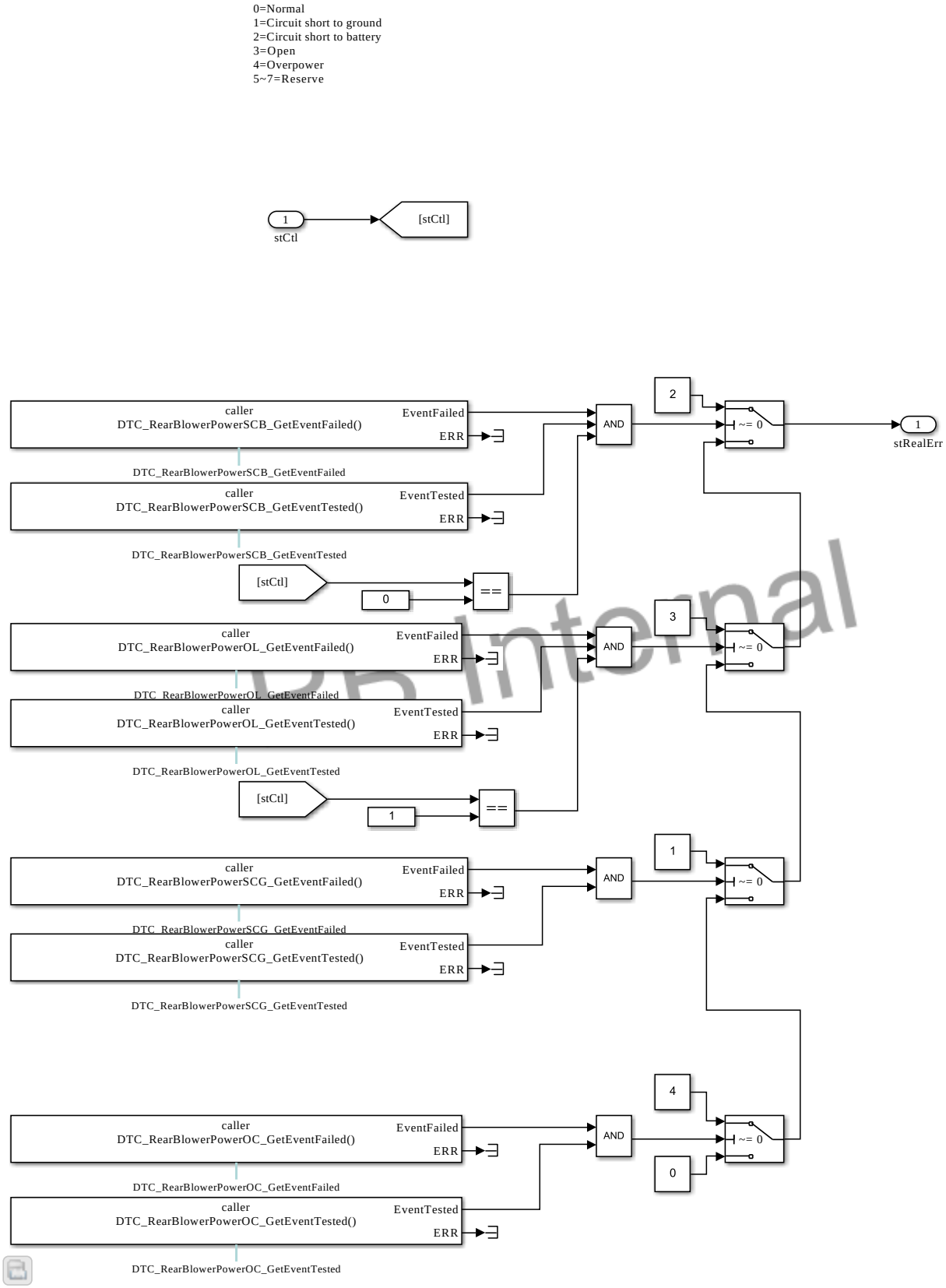

Figure 96 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff]

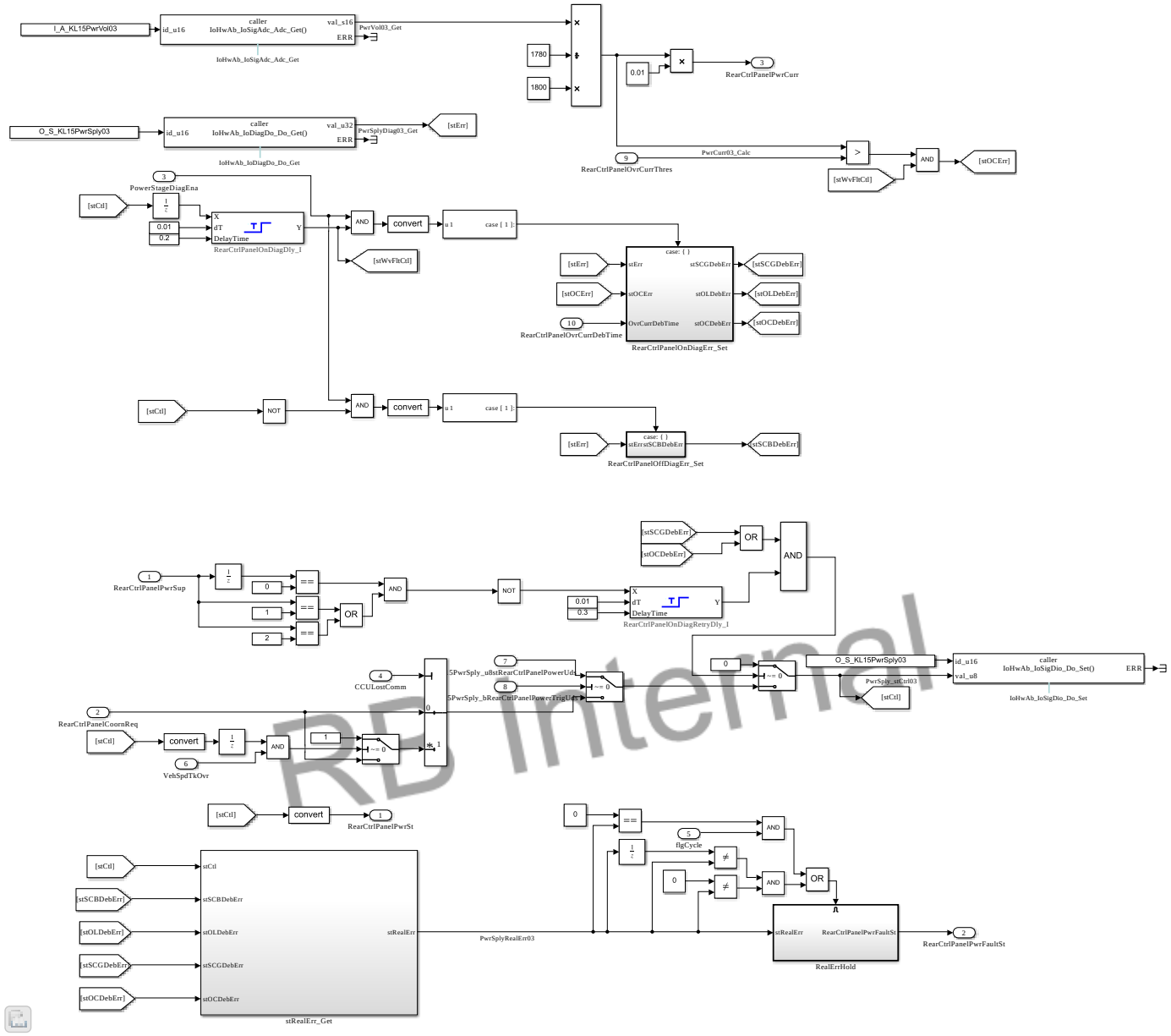

Figure 97 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_RealErrHold]

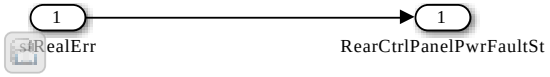

Figure 98 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_RearCtrlPanelOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_RearCtrlPanelOffDiagErr_Set]

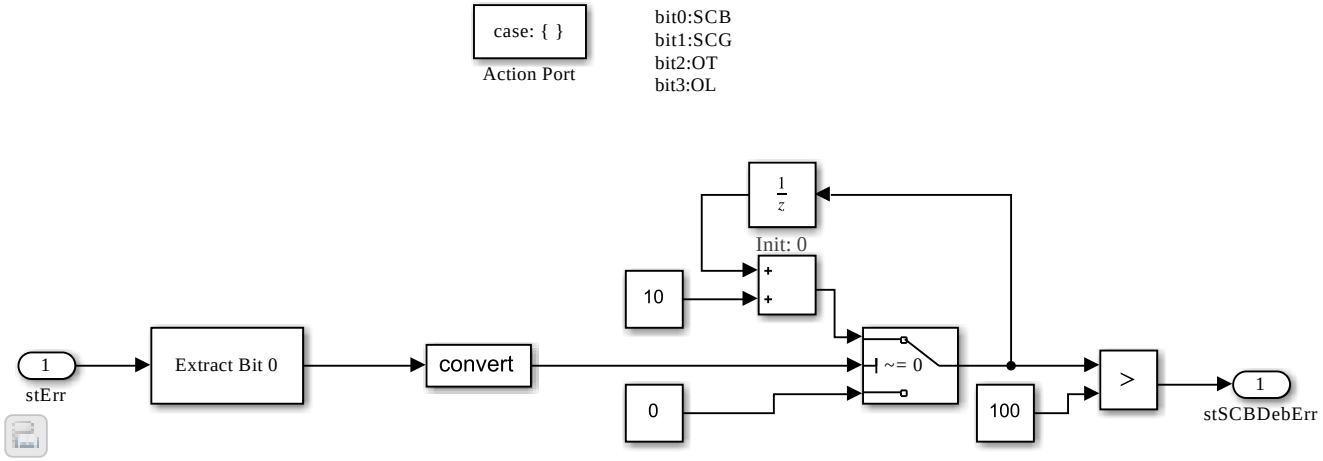

Figure 99 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_RearCtrlPanelOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_RearCtrlPanelOnDiagErr_Set]

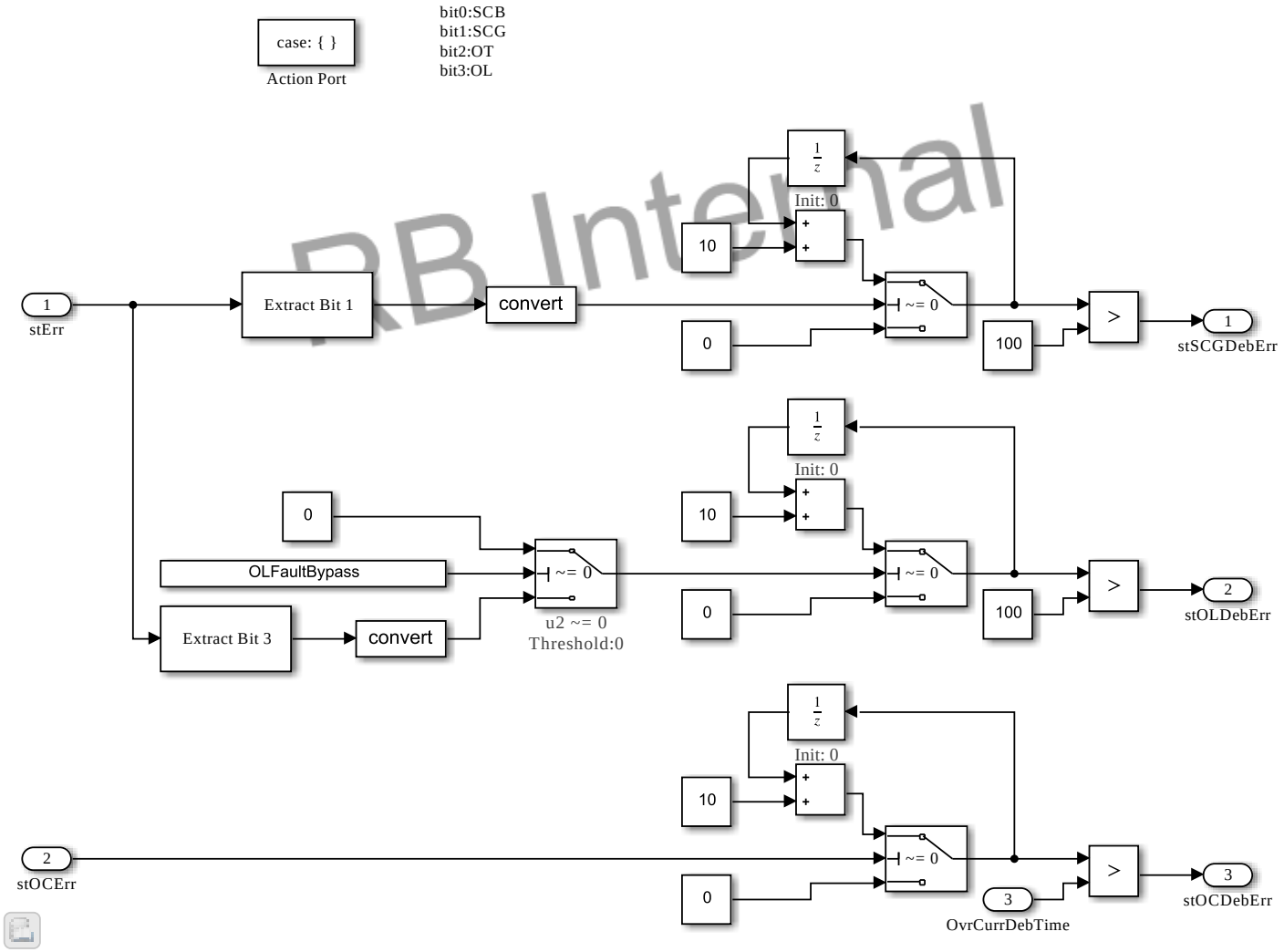

Figure 100 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearCtrlPanel_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
1=Circuit short to ground
2=Circuit short to battery
3=Open
4=Overpower
5~7=Reserve

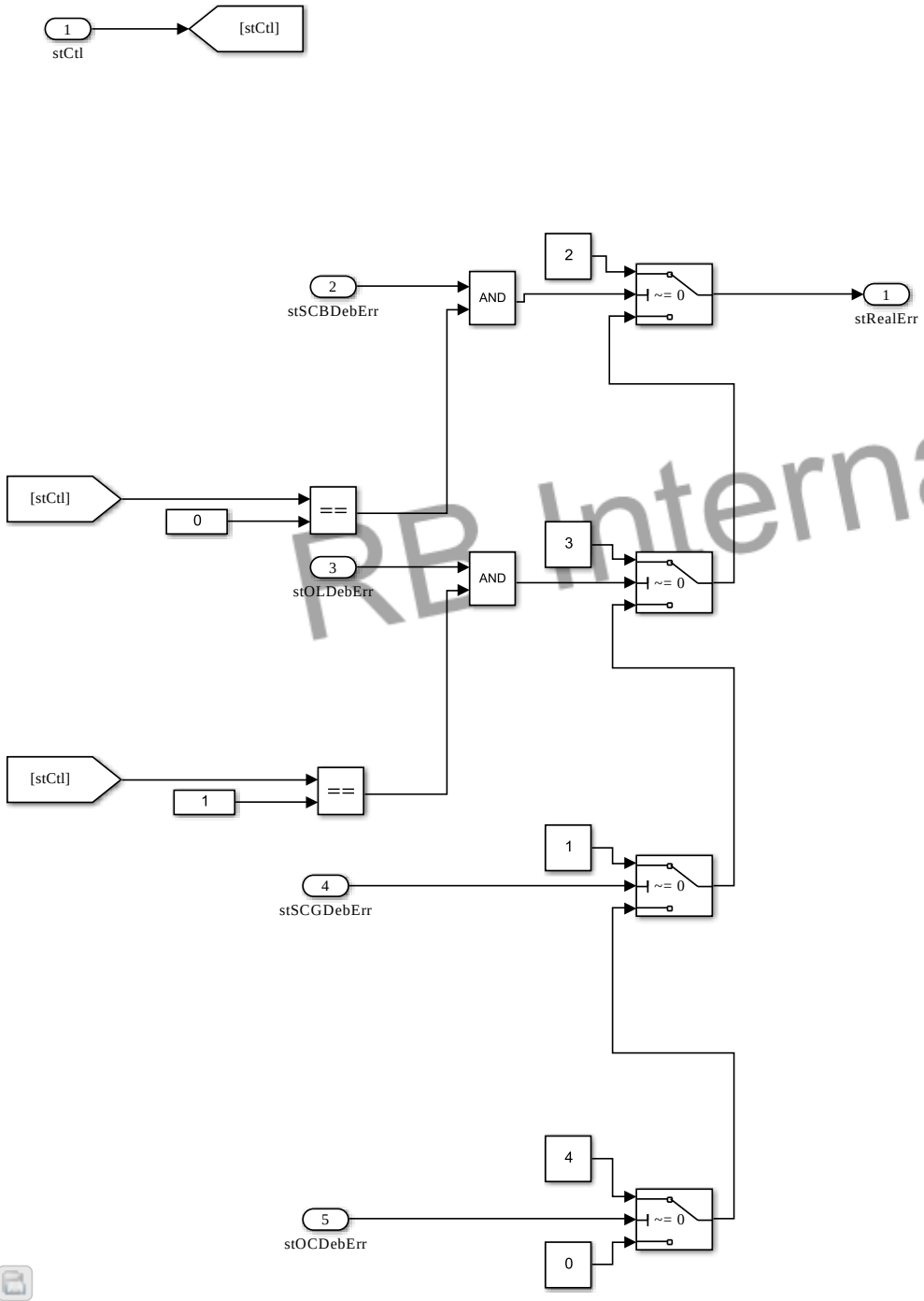

Figure 101 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff]

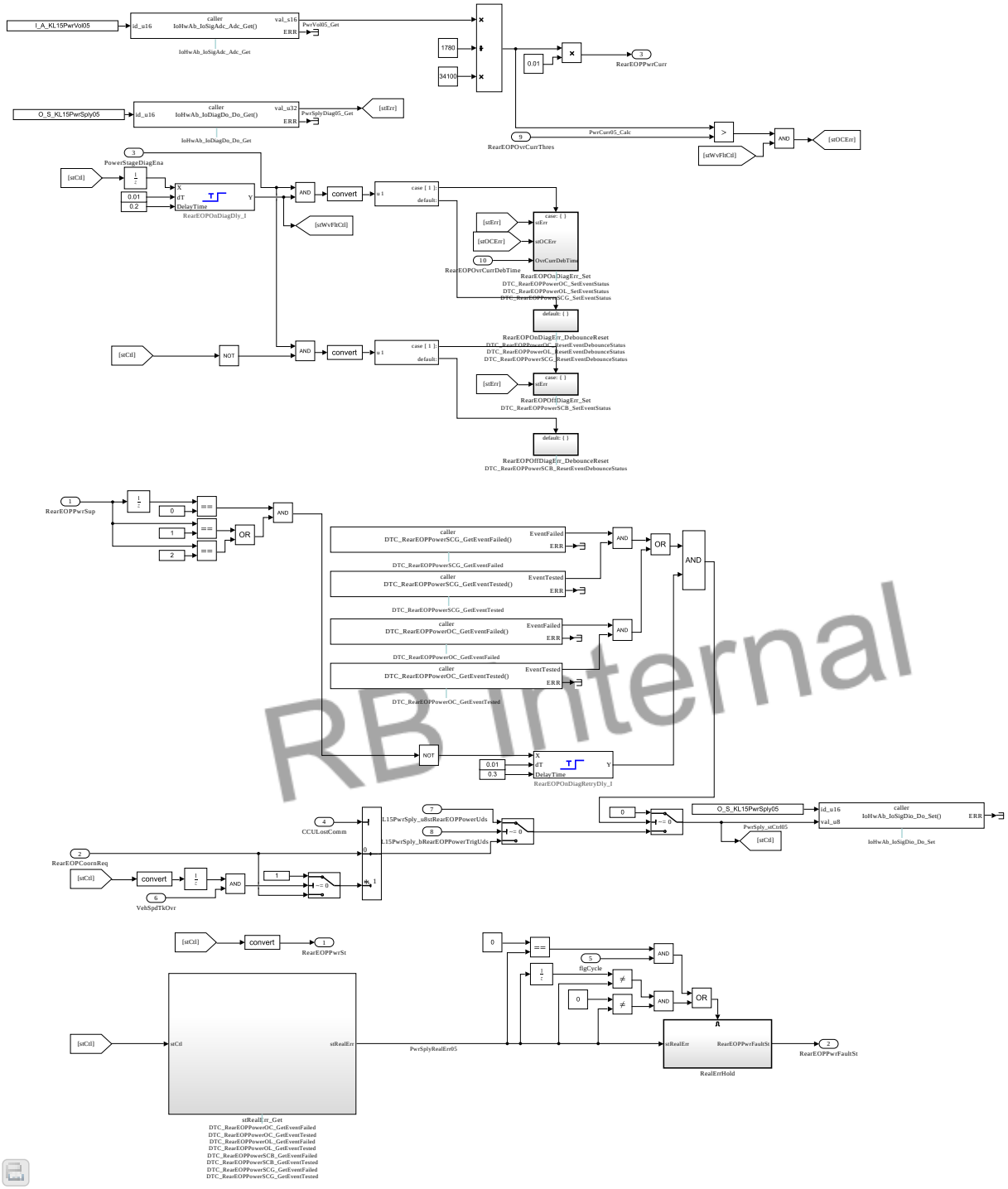

Figure 102 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RealErrHold]

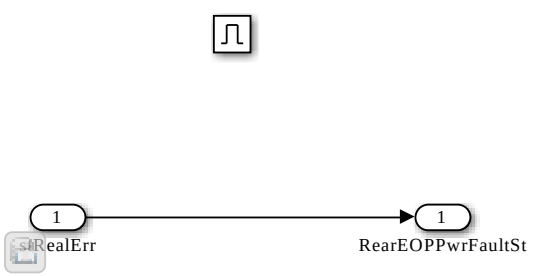

Figure 103 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOffDiagErr_DebounceReset]

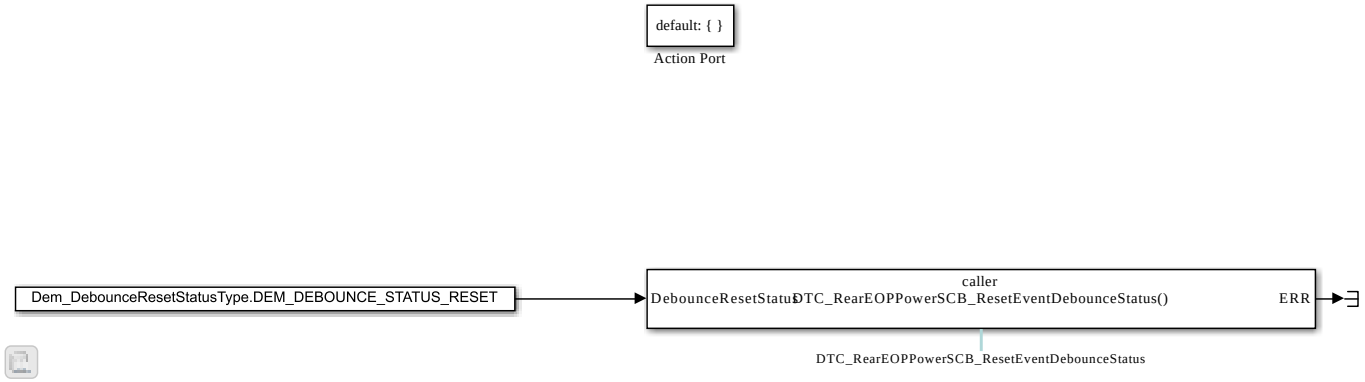

Figure 104 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOffDiagErr_Set]

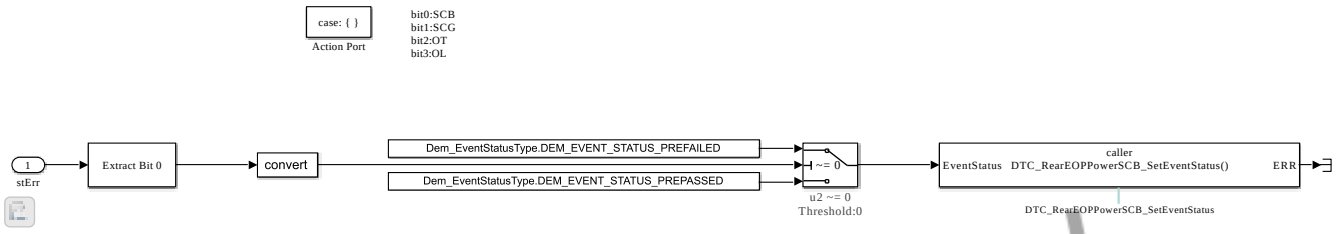

Figure 105 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOnDiagErr_DebounceReset]

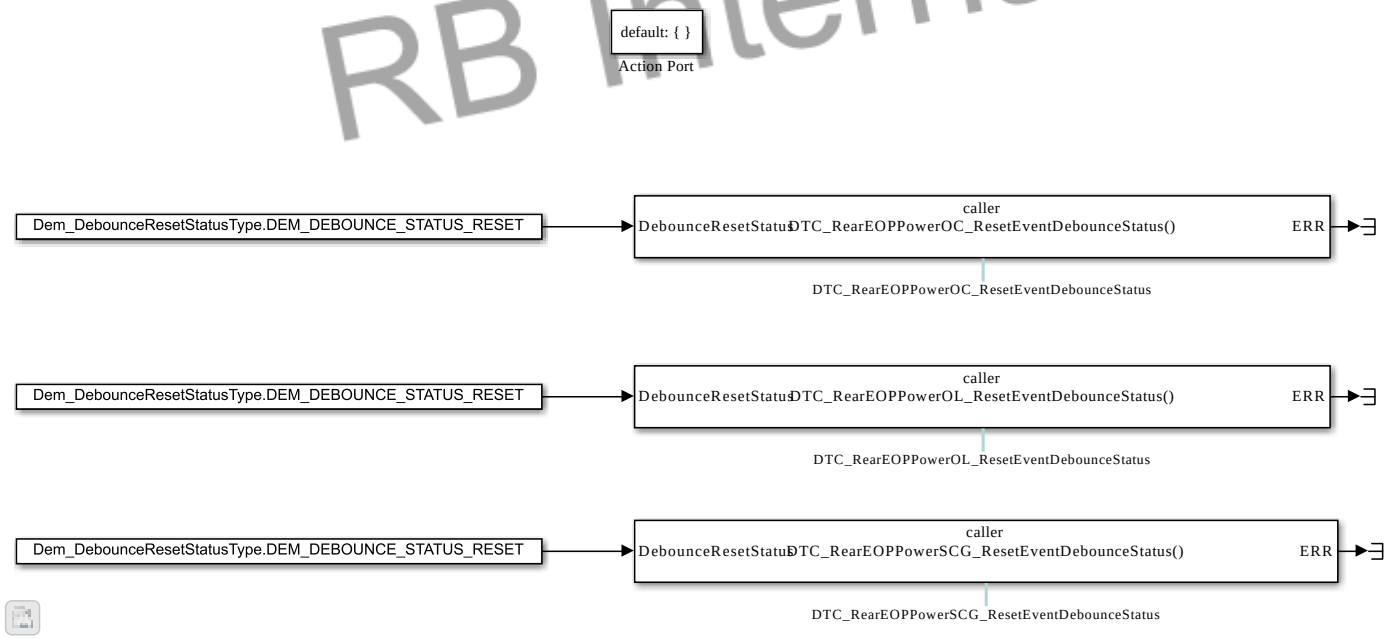

Figure 106 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_RearEOPOnDiagErr_Set]

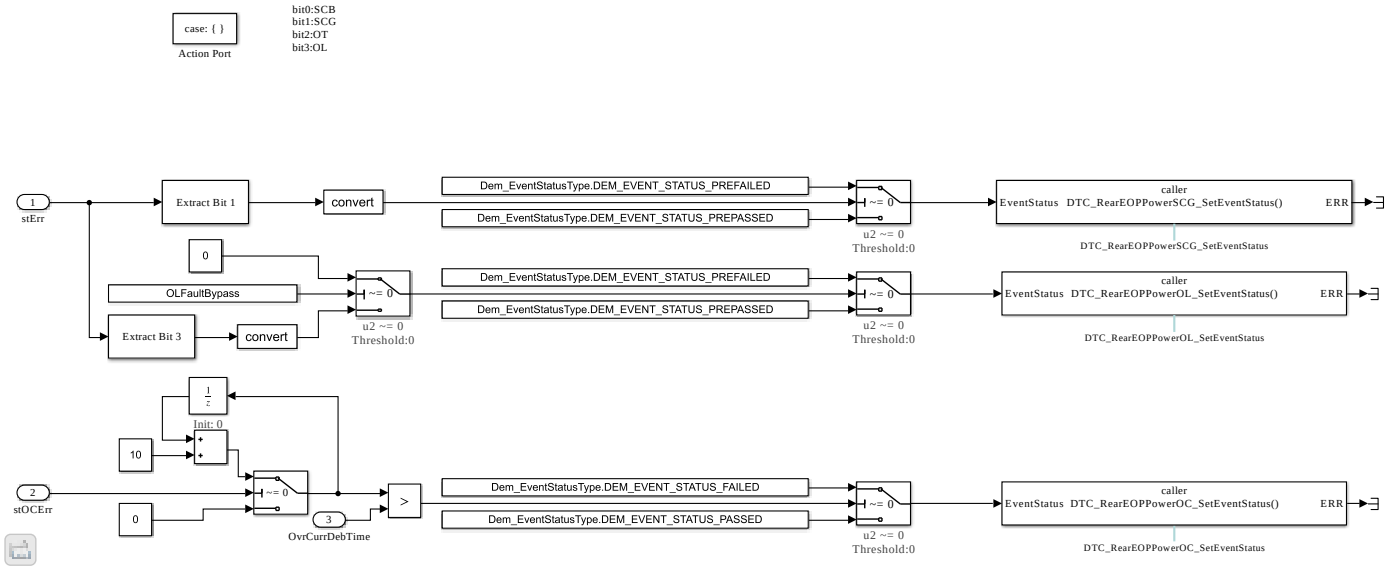

Figure 107 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearEOP_Ctl_2F_Diag_CutOff_stRealErr_Get]

0=Normal
1=Circuit short to ground
2=Circuit short to battery
3=Open
4=Overpower
5~7=Reserve

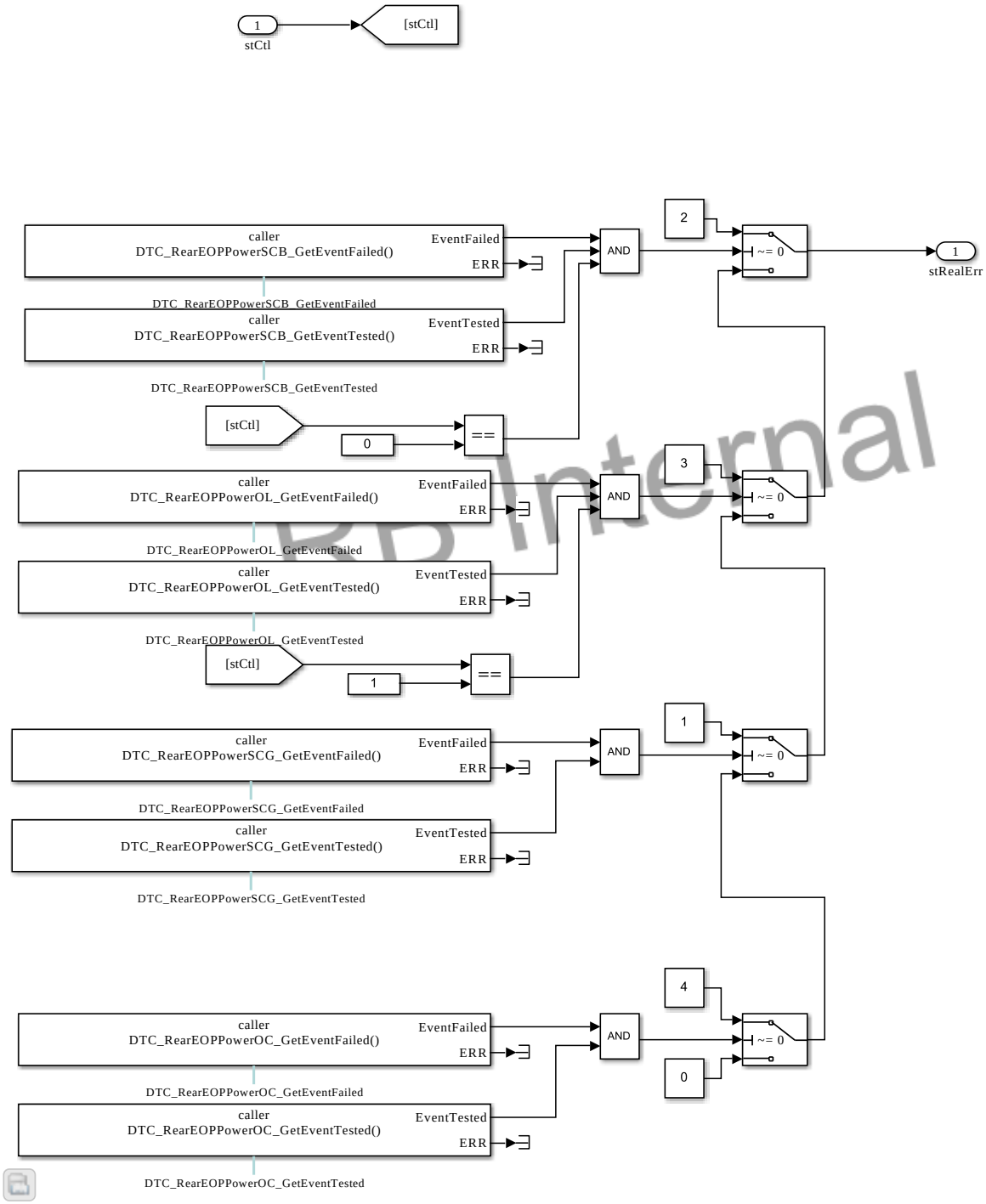

Figure 108 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotorAllwSleepDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotorAllwSleepDly]

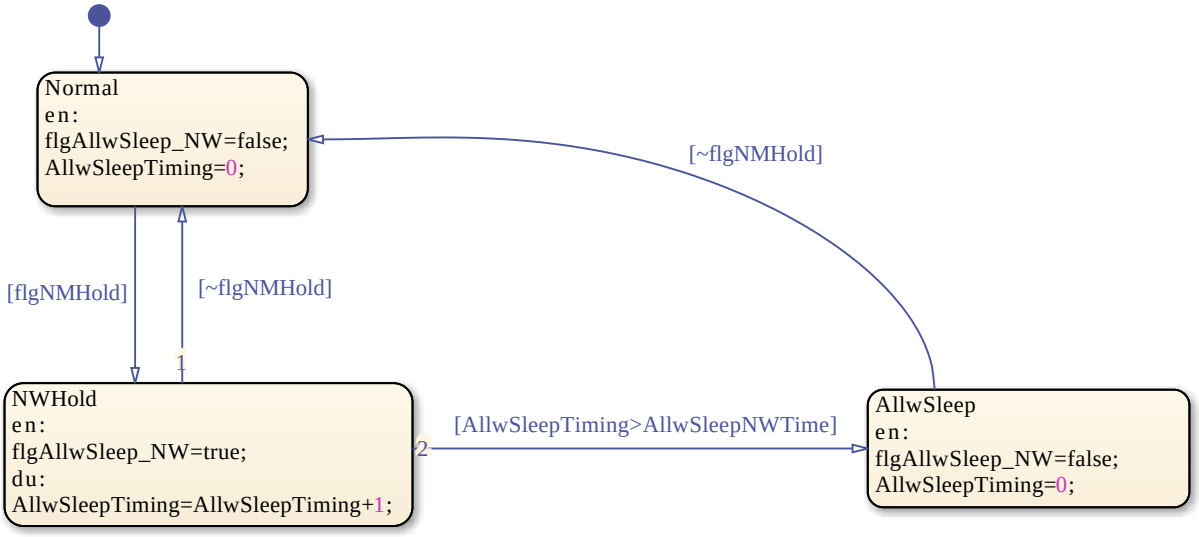

Figure 109 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotorWkUpDly [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotorWkUpDly]

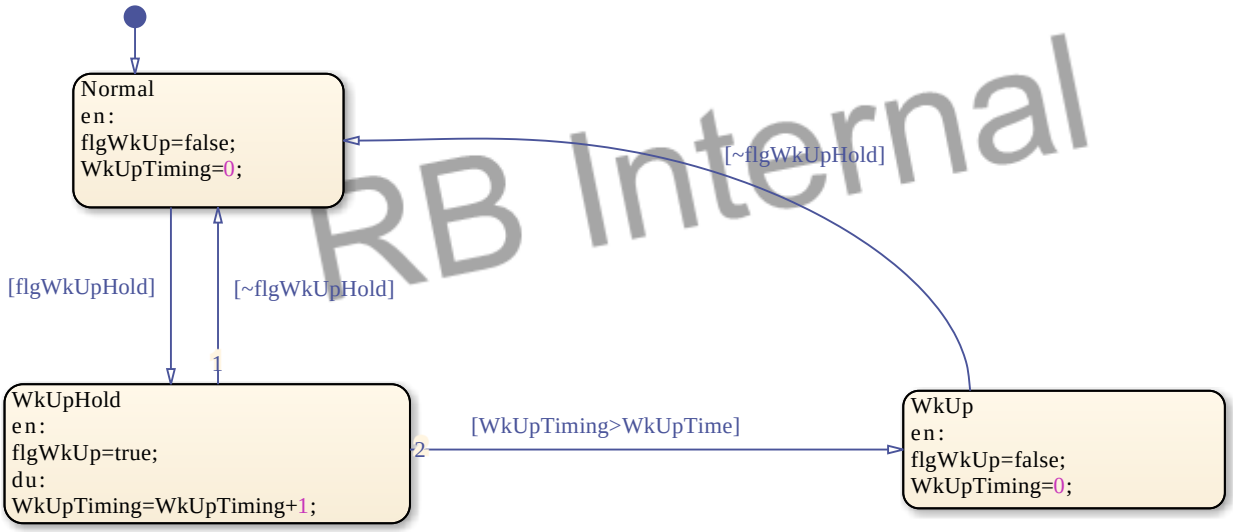

Figure 110 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff]

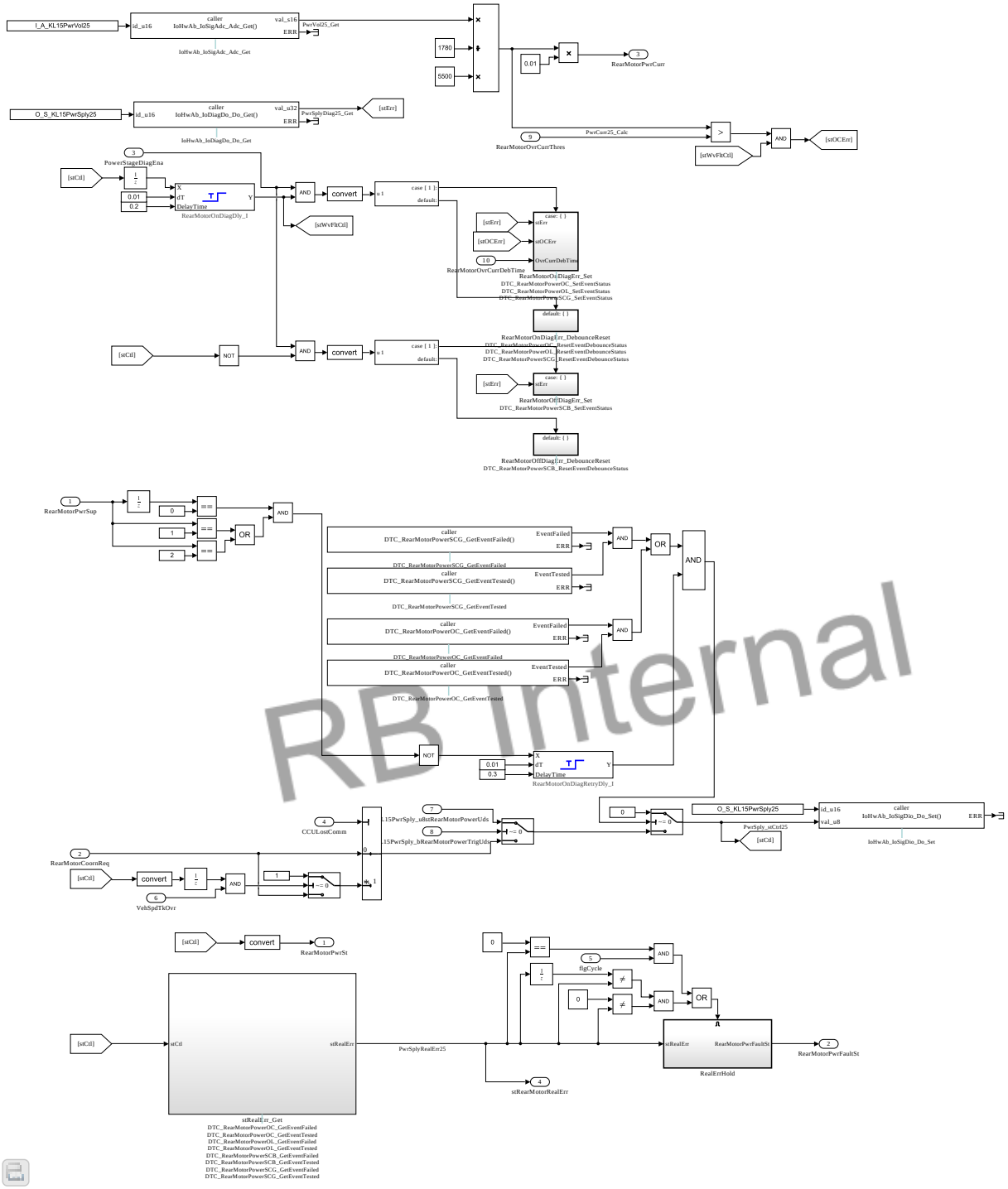

Figure 111 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RealErrHold]

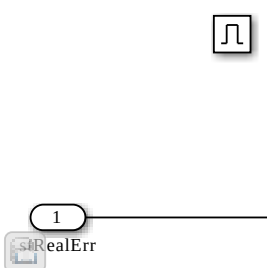

Figure 112 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOffDiagErr_DebounceReset]

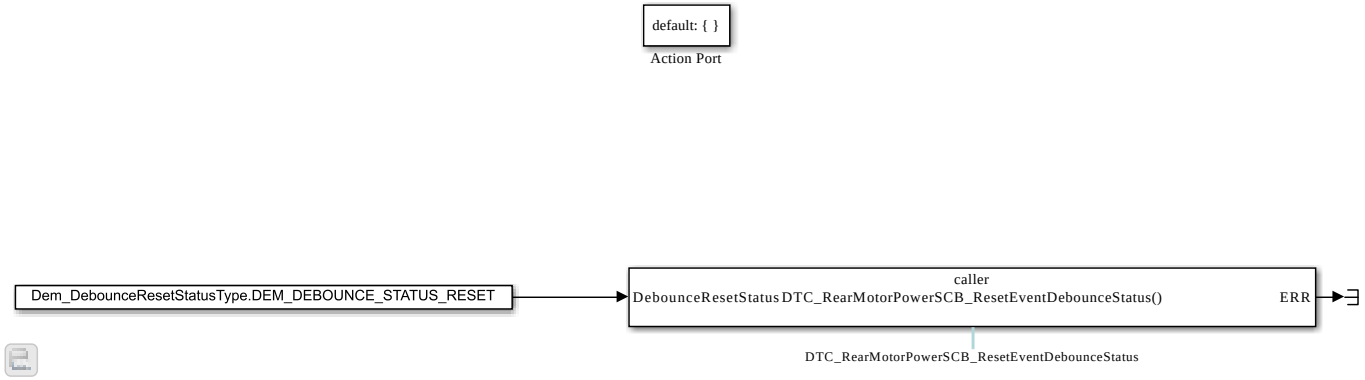

Figure 113 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOffDiagErr_Set]

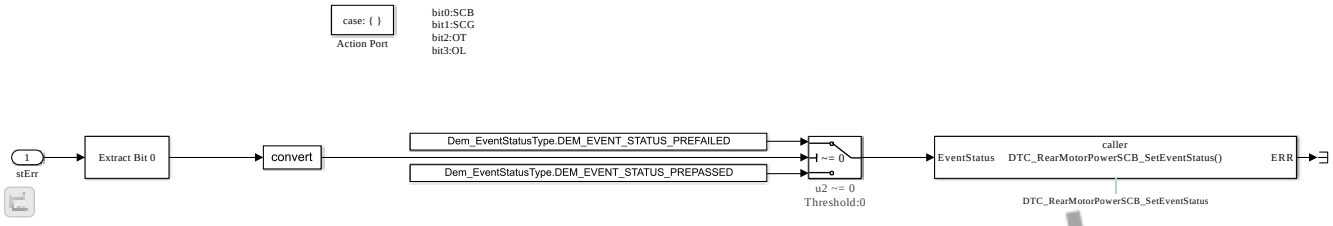

Figure 114 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOnDiagErr_DebounceReset]

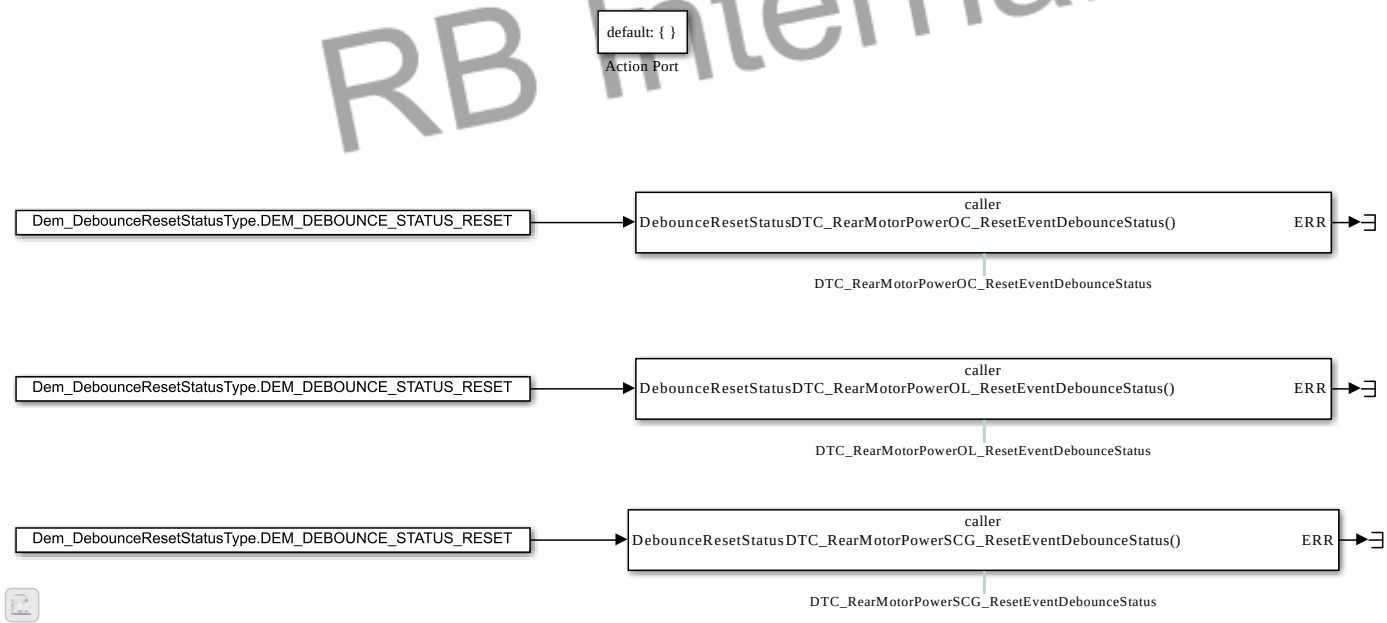

Figure 115 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_RearMotorOnDiagErr_Set]

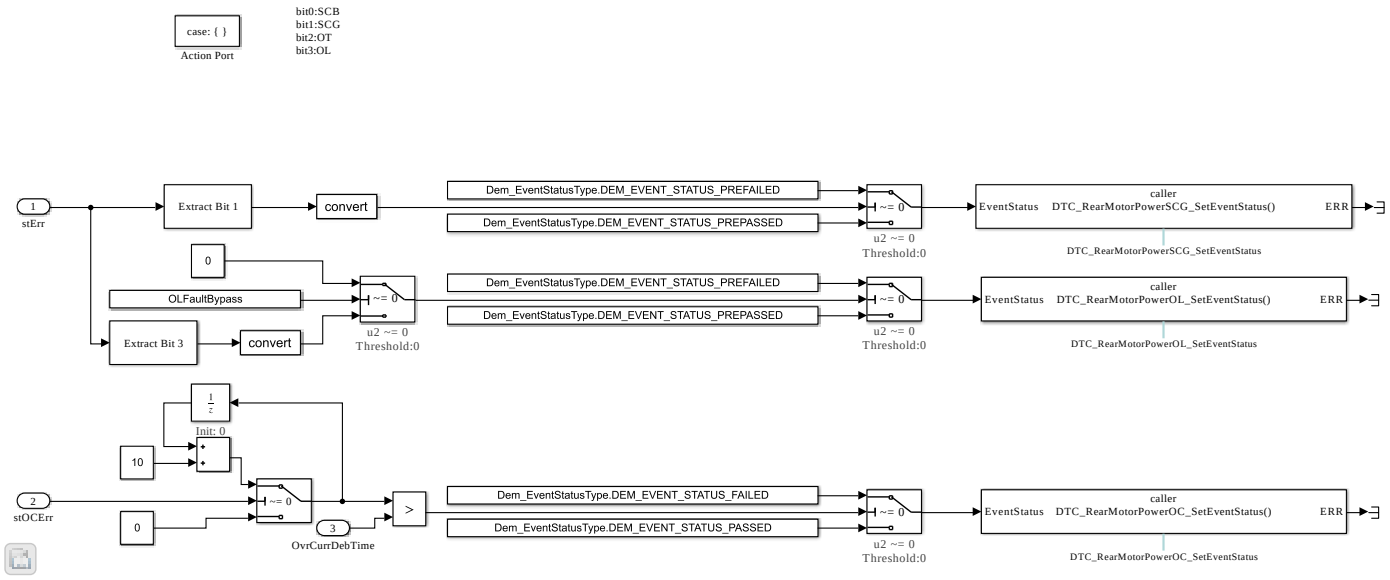

Figure 116 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearMotor_Ctl_2F_Diag_CutOff_stRealErr_Get]

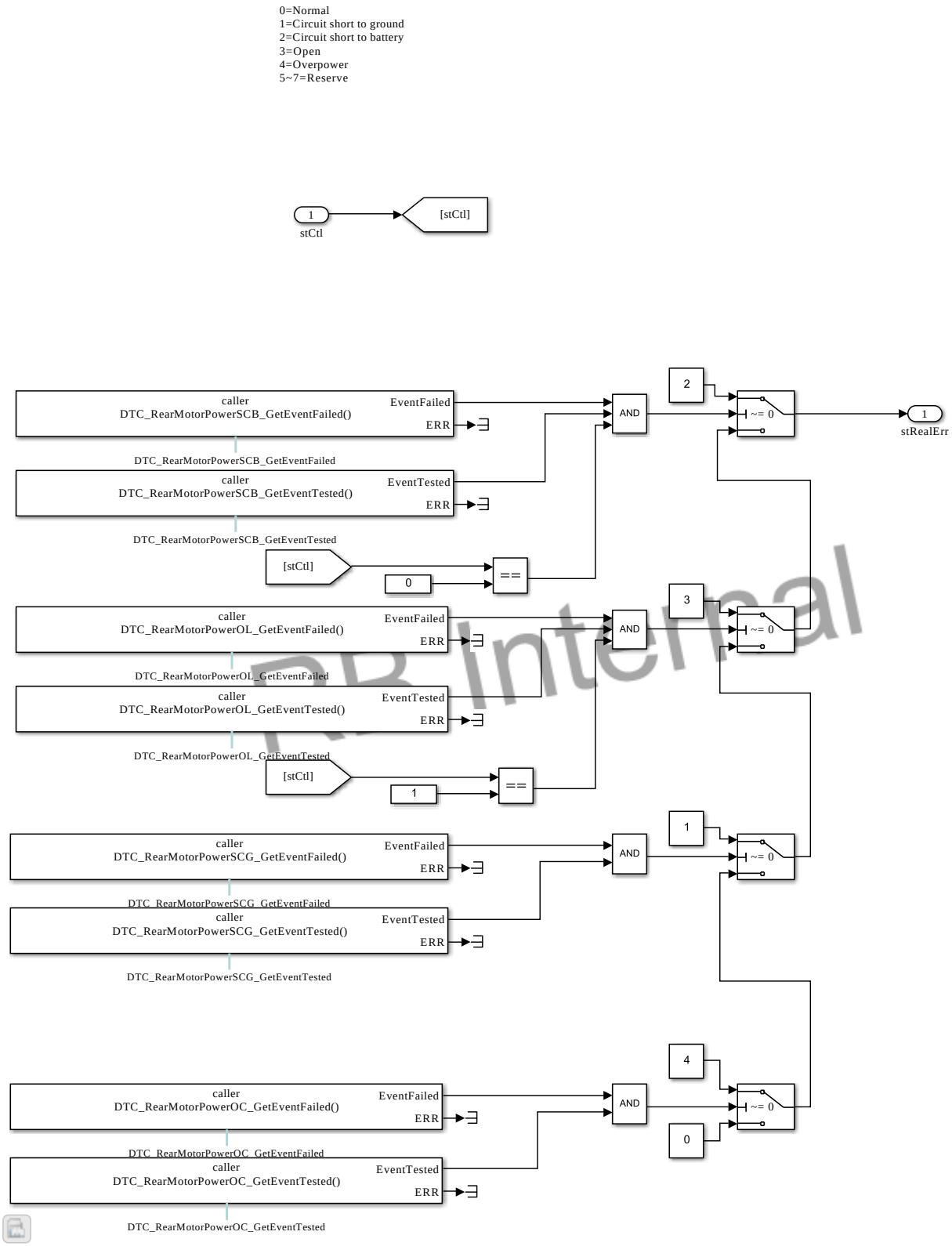

Figure 117 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff]

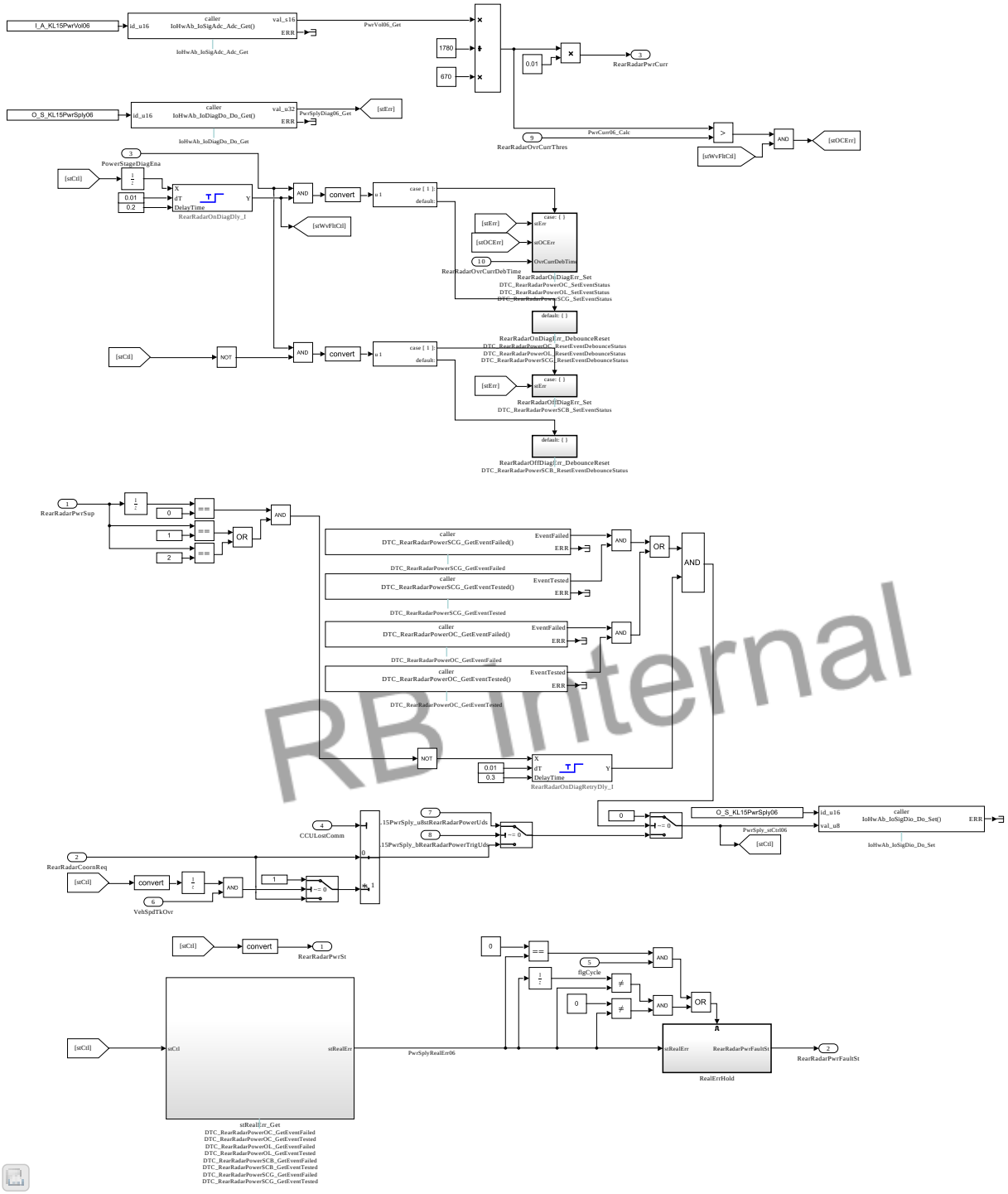

Figure 118 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RealErrHold [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RealErrHold]

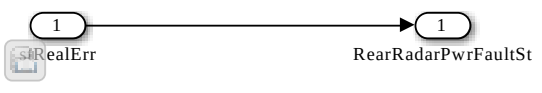

Figure 119 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOffDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOffDiagErr_DebounceReset]

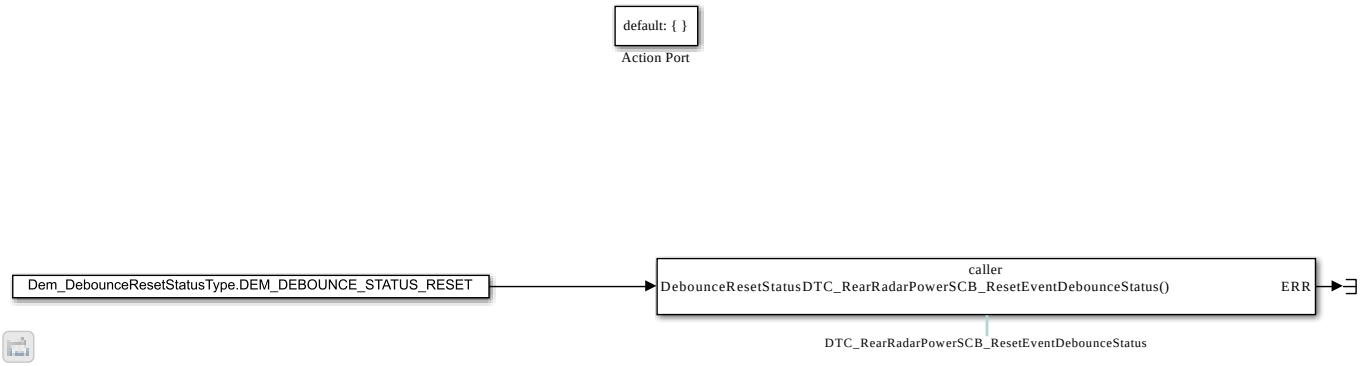

Figure 120 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOffDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOffDiagErr_Set]

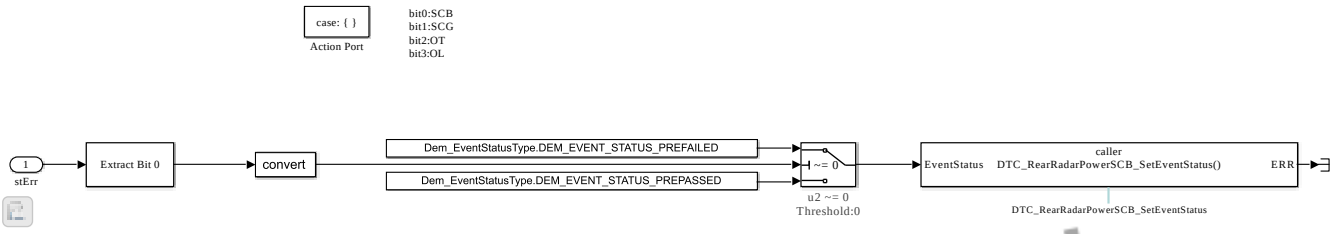

Figure 121 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOnDiagErr_DebounceReset [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOnDiagErr_DebounceReset]

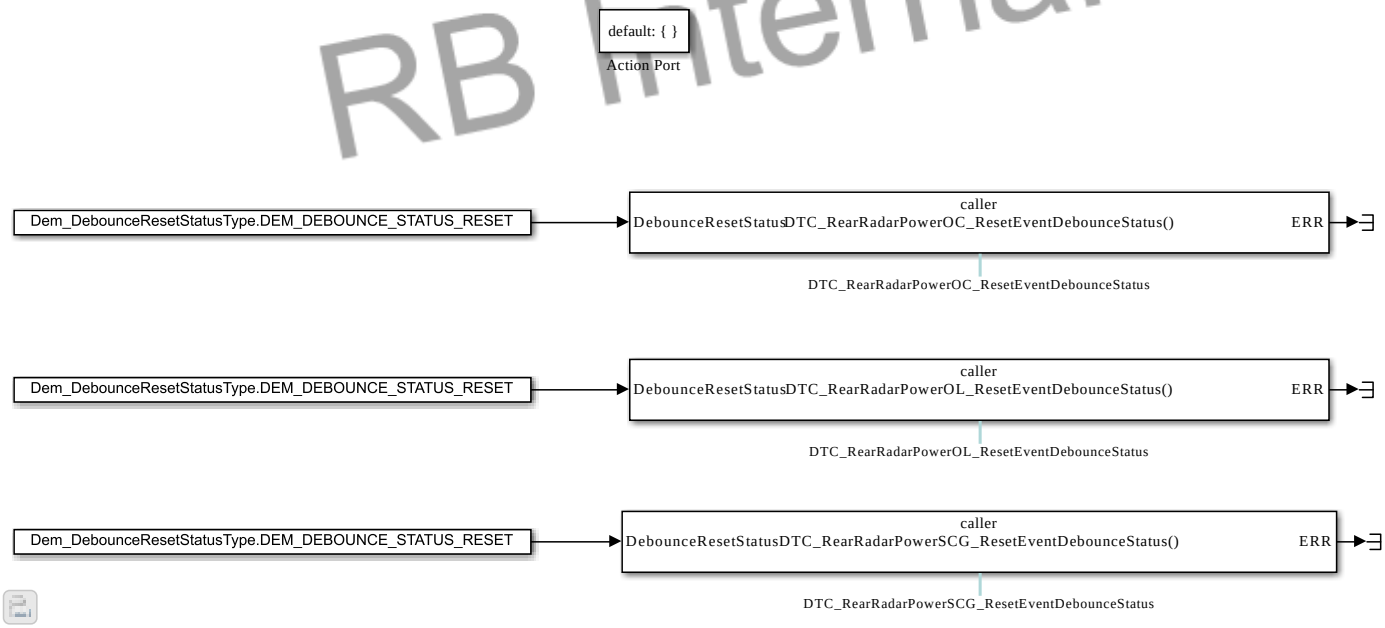

Figure 122 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOnDiagErr_Set [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_RearRadarOnDiagErr_Set]

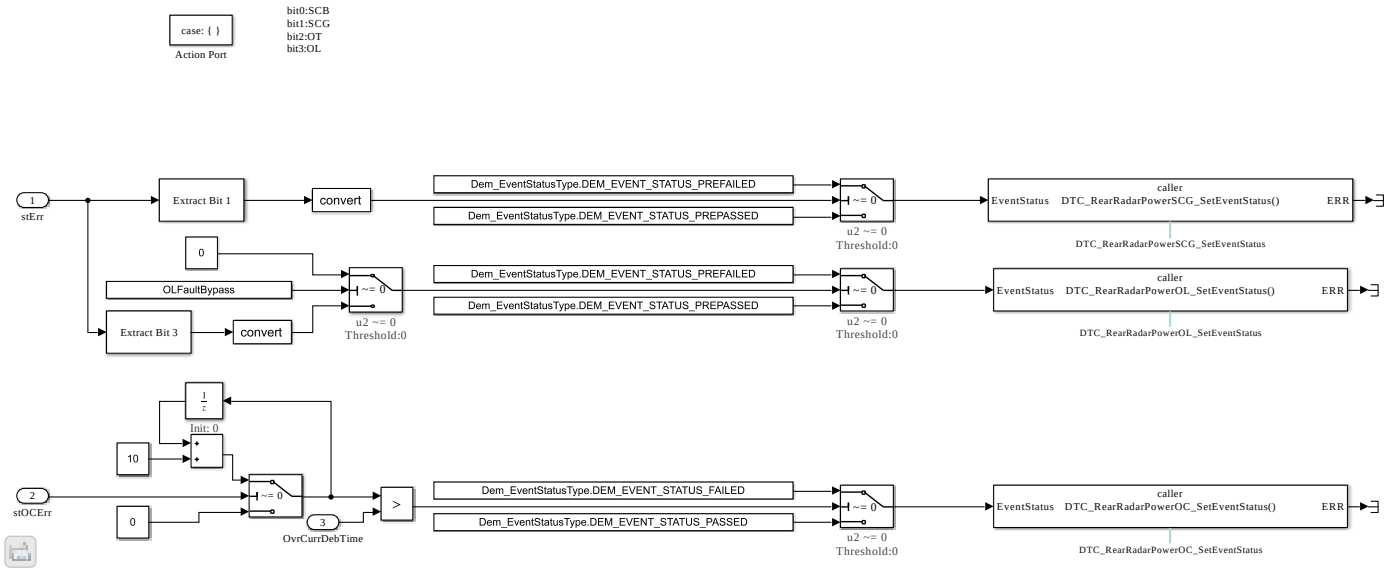

Figure 123 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_stRealErr_Get [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_10ms_sys_RearRadar_Ctl_2F_Diag_CutOff_stRealErr_Get]

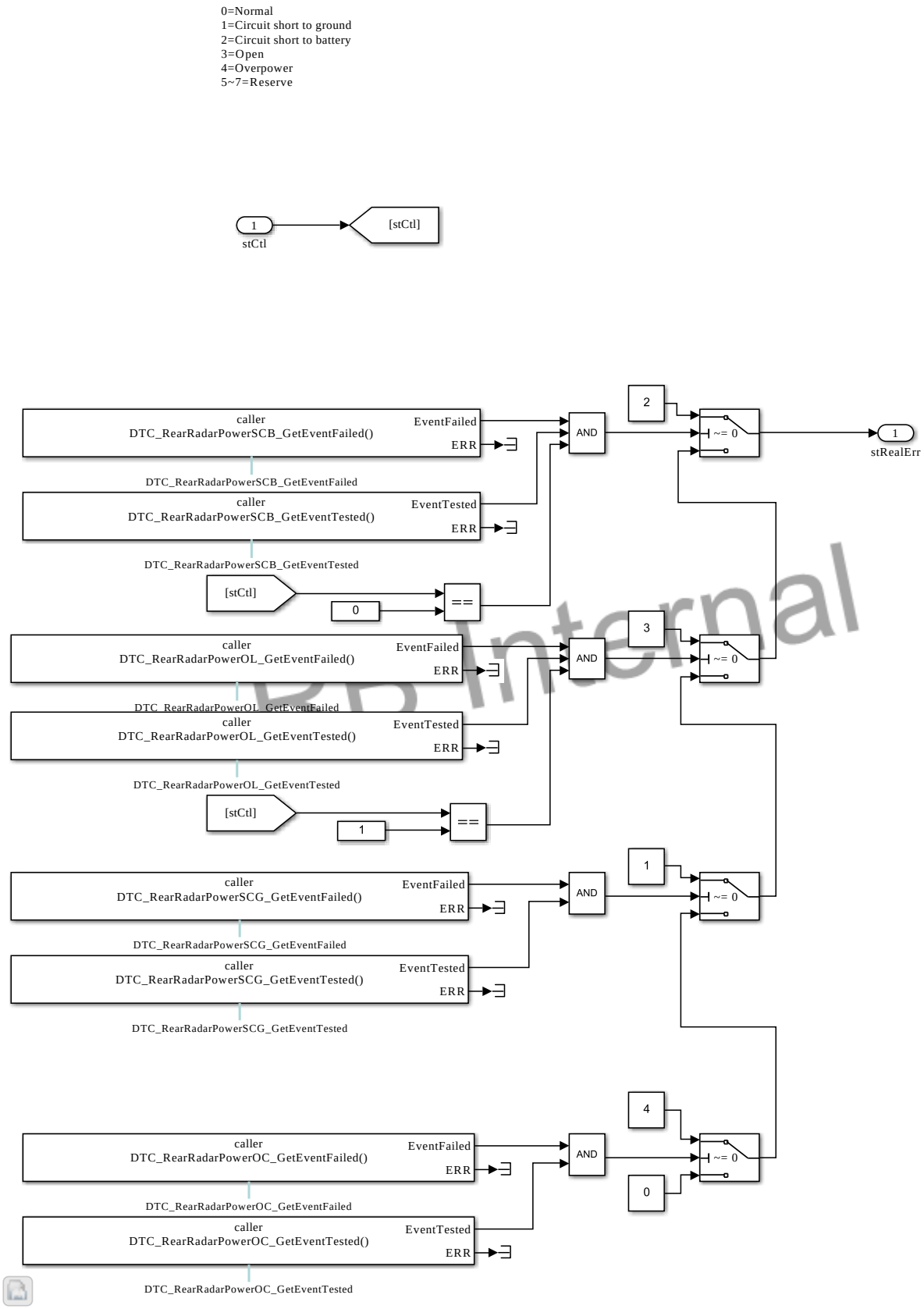

Figure 124 KL15PwrSply_DD_KL15PwrSply_DD_Runnable_Init [KL15PwrSply_DD_KL15PwrSply_DD_Runnable_Init]

Table 1 Data Types for port interfaces [PortInterfaceDataTypes]

Port	AccessMode	Interface	DE	Datatype
BattU_bVoltEnbPinDiag	ImplicitReceive	BattU_bVoltEnbPinDiag	Val	boolean
gPRM_au16OvrCurrDebTime	ImplicitReceive	gPRM_au16OvrCurrDebTime	Val	uint16
gPRM_au16OvrCurrThres	ImplicitReceive	gPRM_au16OvrCurrThres	Val	uint16

Port	AccessMode	Interface	DE	Datatype
gVRM_u16BatteryVolt_mv	ImplicitReceive	gVRM_u16BatteryVolt_mv	Val	uint16
KL15PwrSply_bAMPPower-TrigUds	ImplicitReceive	KL15PwrSply_bAMPPower-TrigUds	Val	boolean
KL15PwrSply_bInverter-PowerTrigUds	ImplicitReceive	KL15PwrSply_bInverter-PowerTrigUds	Val	boolean
KL15PwrS-ply_bKL15T1PowerTrigUds	ImplicitReceive	KL15PwrS-ply_bKL15T1PowerTrigUds	Val	boolean
KL15PwrS-ply_bKL15T2PowerTrigUds	ImplicitReceive	KL15PwrS-ply_bKL15T2PowerTrigUds	Val	boolean
KL15PwrSply_bPCUPower-TrigUds	ImplicitReceive	KL15PwrSply_bPCUPower-TrigUds	Val	boolean
KL15PwrSply_bRear12V-PowerTrigUds	ImplicitReceive	KL15PwrSply_bRear12V-PowerTrigUds	Val	boolean
KL15PwrSply_bRearBlower-PowerTrigUds	ImplicitReceive	KL15PwrSply_bRearBlower-PowerTrigUds	Val	boolean
KL15PwrSply_bRearCtrlPa-nelPowerTrigUds	ImplicitReceive	KL15PwrSply_bRearCtrlPa-nelPowerTrigUds	Val	boolean
KL15PwrSply_bRearEOP-PowerTrigUds	ImplicitReceive	KL15PwrSply_bRearEOP-PowerTrigUds	Val	boolean
KL15PwrSply_bRearMotor-PowerTrigUds	ImplicitReceive	KL15PwrSply_bRearMotor-PowerTrigUds	Val	boolean
KL15PwrSply_bRearRadar-PowerTrigUds	ImplicitReceive	KL15PwrSply_bRearRadar-PowerTrigUds	Val	boolean
KL15PwrSply_bRLALampC-tlUnitPowerTrigUds	ImplicitReceive	KL15PwrSply_bRLALampC-tlUnitPowerTrigUds	Val	boolean
KL15PwrSply_bRLBLampC-tlUnitPowerTrigUds	ImplicitReceive	KL15PwrSply_bRLBLampC-tlUnitPowerTrigUds	Val	boolean
KL15PwrSply_bRMPowerTri-gUds	ImplicitReceive	KL15PwrSply_bRMPowerTri-gUds	Val	boolean
KL15PwrSply_bRRALampC-tlUnitPowerTrigUds	ImplicitReceive	KL15PwrSply_bRRALampC-tlUnitPowerTrigUds	Val	boolean
KL15PwrSply_bRRBLampC-tlUnitPowerTrigUds	ImplicitReceive	KL15PwrSply_bRRBLampC-tlUnitPowerTrigUds	Val	boolean
KL15PwrSply_u8stAMP-PowerUds	ImplicitReceive	KL15PwrSply_u8stAMP-PowerUds	Val	uint8
KL15PwrSply_u8stInverter-PowerUds	ImplicitReceive	KL15PwrSply_u8stInverter-PowerUds	Val	uint8
KL15PwrSply_u8st-KL15T1PowerUds	ImplicitReceive	KL15PwrSply_u8st-KL15T1PowerUds	Val	uint8
KL15PwrSply_u8st-KL15T2PowerUds	ImplicitReceive	KL15PwrSply_u8st-KL15T2PowerUds	Val	uint8
KL15PwrSply_u8stPCU-PowerUds	ImplicitReceive	KL15PwrSply_u8stPCU-PowerUds	Val	uint8
KL15PwrSply_u8stRear12V-PowerUds	ImplicitReceive	KL15PwrSply_u8stRear12V-PowerUds	Val	uint8
KL15PwrSply_u8stRear-BlowerPowerUds	ImplicitReceive	KL15PwrSply_u8stRear-BlowerPowerUds	Val	uint8
KL15PwrSply_u8stRearCtrl-PanelPowerUds	ImplicitReceive	KL15PwrSply_u8stRearCtrl-PanelPowerUds	Val	uint8
KL15PwrSply_u8stRearEOP-PowerUds	ImplicitReceive	KL15PwrSply_u8stRearEOP-PowerUds	Val	uint8
KL15PwrSply_u8stRearMo-torPowerUds	ImplicitReceive	KL15PwrSply_u8stRearMo-torPowerUds	Val	uint8
KL15PwrSply_u8stRearRa-darPowerUds	ImplicitReceive	KL15PwrSply_u8stRearRa-darPowerUds	Val	uint8
KL15PwrSply_u8stRLALam-pCtlUnitPowerUds	ImplicitReceive	KL15PwrSply_u8stRLALam-pCtlUnitPowerUds	Val	uint8

Port	AccessMode	Interface	DE	Datatype
KL15PwrSply_u8stRLBLampCtlUnitPowerUds	ImplicitReceive	KL15PwrSply_u8stRLBLampCtlUnitPowerUds	Val	uint8
KL15PwrSply_u8stRMPowerUds	ImplicitReceive	KL15PwrSply_u8stRMPowerUds	Val	uint8
KL15PwrSply_u8stRRALampCtlUnitPowerUds	ImplicitReceive	KL15PwrSply_u8stRRALampCtlUnitPowerUds	Val	uint8
KL15PwrSply_u8stRRBLampCtlUnitPowerUds	ImplicitReceive	KL15PwrSply_u8stRRBLampCtlUnitPowerUds	Val	uint8
OilPmp_stReqIn	ImplicitReceive	OilPmp_stReqIn	Val	uint8
PwrSply_au8CoornReq	ImplicitReceive	PwrSply_au8CoornReq	Val	uint8
PwrSply_au8PwrSup	ImplicitReceive	PwrSply_au8PwrSup	Val	uint8
PwrSply_bCCULostComm	ImplicitReceive	PwrSply_bCCULostComm	Val	boolean
PwrSply_bNetSleepFlag	ImplicitReceive	PwrSply_bNetSleepFlag	Val	boolean
PwrSply_bVehSpdTkOvr	ImplicitReceive	PwrSply_bVehSpdTkOvr	Val	boolean
PwrSply_u8UMM_UsageModeSt	ImplicitReceive	PwrSply_u8UMM_UsageModeSt	Val	uint8
PwrSply_u8VehicleTypeCfg	ImplicitReceive	PwrSply_u8VehicleTypeCfg	Val	uint8
PwrSply_u8VMM_OTAModeSt	ImplicitReceive	PwrSply_u8VMM_OTAModeSt	Val	uint8
PwrSply_bPositionLightCtrlCmd01	ImplicitReceive	PwrSply_bPositionLightCtrlCmd01	Val	boolean
PwrSply_bPositionLightCtrlCmd02	ImplicitReceive	PwrSply_bPositionLightCtrlCmd02	Val	boolean
PwrSply_bPositionLightCtrlCmd03	ImplicitReceive	PwrSply_bPositionLightCtrlCmd03	Val	boolean
PwrSply_bPositionLightCtrlCmd04	ImplicitReceive	PwrSply_bPositionLightCtrlCmd04	Val	boolean
PwrSply_bPositionLightTimingOFFEnableCfg	ImplicitReceive	PwrSply_bPositionLightTimingOFFEnableCfg	Val	boolean
gKL15PwrSply_u32AllowSleep_Local	ImplicitSend	gKL15PwrSply_u32AllowSleep_Local	Val	uint32
gKL15PwrSply_u32AllowSleep_NW	ImplicitSend	gKL15PwrSply_u32AllowSleep_NW	Val	uint32
gKL15PwrSply_u8Wakeup	ImplicitSend	gKL15PwrSply_u8Wakeup	Val	uint8
PwrSply_abPwrSt	ImplicitSend	PwrSply_abPwrSt	Val	boolean
PwrSply_au16PwrCurr	ImplicitSend	PwrSply_au16PwrCurr	Val	uint16
PwrSply_au8PwrFaultSt	ImplicitSend	PwrSply_au8PwrFaultSt	Val	uint8

3 System Constants - Parameters - Variables - Structures

3.1 Parameters

Table 2 **KL15PwrSply_DD** Autosar Parameters: overview

Name	Detailed name	Mode	Type	Defined in
AMPPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
AMPPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
AMPPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
AMPPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
InverterPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
InverterPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
InverterPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
InverterPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD

Name	Detailed name	Mode	Type	Defined in
KL15T1PwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
KL15T1PwrSply_StrtCur-rIp_C			VALUE	KL15PwrSply_DD
KL15T1PwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
KL15T1PwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
KL15T2PwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
KL15T2PwrSply_StrtCur-rIp_C			VALUE	KL15PwrSply_DD
KL15T2PwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
KL15T2PwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
PCUPwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
PCUPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
PCUPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
PCUPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
PwrSply_EnaOvrCurr-Thres_C			VALUE	KL15PwrSply_DD
PwrSply_EnaTstCurr_C			VALUE	KL15PwrSply_DD
Rear12VPwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
Rear12VPwrSply_StrtCur-rIp_C			VALUE	KL15PwrSply_DD
Rear12VPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
Rear12VPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RearBlowerPwrSply_OvrCur-rThres_C			VALUE	KL15PwrSply_DD
RearBlowerPwrSply_StrtCu-rrIp_C			VALUE	KL15PwrSply_DD
RearBlowerPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RearBlowerPwrSply_TstCur-r_C			VALUE	KL15PwrSply_DD
RearCtrlPwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
RearCtrlPwrSply_StrtCur-rIp_C			VALUE	KL15PwrSply_DD
RearCtrlPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RearCtrlPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RearEOPPwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
RearEOPPwrSply_StrtCur-rIp_C			VALUE	KL15PwrSply_DD
RearEOPPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RearEOPPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RearMotorPwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD
RearMotorPwrSply_StrtCur-rIp_C			VALUE	KL15PwrSply_DD
RearMotorPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RearMotorPwrSply_TstCur-r_C			VALUE	KL15PwrSply_DD
RearRadarPwrSply_OvrCurr-Thres_C			VALUE	KL15PwrSply_DD

Name	Detailed name	Mode	Type	Defined in
RearRadarPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
RearRadarPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RearRadarPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RLALampCtlUnitPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
RLALampCtlUnitPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
RLALampCtlUnitPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RLALampCtlUnitPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RLBLampCtlUnitPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
RLBLampCtlUnitPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
RLBLampCtlUnitPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RLBLampCtlUnitPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RMPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
RMPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
RMPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RMPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RRALampCtlUnitPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
RRALampCtlUnitPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
RRALampCtlUnitPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RRALampCtlUnitPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD
RRBLampCtlUnitPwrSply_OvrCurrThres_C			VALUE	KL15PwrSply_DD
RRBLampCtlUnitPwrSply_StrtCurrIp_C			VALUE	KL15PwrSply_DD
RRBLampCtlUnitPwrSply_TiKtp_C			VALUE	KL15PwrSply_DD
RRBLampCtlUnitPwrSply_TstCurr_C			VALUE	KL15PwrSply_DD

Table 3 **KL15PwrSply_DD** Autosar Parameters: details

Name	Value range coded Value range phys	Quantisation	Conversion	Data type	MaxSst	X-Input X-Axis points	Y-Input Y-Axis points
AMPPwrSply_OvrCurrThres_C							
AMPPwrSply_StrtCurrIp_C							
AMPPwrSply_TiKtp_C							
AMPPwrSply_TstCurr_C							

BOSCH

Name	Value range coded Value range phys	Quantisation	Conversion	Data type	MaxSst	X-Input X-Axis points	Y-Input Y-Axis points
InverterPwrSply_OvrCurrThres_C							
InverterPwrSply_StrtCurrIp_C							
InverterPwrSply_TiKtp_C							
InverterPwrSply_TstCurr_C							
KL15T1PwrSply_OvrCurrThres_C							
KL15T1PwrSply_StrtCurrIp_C							
KL15T1PwrSply_TiKtp_C							
KL15T1PwrSply_TstCurr_C							
KL15T2PwrSply_OvrCurrThres_C							
KL15T2PwrSply_StrtCurrIp_C							
KL15T2PwrSply_TiKtp_C							
KL15T2PwrSply_TstCurr_C							
PCUPwrSply_OvrCurrThres_C							
PCUPwrSply_StrtCurrIp_C							
PCUPwrSply_TiKtp_C							
PCUPwrSply_TstCurr_C							
PwrSply_EnaOvrCurrThres_C							
PwrSply_EnaTstCurr_C							
Rear12VPwrSply_OvrCurrThres_C							
Rear12VPwrSply_StrtCurrIp_C							
Rear12VPwrSply_TiKtp_C							
Rear12VPwrSply_TstCurr_C							
RearBlowerPwrSply_OvrCurrThres_C							
RearBlowerPwrSply_StrtCurrIp_C							
RearBlowerPwrSply_TiKtp_C							

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Name	Value range coded Value range phys	Quantisation	Conversion	Data type	MaxSst	X-Input X-Axis points	Y-Input Y-Axis points
RearBlowerPwrSply_TstCurr_C							
RearCtrlPwrSply_OvrCurrThres_C							
RearCtrlPwrSply_StrtCurrIp_C							
RearCtrlPwrSply_TiKtp_C							
RearCtrlPwrSply_TstCurr_C							
RearEOPPwrSply_OvrCurrThres_C							
RearEOPPwrSply_StrtCurrIp_C							
RearEOPPwrSply_TiKtp_C							
RearEOPPwrSply_TstCurr_C							
RearMotorPwrSply_OvrCurrThres_C							
RearMotorPwrSply_StrtCurrIp_C							
RearMotorPwrSply_TiKtp_C							
RearMotorPwrSply_TstCurr_C							
RearRadarPwrSply_OvrCurrThres_C							
RearRadarPwrSply_StrtCurrIp_C							
RearRadarPwrSply_TiKtp_C							
RearRadarPwrSply_TstCurr_C							
RLALampCtlUnitPwrSply_OvrCurrThres_C							
RLALampCtlUnitPwrSply_StrtCurrIp_C							
RLALampCtlUnitPwrSply_TiKtp_C							
RLALampCtlUnitPwrSply_TstCurr_C							
RLBLampCtlUnitPwrSply_OvrCurrThres_C							
RLBLampCtlUnitPwrSply_StrtCurrIp_C							
RLBLampCtlUnitPwrSply_TiKtp_C							

Name	Value range coded Value range phys	Quantisation	Conversion	Data type	MaxSst	X-Input X-Axis points	Y-Input Y-Axis points
RLBLampCtlUnitPwrSply_TstCurr_C							
RMPwrSply_OvrCurrThres_C							
RMPwrSply_StrtCurrIp_C							
RMPwrSply_TiKtp_C							
RMPwrSply_TstCurr_C							
RRALampCtlUnitPwrSply_OvrCurrThres_C							
RRALampCtlUnitPwrSply_StrtCurrIp_C							
RRALampCtlUnitPwrSply_TiKtp_C							
RRALampCtlUnitPwrSply_TstCurr_C							
RRBLampCtlUnitPwrSply_OvrCurrThres_C							
RRBLampCtlUnitPwrSply_StrtCurrIp_C							
RRBLampCtlUnitPwrSply_TiKtp_C							
RRBLampCtlUnitPwrSply_TstCurr_C							

3.2 Variables

Table 4 **KL15PwrSply_DD** Autosar Variables: overview

Name	Detailed name	Mode	Type	Defined in
KL15PwrS_flgRLALampAlwSleep_NW			VALUE	KL15PwrSply_DD
KL15PwrS_flgRLBLampAlwSleep_NW			VALUE	KL15PwrSply_DD
KL15PwrS_flgRRALampAlwSleep_NW			VALUE	KL15PwrSply_DD
KL15PwrS_flgRRBLampAlwSleep_NW			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSplyDiag01_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSplyDiag02_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSplyDiag03_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSplyDiag04_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSplyDiag05_Get			VALUE	KL15PwrSply_DD

Name	Detailed name	Mode	Type	Defined in
KL15PwrSply_D_PwrSply-Diag06_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag07_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag08_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag09_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag10_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag11_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag12_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag25_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag26_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag27_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag28_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_D_PwrSply-Diag29_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_flgRear-MotorWkUp			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_flgRLA-LampWkUp			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_flgRL-BLampWkUp			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_flgRRA-LampWkUp			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_flgRR-BLampWkUp			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r01_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r02_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r03_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r04_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r05_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r06_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r07_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r08_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r09_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r10_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r11_Calc			VALUE	KL15PwrSply_DD

Name	Detailed name	Mode	Type	Defined in
KL15PwrSply_DD_PwrCur-r12_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r25_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r26_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r27_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r28_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrCur-r29_Calc			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl01			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl02			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl03			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl04			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl05			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl06			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl07			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl08			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl09			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl10			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl11			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl12			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl25			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl26			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl27			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl28			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrS-ply_stCtrl29			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr01			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr02			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr03			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr04			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr05			VALUE	KL15PwrSply_DD

Name	Detailed name	Mode	Type	Defined in
KL15PwrSply_DD_PwrSply-RealErr06			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr07			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr08			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr09			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr10			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr11			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr12			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr25			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr26			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr27			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr28			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrSply-RealErr29			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol01_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol02_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol03_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol04_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol05_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol06_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol07_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol08_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol09_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol10_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol11_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol12_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol25_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol26_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol27_Get			VALUE	KL15PwrSply_DD
KL15PwrSply_DD_PwrVol28_Get			VALUE	KL15PwrSply_DD

Name	Detailed name	Mode	Type	Defined in
KL15PwrSply_DD_PwrVol29_Get			VALUE	KL15PwrSply_DD
KL_flgRearMotorLampAllwSleep_NW			VALUE	KL15PwrSply_DD

Table 5 **KL15PwrSply_DD** Autosar Variables: details

Name	Impl. type	Value range coded Value range phys	Quantisation	Conversion Bit base (BB)	Data type BB type	Ele# Bit#	AddrMethod Bit pos.
KL15PwrS_flgRLALampAllwSleep_NW	STANDARD						
KL15PwrS_flgRLBLampAllwSleep_NW	STANDARD						
KL15PwrS_flgRRALampAllwSleep_NW	STANDARD						
KL15PwrS_flgRRBLampAllwSleep_NW	STANDARD						
KL15PwrSply_D_PwrSplyDiag01_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag02_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag03_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag04_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag05_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag06_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag07_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag08_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag09_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag10_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag11_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag12_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag25_Get	STANDARD						

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Name	Impl. type	Value range coded Value range phys	Quantisation	Conversion Bit base (BB)	Data type BB type	Ele# Bit#	AddrMethod Bit pos.
KL15PwrSply_D_PwrSplyDiag26_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag27_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag28_Get	STANDARD						
KL15PwrSply_D_PwrSplyDiag29_Get	STANDARD						
KL15PwrSply_DD_flgRearMotorWkUp	STANDARD						
KL15PwrSply_DD_flgRLALampWkUp	STANDARD						
KL15PwrSply_DD_flgRLBLampWkUp	STANDARD						
KL15PwrSply_DD_flgRRALampWkUp	STANDARD						
KL15PwrSply_DD_flgRRBLampWkUp	STANDARD						
KL15PwrSply_DD_PwrCurr01_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr02_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr03_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr04_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr05_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr06_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr07_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr08_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr09_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr10_Calc	STANDARD						
KL15PwrSply_DD_PwrCurr11_Calc	STANDARD						

Name	Impl. type	Value range coded Value range phys	Quantisation	Conversion Bit base (BB)	Data type BB type	Ele# Bit#	AddrMethod Bit pos.
KL15PwrS-ply_DD_PwrCur-r12_Calc	STANDARD						
KL15PwrS-ply_DD_PwrCur-r25_Calc	STANDARD						
KL15PwrS-ply_DD_PwrCur-r26_Calc	STANDARD						
KL15PwrS-ply_DD_PwrCur-r27_Calc	STANDARD						
KL15PwrS-ply_DD_PwrCur-r28_Calc	STANDARD						
KL15PwrS-ply_DD_PwrCur-r29_Calc	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl01	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl02	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl03	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl04	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl05	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl06	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl07	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl08	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl09	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl10	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl11	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl12	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl25	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl26	STANDARD						

Name	Impl. type	Value range coded Value range phys	Quantisation	Conversion Bit base (BB)	Data type BB type	Ele# Bit#	AddrMethod Bit pos.
KL15PwrS-ply_DD_PwrS-ply_stCtrl27	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl28	STANDARD						
KL15PwrS-ply_DD_PwrS-ply_stCtrl29	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr01	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr02	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr03	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr04	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr05	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr06	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr07	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr08	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr09	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr10	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr11	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr12	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr25	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr26	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr27	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr28	STANDARD						
KL15PwrS-ply_DD_PwrSply-RealErr29	STANDARD						

Name	Impl. type	Value range coded Value range phys	Quantisation	Conversion Bit base (BB)	Data type BB type	Ele# Bit#	AddrMethod Bit pos.
KL15PwrSply_DD_PwrVol01_Get	STANDARD						
KL15PwrSply_DD_PwrVol02_Get	STANDARD						
KL15PwrSply_DD_PwrVol03_Get	STANDARD						
KL15PwrSply_DD_PwrVol04_Get	STANDARD						
KL15PwrSply_DD_PwrVol05_Get	STANDARD						
KL15PwrSply_DD_PwrVol06_Get	STANDARD						
KL15PwrSply_DD_PwrVol07_Get	STANDARD						
KL15PwrSply_DD_PwrVol08_Get	STANDARD						
KL15PwrSply_DD_PwrVol09_Get	STANDARD						
KL15PwrSply_DD_PwrVol10_Get	STANDARD						
KL15PwrSply_DD_PwrVol11_Get	STANDARD						
KL15PwrSply_DD_PwrVol12_Get	STANDARD						
KL15PwrSply_DD_PwrVol25_Get	STANDARD						
KL15PwrSply_DD_PwrVol26_Get	STANDARD						
KL15PwrSply_DD_PwrVol27_Get	STANDARD						
KL15PwrSply_DD_PwrVol28_Get	STANDARD						
KL15PwrSply_DD_PwrVol29_Get	STANDARD						
KL_flgRearMotorLampAllwSleep_NW	STANDARD						

4 Conversion forms

Table 6 Conversion forms

Name	Category	Unit	Contents int
CM_Curr_Ampr	LINEAR	Curr_A	
CM_boolean	TEXTTABLE	NoUnit	(FALSE,0),(TRUE,1)

Name	Category	Unit	Contents int
CM_q1Em6	LINEAR	NoUnit	
Dem_DTCFormatType	TEXTTABLE		(DEM_DTC_FORMAT_OBD,0),(DEM_DTC_FORMAT_UDS,1),(DEM_DTC_FORMAT_J1939,2)
Dem_DebounceResetStatusType	TEXTTABLE	NoUnit	(DEM_DEBOUNCE_STATUS_FREEZE,0),(DEM_DEBOUNCE_STATUS_RESET,1)
Dem_EventStatusType	TEXTTABLE	NoUnit	(DEM_EVENT_STATUS_PASSED,0),(DEM_EVENT_STATUS_FAILED,1),(DEM_EVENT_STATUS_PREPASSED,2),(DEM_EVENT_STATUS_PREFAILED,3),(DEM_EVENT_STATUS_FDC_THRESHOLD_REACHED,4),(DEM_EVENT_STATUS_PASSED_CONDITIONS_NOT_FULFILLED,5),(DEM_EVENT_STATUS_FAILED_CONDITIONS_NOT_FULFILLED,6),(DEM_EVENT_STATUS_PREPASSED_CONDITIONS_NOT_FULFILLED,7),(DEM_EVENT_STATUS_PREFAILED_CONDITIONS_NOT_FULFILLED,8)
Dem_UdsStatusByteType	SCALE_LINEAR_AND_TEXTTABLE		(DEM_UDS_STATUS_TF,1),(DEM_UDS_STATUS_TFTOC,2),(DEM_UDS_STATUS_PDTC,4),(DEM_UDS_STATUS_CDTC,8),(DEM_UDS_STATUS_TNCSLC,16),(DEM_UDS_STATUS_TFSLC,32),(DEM_UDS_STATUS_TNCTOC,64),(DEM_UDS_STATUS_WIR,128)
boolean_CompuMethod	TEXTTABLE	NoUnit	(FALSE,0),(TRUE,1)

5 Ports and Interfaces

5.1 Sender Receiver Interface and Ports

5.1.1 Incoming

Table 7 Incoming

Port	Variable	Description	Type
BattU_bVoltEnbPinDiag	Val		VALUE
gPRM_au16OvrCurrDebTime	Val		ARRAY
gPRM_au16OvrCurrThres	Val		ARRAY
gVRM_u16BatteryVolt_mv	Val		VALUE
KL15PwrSply_bAMPPowerTrigUds	Val		VALUE
KL15PwrSply_bInverterPowerTrigUds	Val		VALUE
KL15PwrSply_bKL15T1PowerTrigUds	Val		VALUE
KL15PwrSply_bKL15T2PowerTrigUds	Val		VALUE
KL15PwrSply_bPCUPowerTrigUds	Val		VALUE
KL15PwrSply_bRear12VPowerTrigUds	Val		VALUE
KL15PwrSply_bRearBlowerPowerTrigUds	Val		VALUE
KL15PwrSply_bRearCtrlPanelPowerTrigUds	Val		VALUE
KL15PwrSply_bRearEOPPowerTrigUds	Val		VALUE
KL15PwrSply_bRearMotorPowerTrigUds	Val		VALUE
KL15PwrSply_bRearRadarPowerTrigUds	Val		VALUE
KL15PwrSply_bRLALampCtlUnitPowerTrigUds	Val		VALUE
KL15PwrSply_bRLBLampCtlUnitPowerTrigUds	Val		VALUE
KL15PwrSply_bRMPowerTrigUds	Val		VALUE
KL15PwrSply_bRRALampCtlUnitPowerTrigUds	Val		VALUE
KL15PwrSply_bRRBLampCtlUnitPowerTrigUds	Val		VALUE

Port	Variable	Description	Type
KL15PwrSply_u8stAMPPowerUds	Val		VALUE
KL15PwrSply_u8stInverterPowerUds	Val		VALUE
KL15PwrSply_u8stKL15T1PowerUds	Val		VALUE
KL15PwrSply_u8stKL15T2PowerUds	Val		VALUE
KL15PwrSply_u8stPCUPowerUds	Val		VALUE
KL15PwrSply_u8stRear12VPowerUds	Val		VALUE
KL15PwrSply_u8stRearBlowerPowerUds	Val		VALUE
KL15PwrSply_u8stRearCtrlPanelPowerUds	Val		VALUE
KL15PwrSply_u8stRearEOPPowerUds	Val		VALUE
KL15PwrSply_u8stRearMotorPowerUds	Val		VALUE
KL15PwrSply_u8stRearRadarPowerUds	Val		VALUE
KL15PwrSply_u8stRLALampCtlUnitPowerUds	Val		VALUE
KL15PwrSply_u8stRLBLampCtlUnitPowerUds	Val		VALUE
KL15PwrSply_u8stRMPowerUds	Val		VALUE
KL15PwrSply_u8stRRALampCtlUnitPowerUds	Val		VALUE
KL15PwrSply_u8stRRBLampCtlUnitPowerUds	Val		VALUE
OilPmp_stReqIn	Val		VALUE
PwrSply_au8CoornReq	Val		ARRAY
PwrSply_au8PwrSup	Val		ARRAY
PwrSply_bCCULostComm	Val		VALUE
PwrSply_bNetSleepFlag	Val		VALUE
PwrSply_bPositionLightCtrlCmd01	Val		VALUE
PwrSply_bPositionLightCtrlCmd02	Val		VALUE
PwrSply_bPositionLightCtrlCmd03	Val		VALUE
PwrSply_bPositionLightCtrlCmd04	Val		VALUE
PwrSply_bPositionLightTimingOFFEnableCfg	Val		VALUE
PwrSply_bVehSpdTkOvr	Val		VALUE
PwrSply_u8UMM_UsageModeSt	Val		VALUE
PwrSply_u8VehicleTypeCfg	Val		VALUE
PwrSply_u8VMM_OTAModeSt	Val		VALUE

5.1.2 Outgoing

Table 8 Outgoing

Port	Variable	Description	Type
gKL15PwrSply_u32AllowSleep_Local	Val		VALUE
gKL15PwrSply_u32AllowSleep_NW	Val		VALUE
gKL15PwrSply_u8Wakeup	Val		VALUE
PwrSply_abPwrSt	Val		ARRAY
PwrSply_au16PwrCurr	Val		ARRAY

Port	Variable	Description	Type
PwrSply_au8PwrFaultSt	Val		ARRAY

5.2 Client Server Interface

5.2.1 Incoming

Table 9 Incoming

Port	Description
DTC_AMPPowerOC	
DTC_AMPPowerOL	
DTC_AMPPowerSCB	
DTC_AMPPowerSCG	
DTC_InverterPowerOC	
DTC_InverterPowerOL	
DTC_InverterPowerSCB	
DTC_InverterPowerSCG	
DTC_PCUPowerOC	
DTC_PCUPowerOL	
DTC_PCUPowerSCB	
DTC_PCUPowerSCG	
DTC_Rear12VPowerOC	
DTC_Rear12VPowerOL	
DTC_Rear12VPowerSCB	
DTC_Rear12VPowerSCG	
DTC_RearBlowerPowerOC	
DTC_RearBlowerPowerOL	
DTC_RearBlowerPowerSCB	
DTC_RearBlowerPowerSCG	
DTC_RearEOPPowerOC	
DTC_RearEOPPowerOL	
DTC_RearEOPPowerSCB	
DTC_RearEOPPowerSCG	
DTC_RearMotorPowerOC	
DTC_RearMotorPowerOL	
DTC_RearMotorPowerSCB	
DTC_RearMotorPowerSCG	
DTC_RearRadarPowerOC	
DTC_RearRadarPowerOL	
DTC_RearRadarPowerSCB	
DTC_RearRadarPowerSCG	
DTC_RLALampCtlUnitPowerOC	
DTC_RLALampCtlUnitPowerOL	
DTC_RLALampCtlUnitPowerSCB	
DTC_RLALampCtlUnitPowerSCG	
DTC_RLBLampCtlUnitPowerOC	
DTC_RLBLampCtlUnitPowerOL	
DTC_RLBLampCtlUnitPowerSCB	
DTC_RLBLampCtlUnitPowerSCG	
DTC_RMPowerOC	
DTC_RMPowerOL	
DTC_RMPowerSCB	

Port	Description
DTC_RMPowerSCG	
DTC_RRALampCtlUnitPowerOC	
DTC_RRALampCtlUnitPowerOL	
DTC_RRALampCtlUnitPowerSCB	
DTC_RRALampCtlUnitPowerSCG	
DTC_RRBLampCtlUnitPowerOC	
DTC_RRBLampCtlUnitPowerOL	
DTC_RRBLampCtlUnitPowerSCB	
DTC_RRBLampCtlUnitPowerSCG	
IoHwAb_IoDiagDo	
IoHwAb_IoDiagPwm	
IoHwAb_IoSigAdc	
IoHwAb_IoSigDio	
IoHwAb_IoSigPwm	
NMW_tGetAllCurrentComModeRTE	
NMW_tGetCurrentComModeRTE	

II Production Note

Table 10 Configuration chosen for DocuNG

Parameter	Value
User	
Project Name	KL15PwrSply_DD
Generator Mode	Continue on non-fatal error
Ascet graphic generator engine	UnifiedGraphicGenerator
Matlab graphic generator engine	UnifiedGraphicGenerator
DocType	Det-SwCalDoc
Condition Evaluation	true
Sorting Algorithm	Config Order
Title Page Logo	
Print Algorithms To Review	true
Support Fallback Language	true
Print List Of Converted System Constants	true
Include Function Information Chapter	true
Create Label Alias Mapping	true
HTML	false
PDF	true
PDF: Language	EN - English
PDF: Links in Graphics	true
PDF: Line Numbers	false
PDF: Confidential Level 2	true
PDF: Docu Security Option	false
EHB	false

Table 11 Version Information

Program Module	Version
Product	AEEE-Pro 2020.1.0