

| Lock_DD | 2022-10-13

Lock_DD

Software and calibration documentation (detailed) [COMP]

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I [Lock_DD]

1 Function Definition

1.1 Purpose

Input the purpose of developing this function

1.2 Introduction

Lock device driver

2 Function Description

2.1 Behavior in normal mode

Figure 1 Lock_DD [Lock_DD]

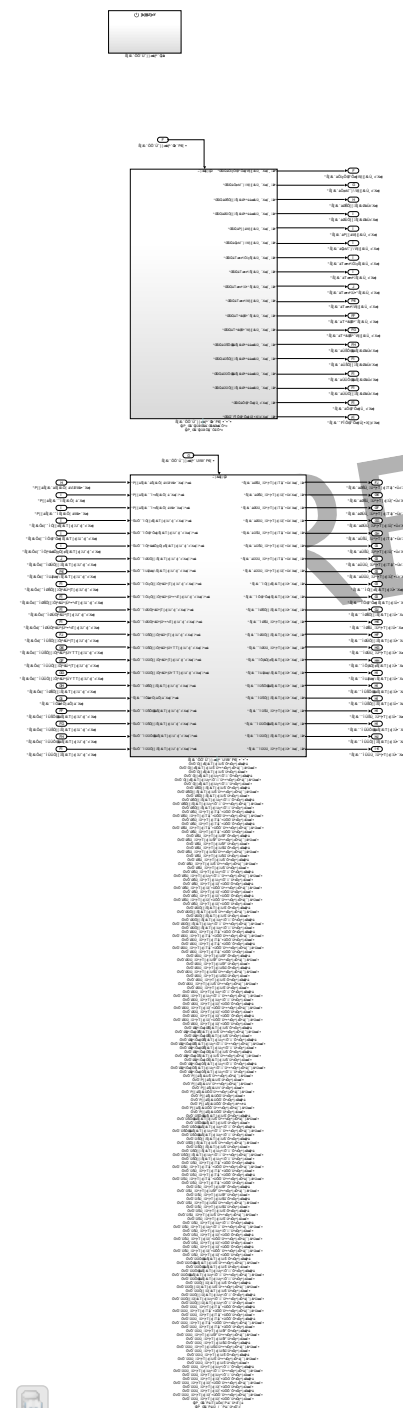


Figure 2 Lock_DD_Lock_DD_Runnable_IN_10ms_sys [Lock_DD_Lock_DD_Runnable_IN_10ms_sys]

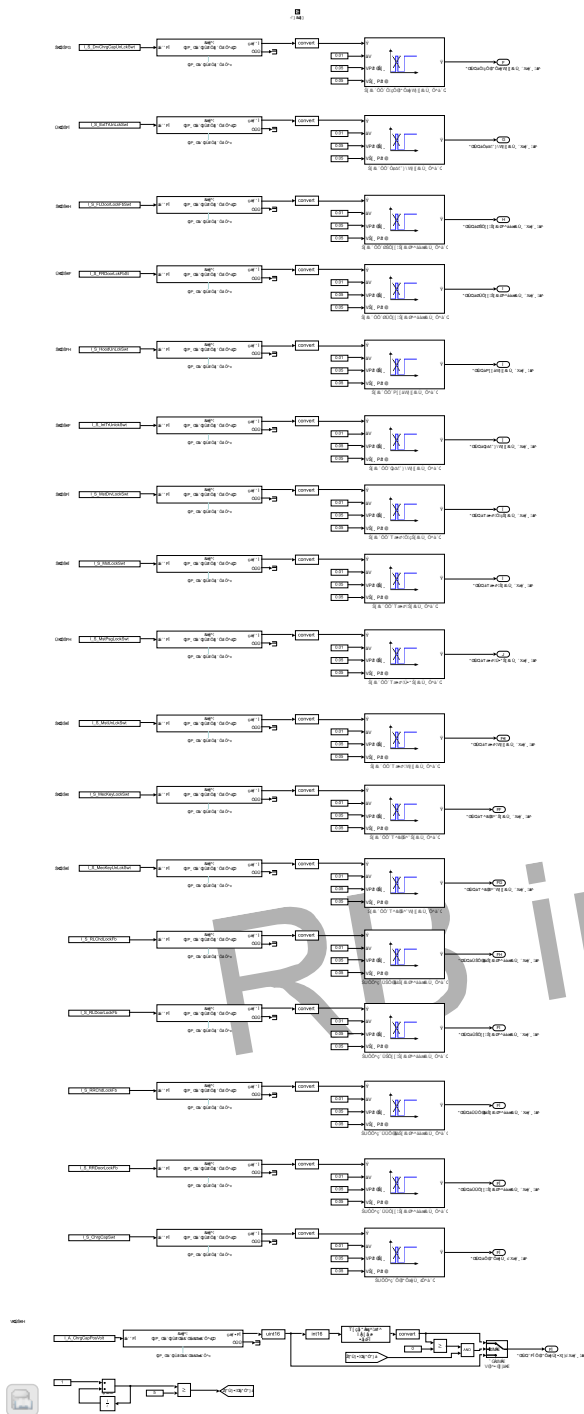
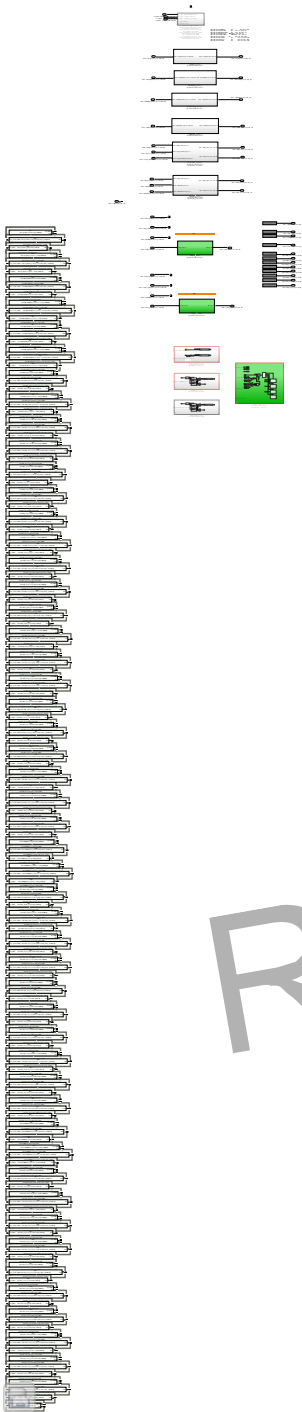


Figure 3 Lock_DD_Lock_DD_Runnable_Init [Lock_DD_Lock_DD_Runnable_Init]



Figure 4 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys]



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Figure 5 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput]

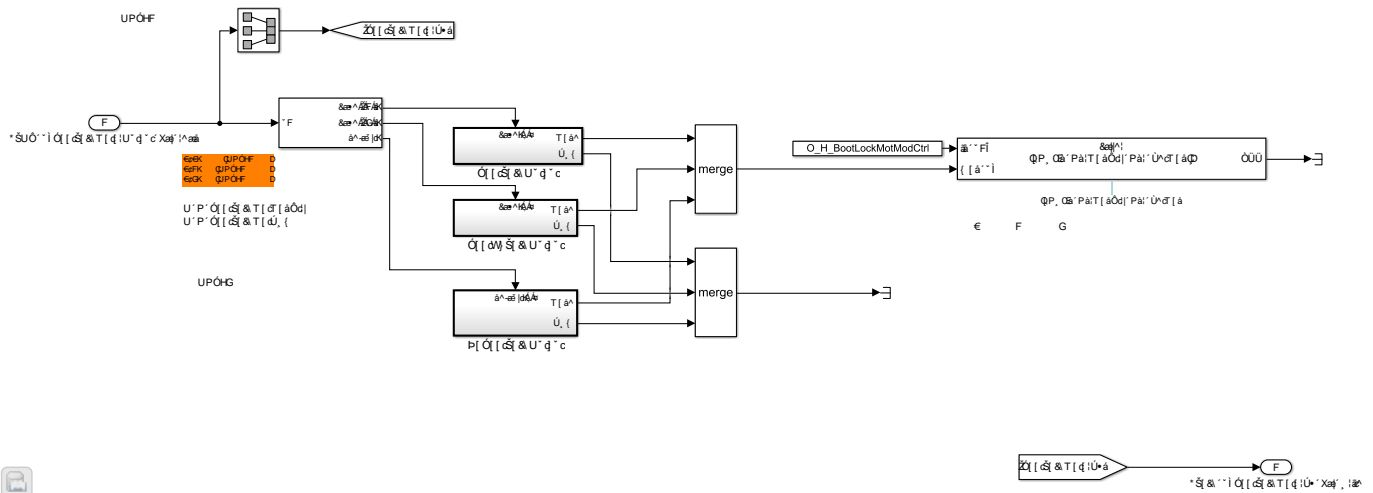


Figure 6 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput_BootLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput_BootLockOutput]

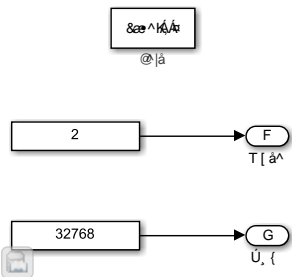


Figure 7 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput_BootUnLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput_BootUnLockOutput]

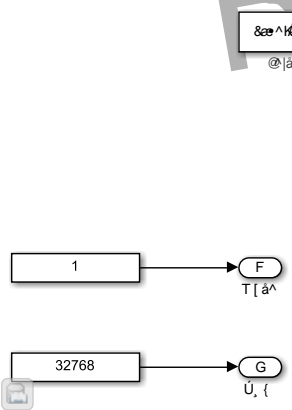


Figure 8 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput_NoBootLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_BootLockMotorOutput_NoBootLockOutput]

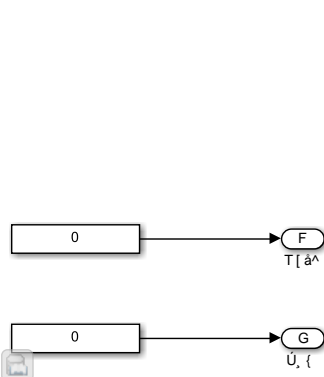


Figure 9 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput]

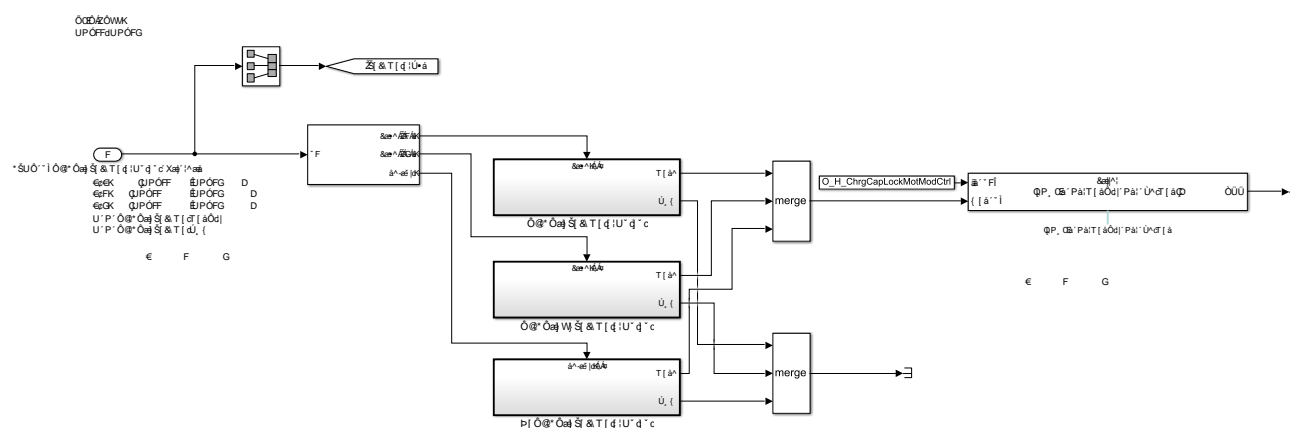


Figure 10 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput_ChrgCapLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput_ChrgCapLockMotorOutput]

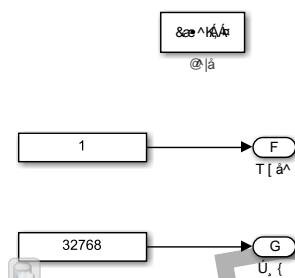


Figure 11 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput_ChrgCapUnLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput_ChrgCapUnLockMotorOutput]

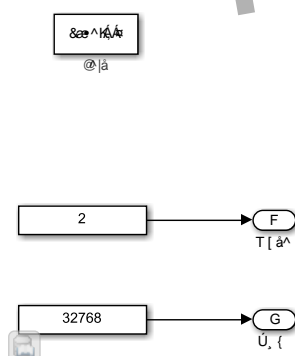


Figure 12 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput_NoChrgCapLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ChrgCapLockMotorOutput_NoChrgCapLockMotorOutput]

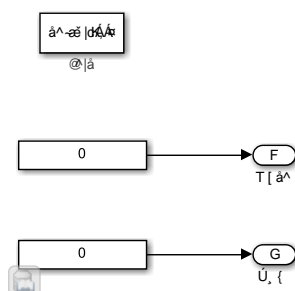


Figure 13 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput]

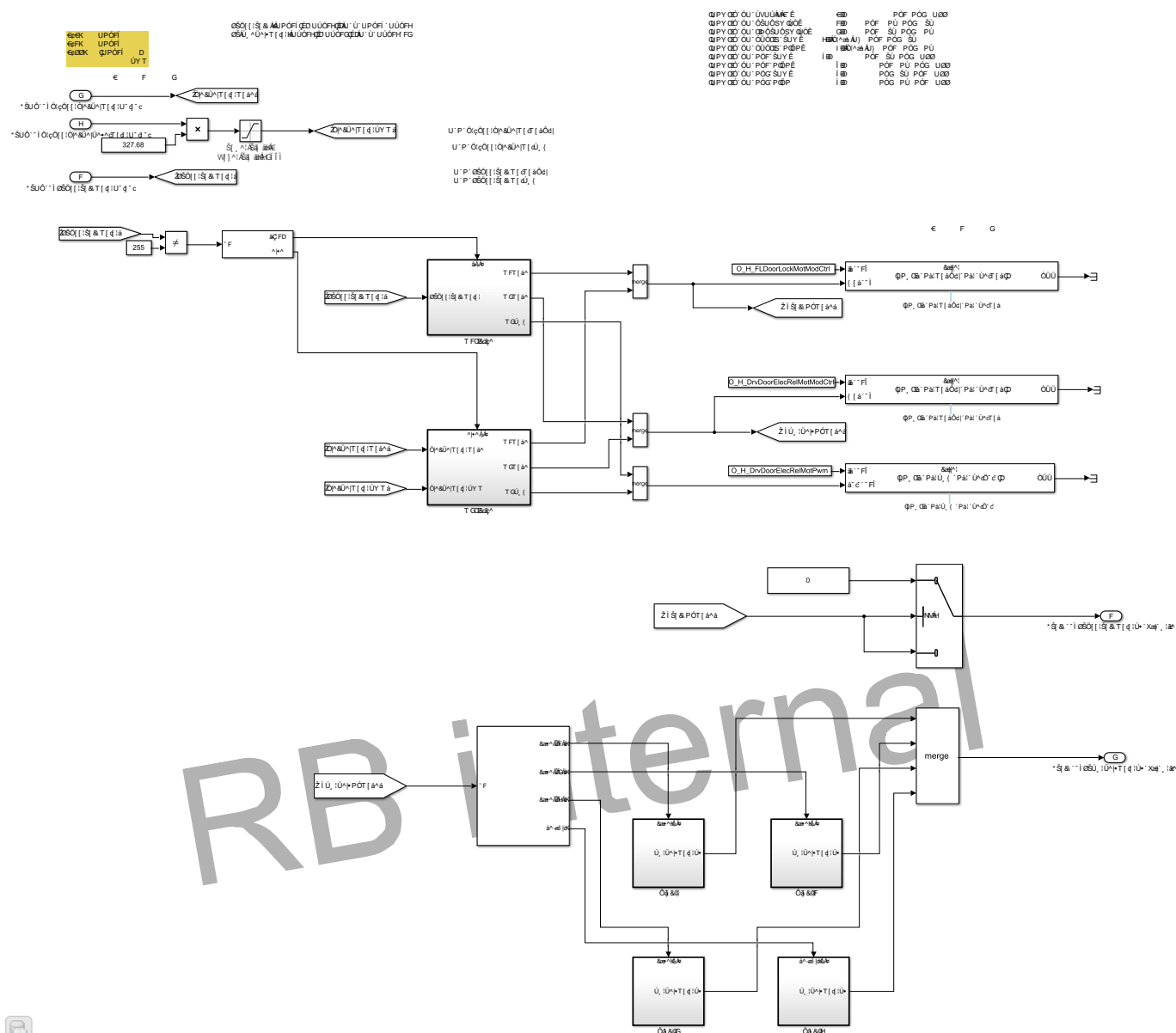


Figure 14 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch]

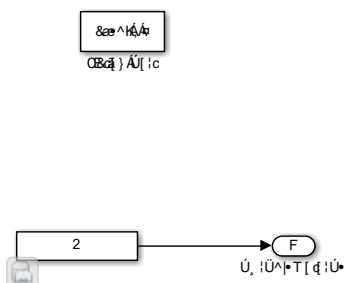


Figure 15 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch1 [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch1]



Figure 16 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch2 [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch2]

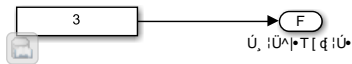
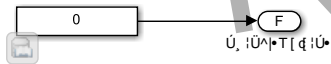


Figure 17 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch3 [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_Cinch3]



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Figure 18 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active]

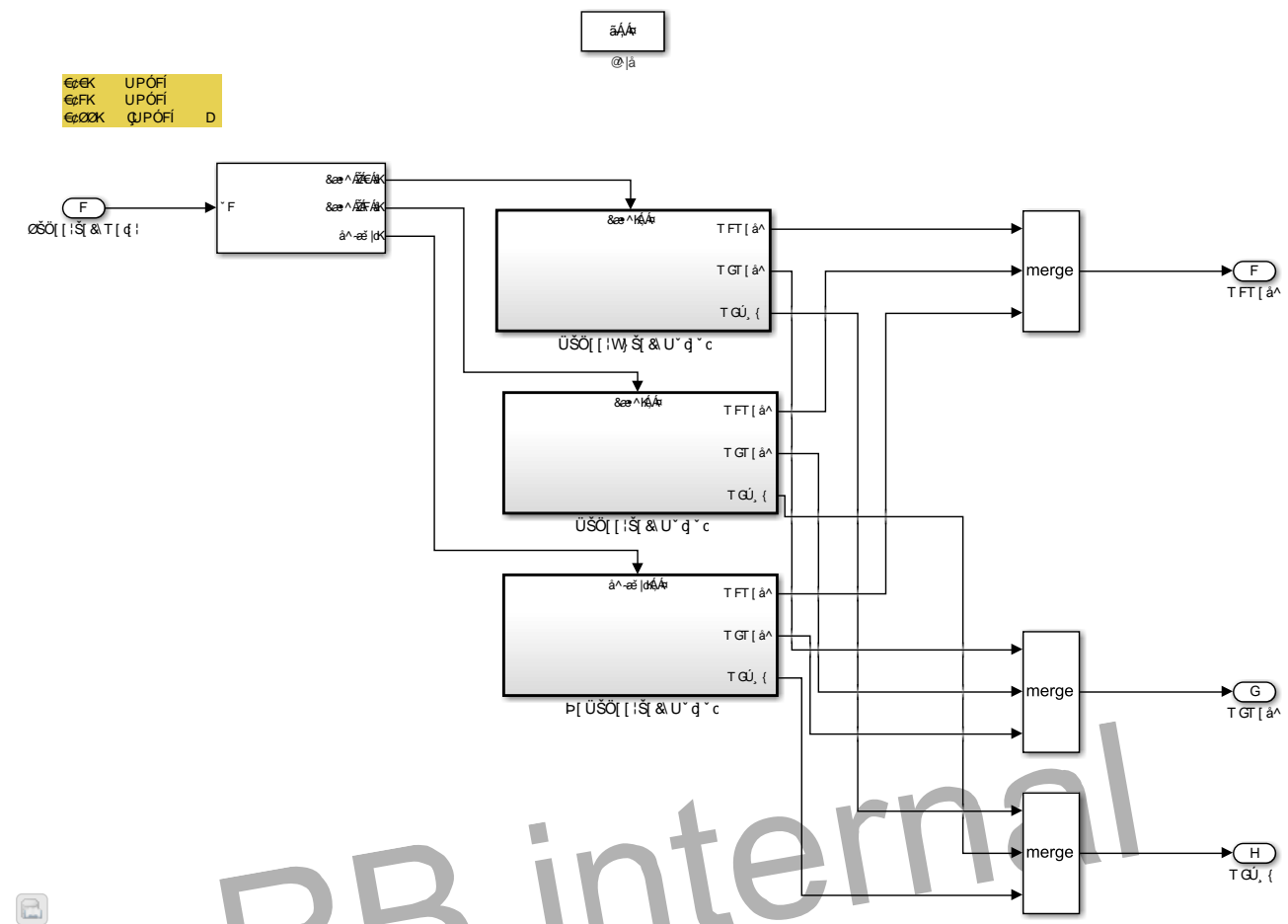


Figure 19 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active_NoRLDoorLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active_NoRLDoorLockOutput]

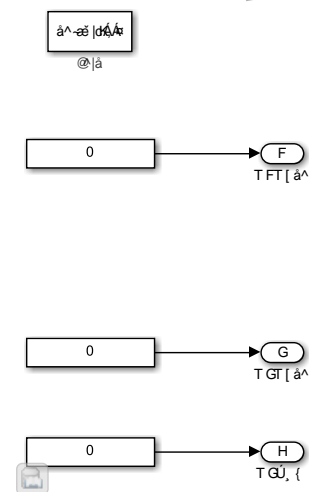


Figure 20 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active_RLDoorLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active_RLDoorLockOutput]

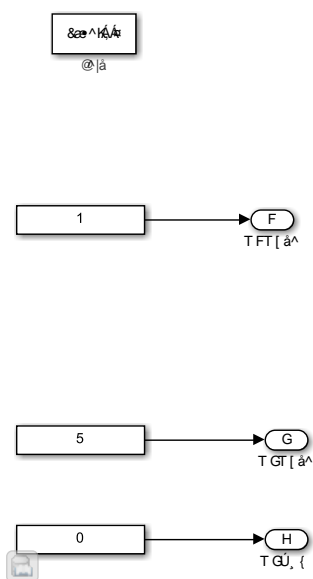


Figure 21 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active_RLDoorUnLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M1Active_RLDoorUnLockOutput]

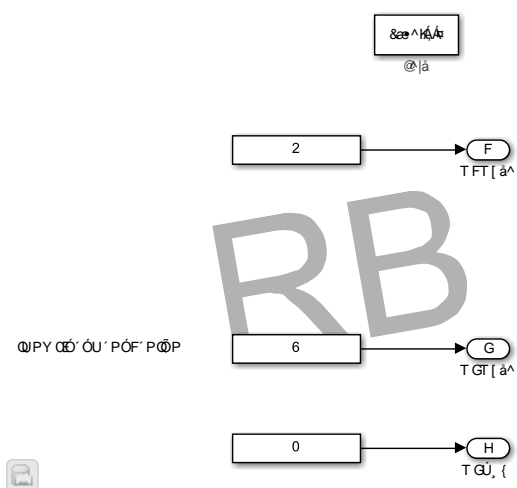


Figure 22 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active]

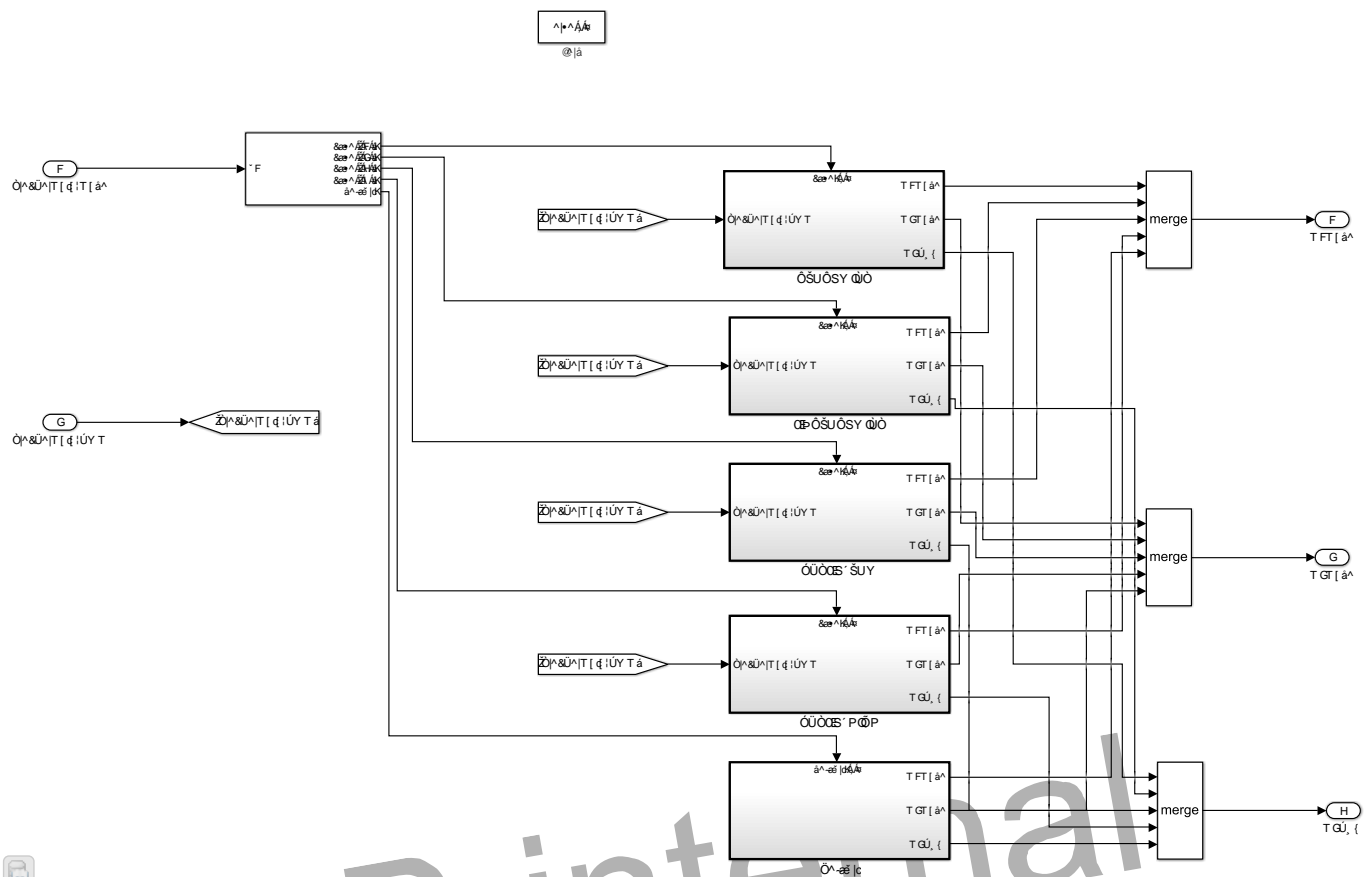


Figure 23 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_ANCLOCKWISE [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_ANCLOCKWISE]

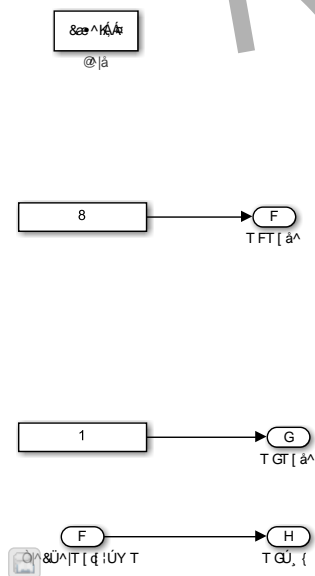


Figure 24 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_BREAK_HIGH [Lock_DD_Lock_DD_Runnable-OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_BREAK_HIGH]

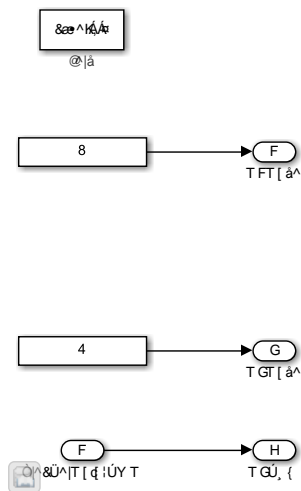


Figure 25 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_BREAK_LOW [Lock_DD_Lock_DD_Runnable-OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_BREAK_LOW]

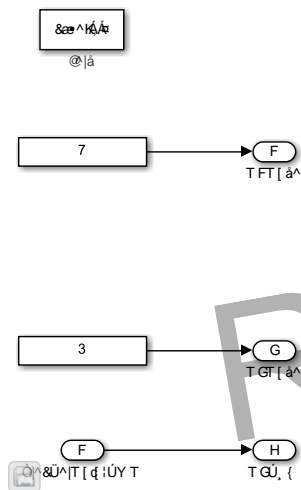


Figure 26 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_CLOCKWISE [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_CLOCKWISE]

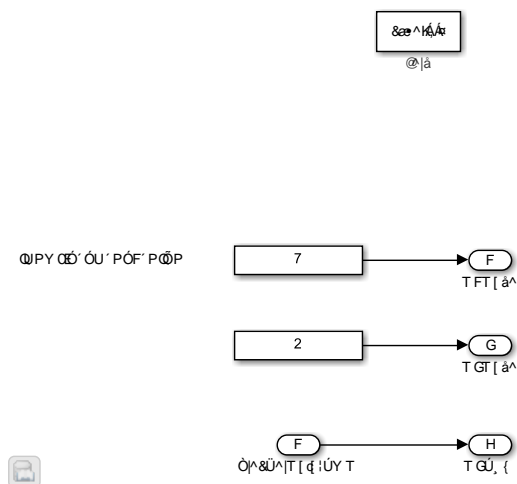


Figure 27 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_Default [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_DrvDoorElecRelMotorOutput_M2Active_Default]

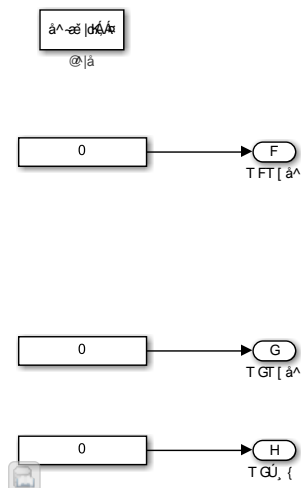


Figure 28 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput]

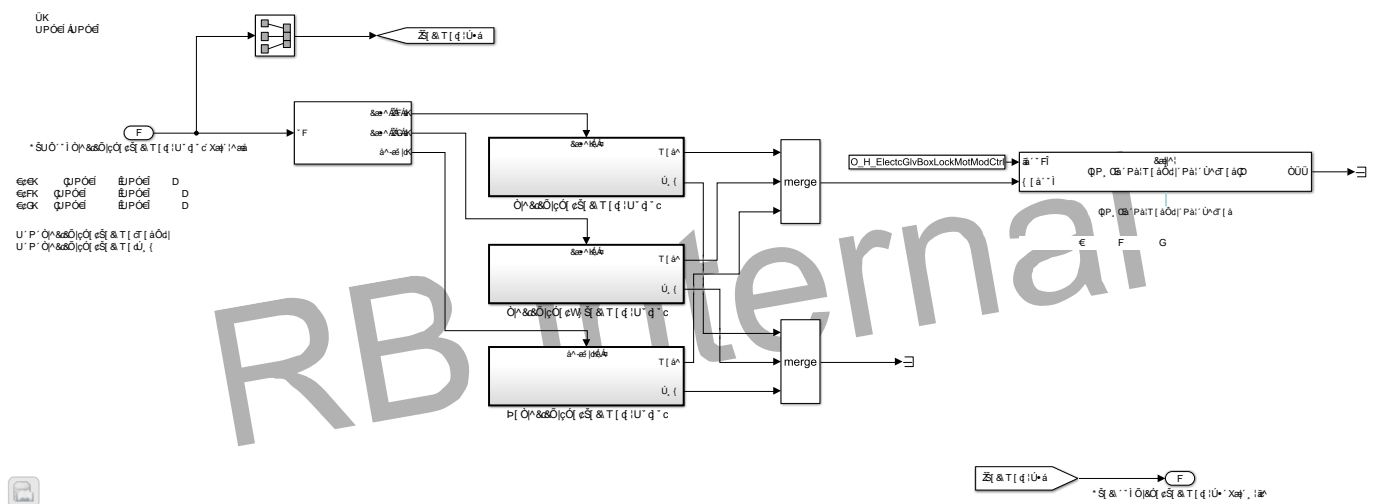


Figure 29 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput_ElectcGlvBoxLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput_ElectcGlvBoxLockMotorOutput]

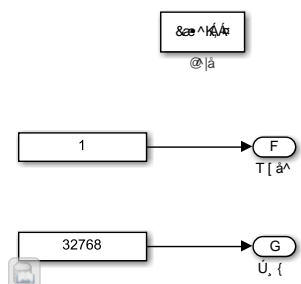


Figure 30 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput_ElectcGlvBoxUnLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput_ElectcGlvBoxUnLockMotorOutput]

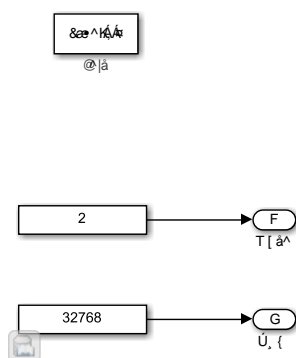
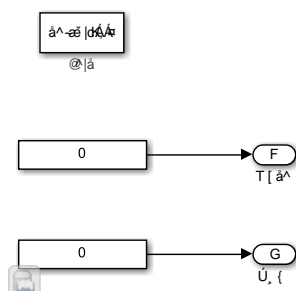


Figure 31 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput_NoElectcGlvBoxLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_ElectcGlvBoxLockMotorOutput_NoElectcGlvBoxLockMotorOutput]



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Figure 32 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput]

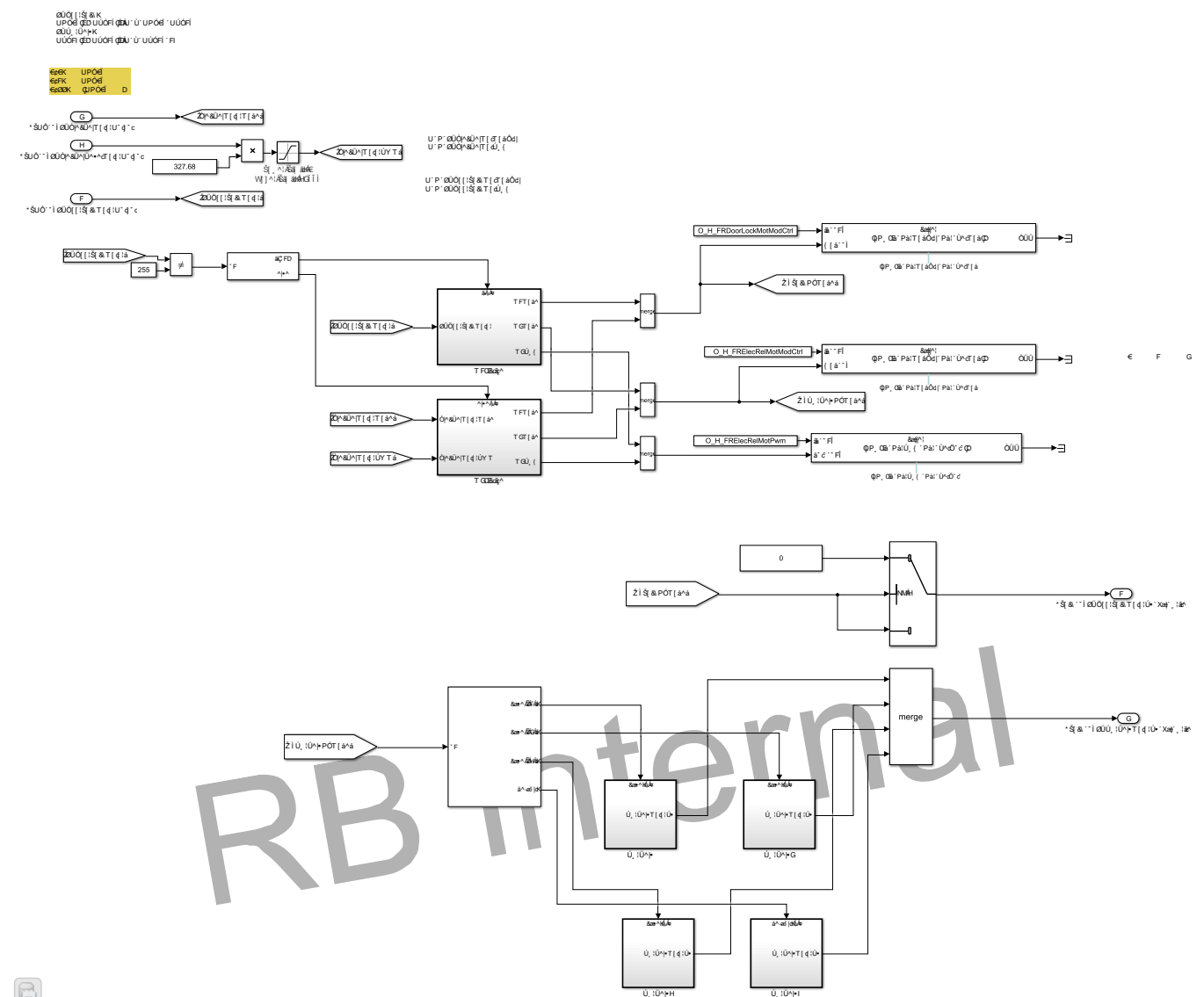


Figure 33 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active]

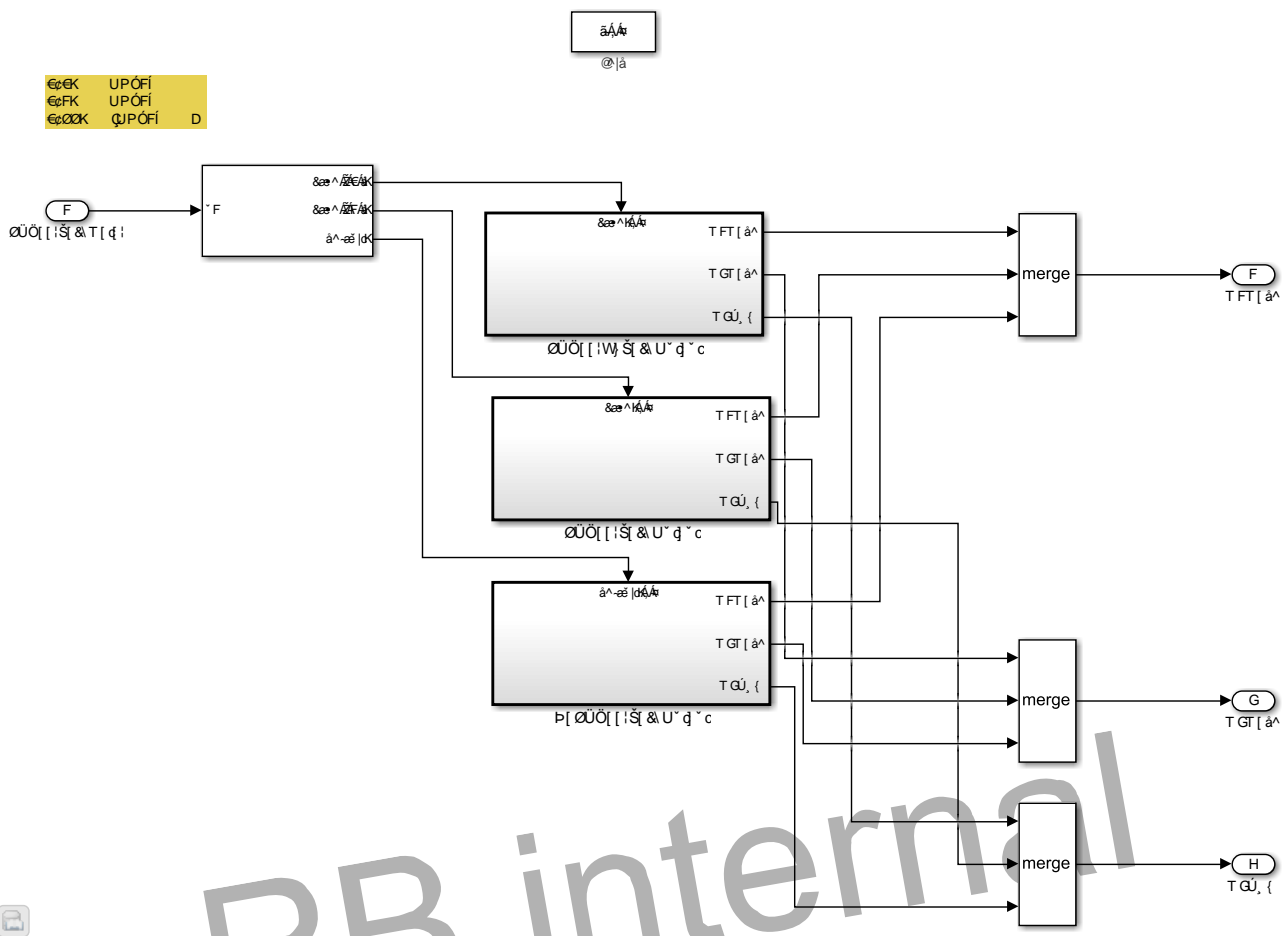


Figure 34 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active_FRDoorLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active_FRDoorLockOutput]

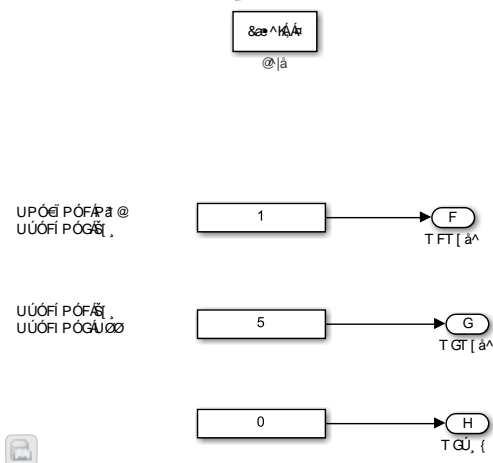


Figure 35 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active_FRDoorUnLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active_FRDoorUnLockOutput]

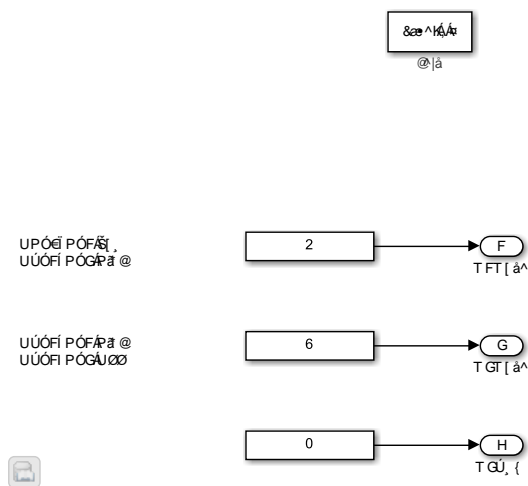


Figure 36 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active_NoFRDoorLockOutput [Lock_DD_Lock_DD-Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M1Active_NoFRDoorLockOutput]

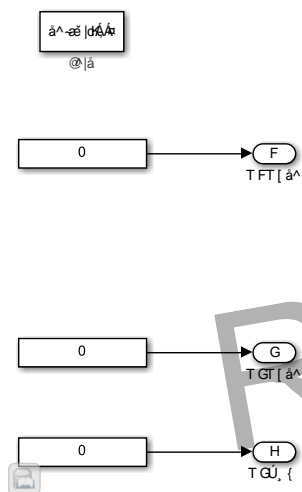


Figure 37 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys-FRElecRelMotorOutput_M2Active]

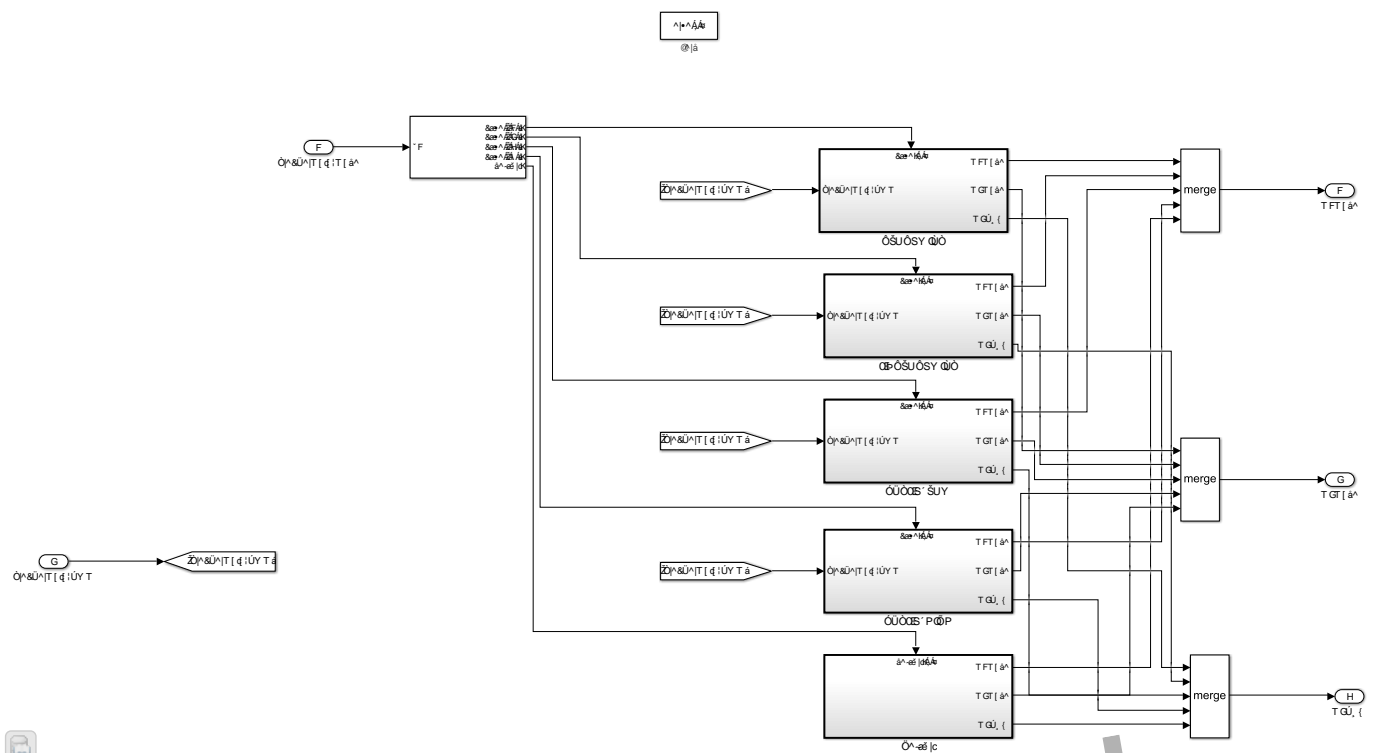


Figure 38 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_ANCLOCKWISE [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_ANCLOCKWISE]

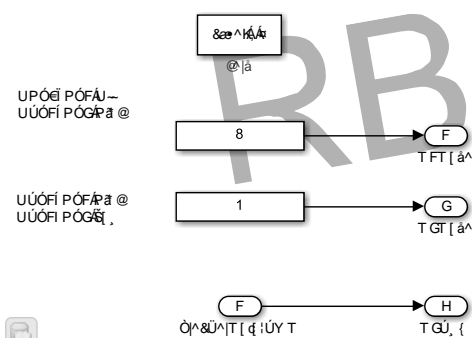


Figure 39 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_BREAK_HIGH [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_BREAK_HIGH]

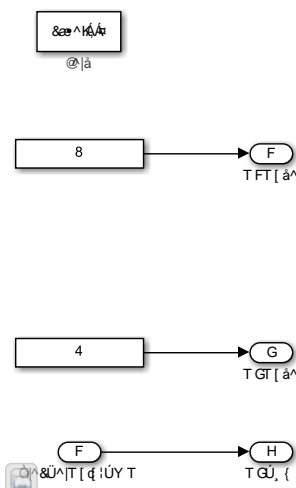


Figure 40 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_BREAK_LOW [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_BREAK_LOW]

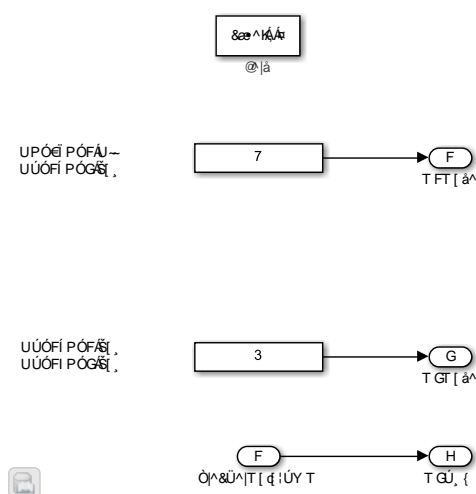


Figure 41 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_CLOCKWISE [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_CLOCKWISE]

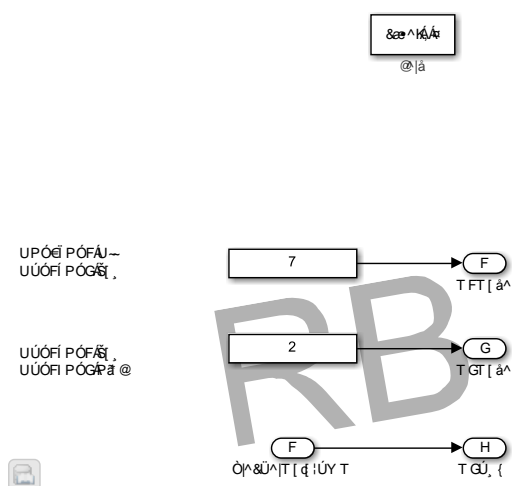


Figure 42 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_Default [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_M2Active_Default]

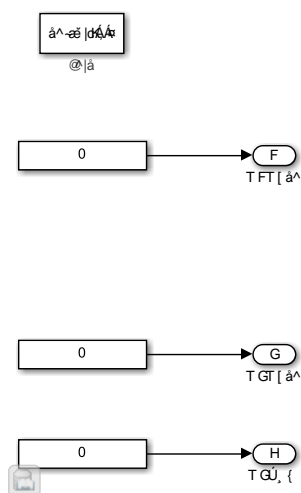


Figure 43 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels]

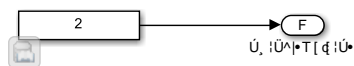


Figure 44 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels2 [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels2]

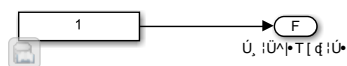


Figure 45 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels3 [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels3]

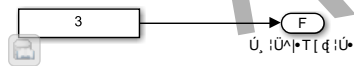


Figure 46 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels4 [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_FRElecRelMotorOutput_PwrRels4]



Figure 47 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput]

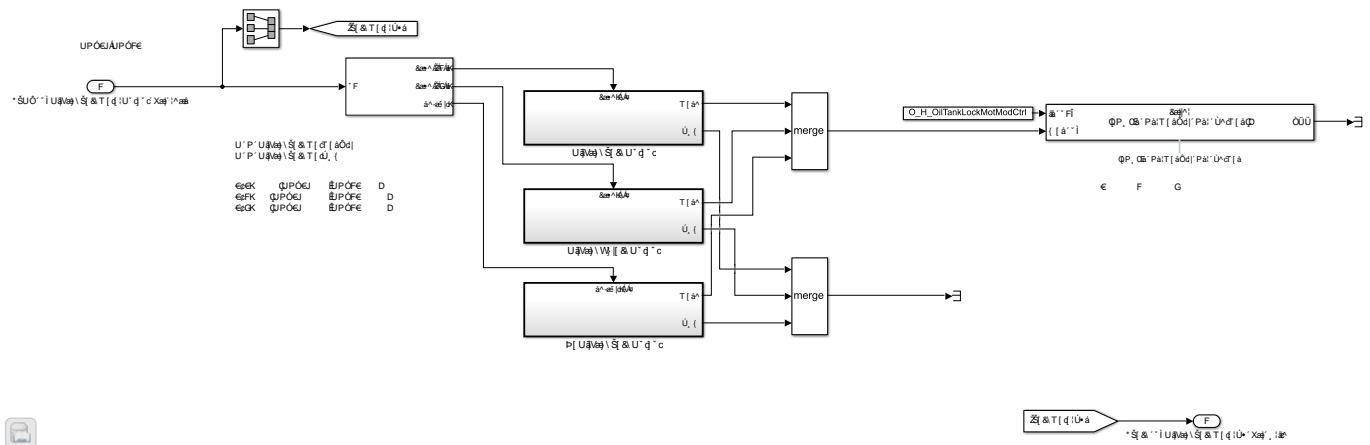


Figure 48 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput_NoOilTankLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput_NoOilTankLockOutput]

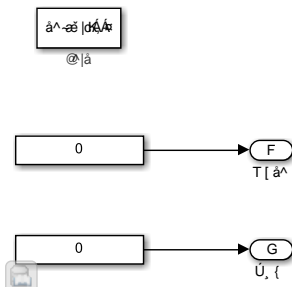


Figure 49 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput_OilTankLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput_OilTankLockOutput]

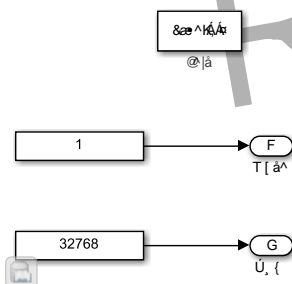


Figure 50 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput_OilTankUnlockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_OilTankLockMotorOutput_OilTankUnlockOutput]

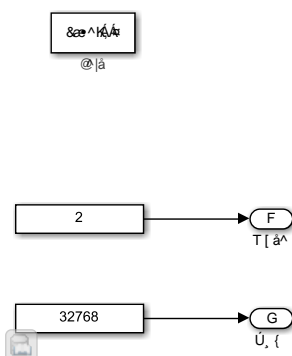


Figure 51 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput]

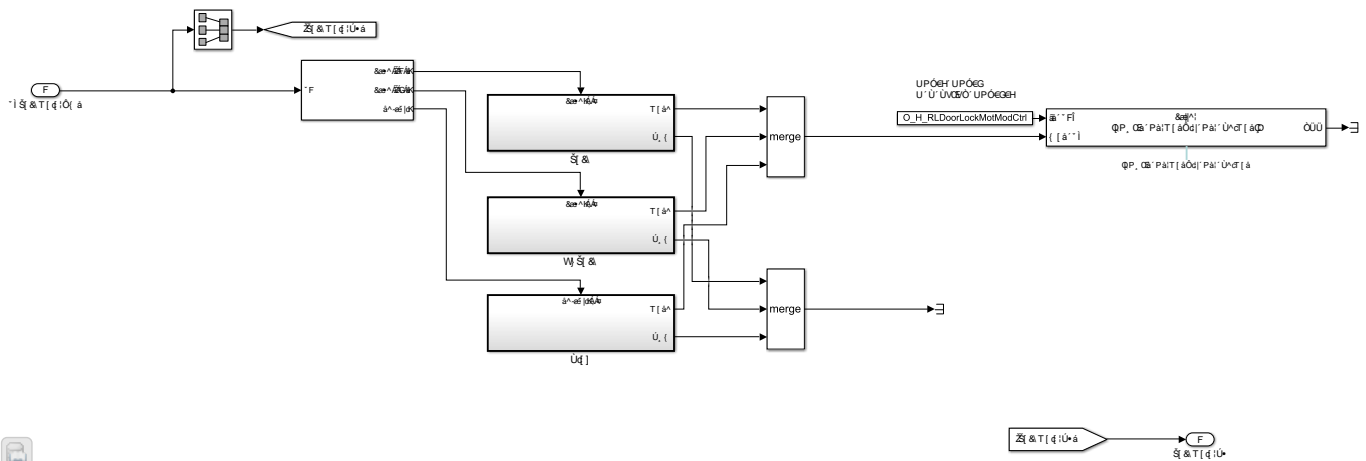


Figure 52 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput_Lock [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput_Lock]

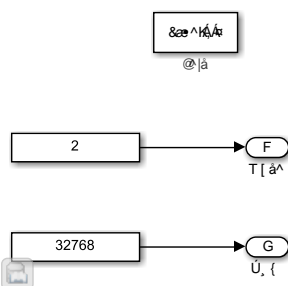


Figure 53 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput_Stop [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput_Stop]

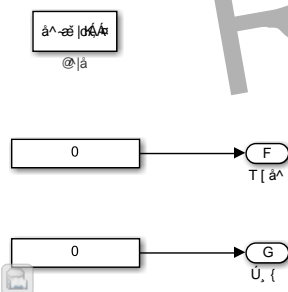


Figure 54 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput_UnLock [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RLDoorLockOutput_UnLock]

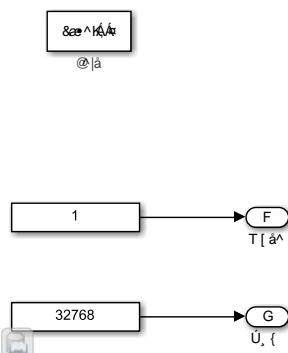


Figure 55 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput]

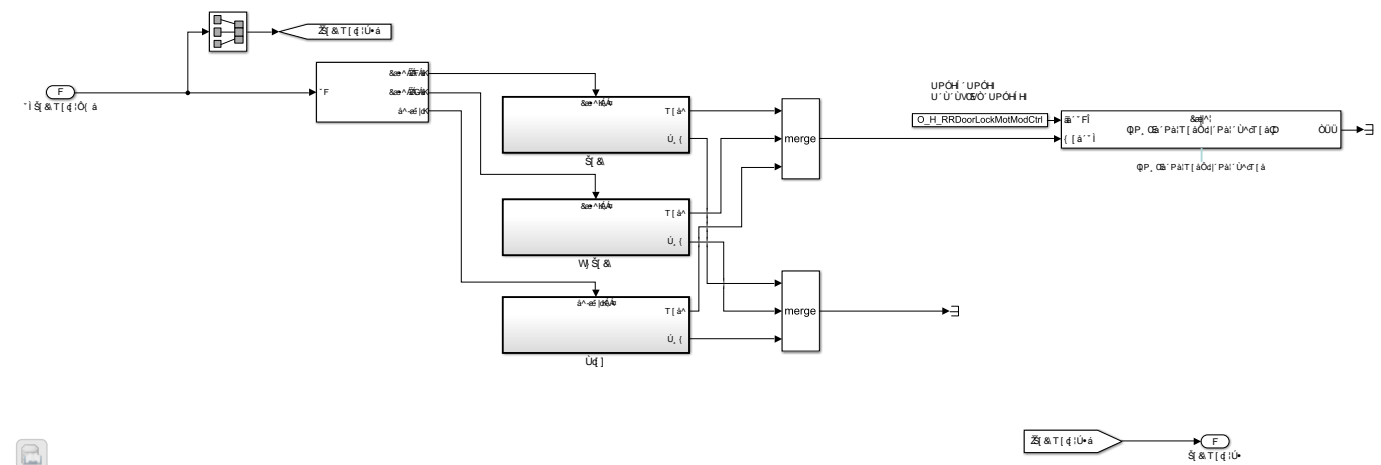


Figure 56 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput_Lock [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput_Lock]

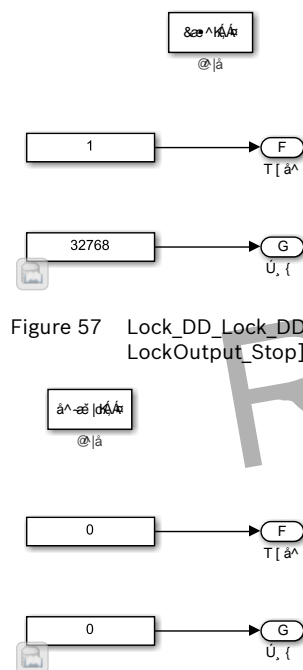


Figure 57 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput_Stop [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput_Stop]

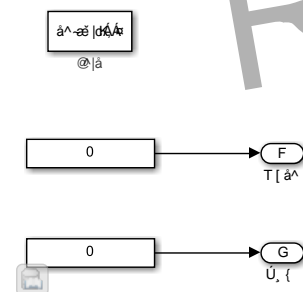


Figure 58 Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput_UnLock [Lock_DD_Lock_DD_Runnable_OUT_10ms_sys_RRDoorLockOutput_UnLock]

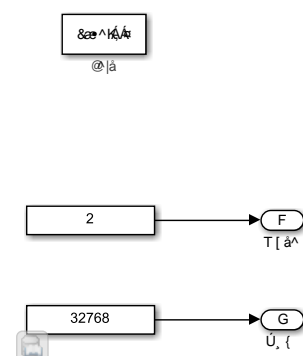


Table 1 Data Types for port interfaces [PortInterfaceDataTypes]

Port	AccessMode	Interface	DE	Datatype
gHoodLock_bLockCmdT-rigUds	ExplicitReceive	gHoodLock_bLockCmdT-rigUds	Val	boolean

Port	AccessMode	Interface	DE	Datatype
gHoodLock_u8LockCmd	ExplicitReceive	gHoodLock_u8LockCmd	Val	uint8
gHoodLock_u8LockCmd-Uds	ExplicitReceive	gHoodLock_u8LockCmd-Uds	Val	uint8
gLockCtrl_u8BootLockMotorOutput	ExplicitReceive	gLockCtrl_u8BootLockMotorOutput	Val	uint8
gLockCtrl_u8ChrgCap-LockMotorOutput	ExplicitReceive	gLockCtrl_u8ChrgCap-LockMotorOutput	Val	uint8
gLockCtrl_u8ElectcGlv-BoxLockMotorOutput	ExplicitReceive	gLockCtrl_u8ElectcGlv-BoxLockMotorOutput	Val	uint8
gLockCtrl_u8FRDoorLock-MotorOutput	ExplicitReceive	gLockCtrl_u8FRDoorLock-MotorOutput	Val	uint8
gLockCtrl_u8OilTankLock-MotorOutput	ExplicitReceive	gLockCtrl_u8OilTankLock-MotorOutput	Val	uint8
gLockCtrl_u8RLChildLock-MotorOutput	ExplicitReceive	gLockCtrl_u8RLChildLock-MotorOutput	Val	uint8
gLockCtrl_u8RLDoorLock-MotorOutput	ExplicitReceive	gLockCtrl_u8RLDoorLock-MotorOutput	Val	uint8
gLockCtrl_u8RRChildLock-MotorOutput	ExplicitReceive	gLockCtrl_u8RRChildLock-MotorOutput	Val	uint8
gLockCtrl_u8RRDoorLock-MotorOutput	ExplicitReceive	gLockCtrl_u8RRDoorLock-MotorOutput	Val	uint8
gLockCtrl_u8FLDoorElec-RelMotorOutput	ExplicitReceive	gLockCtrl_u8FLDoorElec-RelMotorOutput	Val	uint8
gLockCtrl_u8FLDoorElec-RelResetMotorOutput	ExplicitReceive	gLockCtrl_u8FLDoorElec-RelResetMotorOutput	Val	uint8
gLockCtrl_u8FRElecRel-MotorOutput	ExplicitReceive	gLockCtrl_u8FRElecRel-MotorOutput	Val	uint8
gLockCtrl_u8FRElecRelResetMotorOutput	ExplicitReceive	gLockCtrl_u8FRElecRelResetMotorOutput	Val	uint8
gLockCtrl_u8RLDoorElec-RelMotorOutput	ExplicitReceive	gLockCtrl_u8RLDoorElec-RelMotorOutput	Val	uint8
gLockCtrl_u8RLDoorElec-RelPWMMotorOutput	ExplicitReceive	gLockCtrl_u8RLDoorElec-RelPWMMotorOutput	Val	uint8
gLockCtrl_u8RRDoorElec-RelMotorOutput	ExplicitReceive	gLockCtrl_u8RRDoorElec-RelMotorOutput	Val	uint8
gLockCtrl_u8RRDoorElec-RelPWMMotorOutput	ExplicitReceive	gLockCtrl_u8RRDoorElec-RelPWMMotorOutput	Val	uint8
gLockCtrl_u8FLDoorLock-MotorOutput	ExplicitReceive	gLockCtrl_u8FLDoorLock-MotorOutput	Val	uint8
gLock_u8DiagEnbCnd	ExplicitReceive	gLock_u8DiagEnbCnd	Val	uint8
gLock_bDrvChrgCapUnlockSwt	ExplicitSend	gLock_bDrvChrgCapUnlockSwt	Val	boolean
gLock_bExtTrunkUnlockSwt	ExplicitSend	gLock_bExtTrunkUnlockSwt	Val	boolean
gLock_bFLDoorLockFbSt	ExplicitSend	gLock_bFLDoorLockFbSt	Val	boolean
gLock_bFRDoorLockFbSt	ExplicitSend	gLock_bFRDoorLockFbSt	Val	boolean
gLock_bHoodUnlockSwt	ExplicitSend	gLock_bHoodUnlockSwt	Val	boolean
gLock_bIntTrunkUnlockSwt	ExplicitSend	gLock_bIntTrunkUnlockSwt	Val	boolean
gLock_bMasterDrvLockSwt	ExplicitSend	gLock_bMasterDrvLockSwt	Val	boolean
gLock_bMasterLockSwt	ExplicitSend	gLock_bMasterLockSwt	Val	boolean
gLock_bMasterPsgLockSwt	ExplicitSend	gLock_bMasterPsgLockSwt	Val	boolean
gLock_bMasterUnlockSwt	ExplicitSend	gLock_bMasterUnlockSwt	Val	boolean
gLock_bMechKeyLockSwt	ExplicitSend	gLock_bMechKeyLockSwt	Val	boolean
gLock_bMechKeyUnlockSwt	ExplicitSend	gLock_bMechKeyUnlockSwt	Val	boolean

Port	AccessMode	Interface	DE	Datatype
gLock_bRLChildLockFbSt	ExplicitSend	gLock_bRLChildLockFbSt	Val	boolean
gLock_bRLDoorLockFbSt	ExplicitSend	gLock_bRLDoorLockFbSt	Val	boolean
gLock_bRRChildLockFbSt	ExplicitSend	gLock_bRRChildLockFbSt	Val	boolean
gLock_bRRDoorLockFbSt	ExplicitSend	gLock_bRRDoorLockFbSt	Val	boolean
gLock_bChrgCapSwt	ExplicitSend	gLock_bChrgCapSwt	Val	boolean
gLock_u16ChrgCapPos-Volt	ExplicitSend	gLock_u16ChrgCapPos-Volt	Val	uint16
gLock_bFLPwrRelsMotor-MinusSt	ExplicitSend	gLock_bFLPwrRelsMotor-MinusSt	Val	boolean
gLock_bFLPwrRelsMotor-PlusSt	ExplicitSend	gLock_bFLPwrRelsMotor-PlusSt	Val	boolean
gLock_bFRPwrRelsMotor-MinusSt	ExplicitSend	gLock_bFRPwrRelsMotor-MinusSt	Val	boolean
gLock_bFRPwrRelsMotor-PlusSt	ExplicitSend	gLock_bFRPwrRelsMotor-PlusSt	Val	boolean
gLock_bRLPwrRelsMotor-MinusSt	ExplicitSend	gLock_bRLPwrRelsMotor-MinusSt	Val	boolean
gLock_bRLPwrRelsMotor-PlusSt	ExplicitSend	gLock_bRLPwrRelsMotor-PlusSt	Val	boolean
gLock_bRRPwrRelsMotor-MinusSt	ExplicitSend	gLock_bRRPwrRelsMotor-MinusSt	Val	boolean
gLock_bRRPwrRelsMotor-PlusSt	ExplicitSend	gLock_bRRPwrRelsMotor-PlusSt	Val	boolean
gLock_u8BootLockMotor-Ps	ExplicitSend	gLock_u8BootLockMotor-Ps	Val	uint8
gLock_u8ChrgCapLockMotorPs	ExplicitSend	gLock_u8ChrgCapLockMotorPs	Val	uint8
gLock_u8FLDoorLockMotorPs	ExplicitSend	gLock_u8FLDoorLockMotorPs	Val	uint8
gLock_u8FLPwrRelsMotor-Ps	ExplicitSend	gLock_u8FLPwrRelsMotor-Ps	Val	uint8
gLock_u8FRDoorLockMotorPs	ExplicitSend	gLock_u8FRDoorLockMotorPs	Val	uint8
gLock_u8FRPwrRelsMotorPs	ExplicitSend	gLock_u8FRPwrRelsMotorPs	Val	uint8
gLock_u8GlcBoxLockMotorPs	ExplicitSend	gLock_u8GlcBoxLockMotorPs	Val	uint8
gLock_u8OilTankLockMotorPs	ExplicitSend	gLock_u8OilTankLockMotorPs	Val	uint8
gLock_u8RLChildLockMotorPs	ExplicitSend	gLock_u8RLChildLockMotorPs	Val	uint8
gLock_u8RLDoorLockMotorPs	ExplicitSend	gLock_u8RLDoorLockMotorPs	Val	uint8
gLock_u8RLPwrRelsMotorPs	ExplicitSend	gLock_u8RLPwrRelsMotorPs	Val	uint8
gLock_u8RRChildLockMotorPs	ExplicitSend	gLock_u8RRChildLockMotorPs	Val	uint8
gLock_u8RRDoorLockMotorPs	ExplicitSend	gLock_u8RRDoorLockMotorPs	Val	uint8
gLock_u8RRPwrRelsMotorPs	ExplicitSend	gLock_u8RRPwrRelsMotorPs	Val	uint8