

SM2259XT2 跳线说明V0.1-----Yuuta

主控版本	GPIO	GPIO定义	设置值	位号	NAND型号	B16/B17	B27A/B27B	N18	N18	N28	B36R/B37R	B47R	N48R	HY V3/V4/V5	HY V5/V6	SS V3/V4/V5	SS V5/V6	Intel N38	JGS	TAS	Kioxia 1Z MLC	WD 1Z MLC	Kioxia BiCS3	Kioxia BiCS4	WD BiCS3	WD BiCS4	WD BiCS5		
					VCCFQ	1.8V	1.2V	1.8V	1.2V	1.2V	1.2V	1.2V	1.8v	1.2V	1.8V	1.2V	1.2V	1.8V	1.2V	1.2V	1.8V	1.8V	1.8V	1.8V	1.8V	1.8V	1.8V	1.8V	1.8V
					VCCF	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	2.5V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V	3.3V
AA	P03	Enable YMTC 1.2V Vccq Power On Flow	PU: Disable (Default) PD: Enable	RG03																									
AA	P04	Set VDT27 Detect Voltage	PU: Vrr = 2.2v (Default) PD: Vrr = 2.6v	RG04																									
AA	P06	Do Extra NAND Flash Power Cycle	PU: Disable (Default) PD: Enable	RG06	默认不装，M. 2和MSATA若遇到兼容性问题可能要装上																								
AA	P16	Six ALE Feature	PU: Enable PD: Disable (Default)	RG16							✓	✓	✓					✓	✓	✓									
AA	P17	Use 3B command to switch SLC mode	PU: Issue 3B command PD: None (Default)	RG17								✓	✓					✓											
AA	P20	NAND IO Driving Setting under ROM code mode	PU: Many CE PD: Less CE (Default)	RG20	多CE时建议跳，提高ROM下的驱动能力，解决部分开卡最后Fail的问题																								
AA	P22	Flash Interface Toggle Mode with differential enable	PU: Enable PD: Disable (Default)	RG22																									
AA	P23	Flash Mode Option	PU: Toggle mode PD: SDR (Default)	RG23			✓		✓	✓	✓	✓	✓			✓	✓	✓		✓	✓		✓	✓			✓		
AA	P25	Force 7ALE Search Boot Block	PU: Disable (Default) PD: Enable	RG25																									

✓：表示装上
空白：表示不装