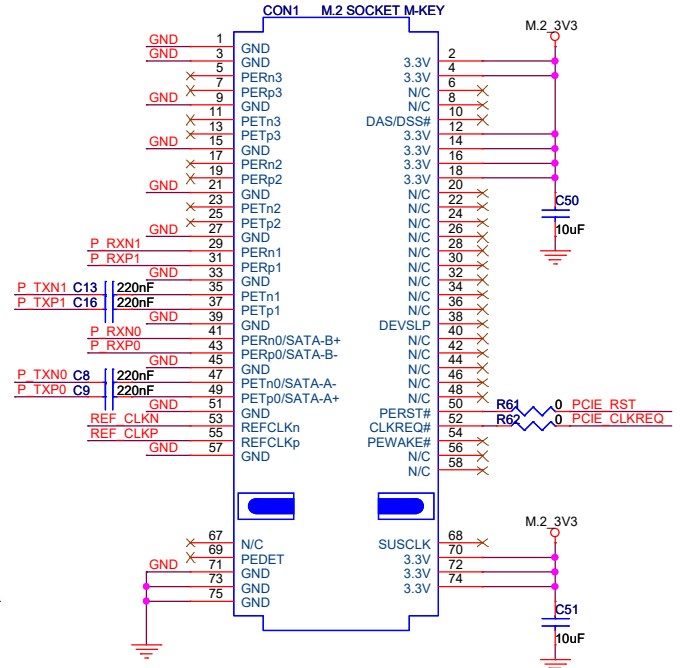
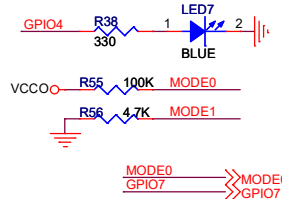
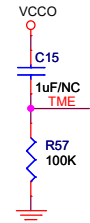
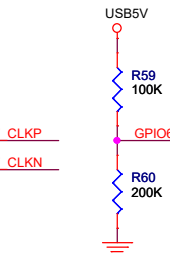
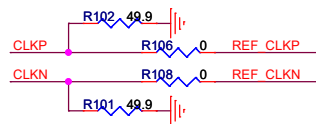
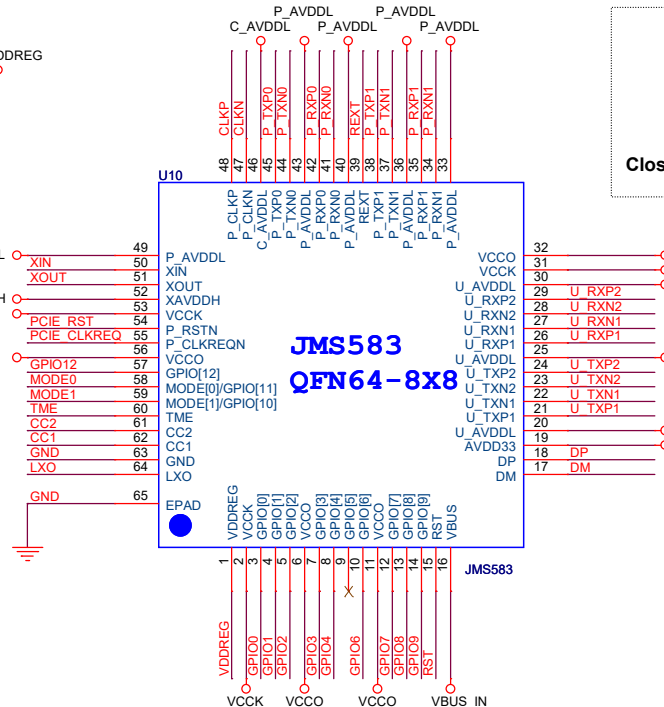
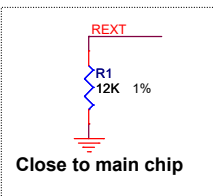
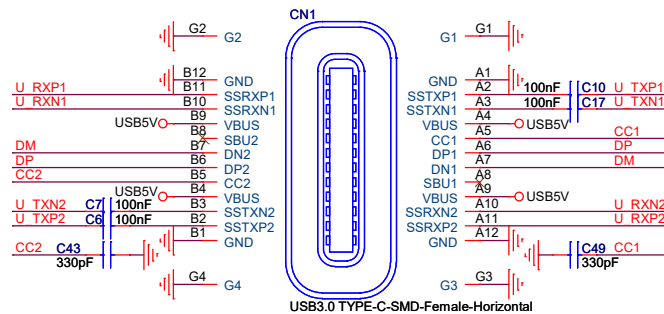
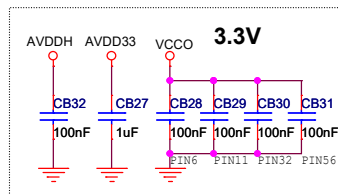
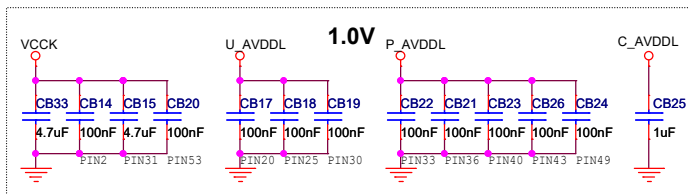
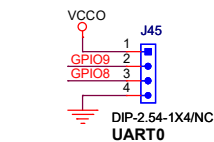
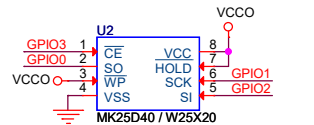
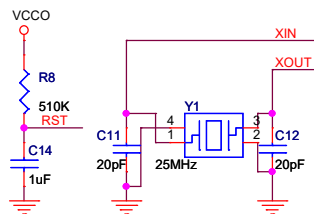
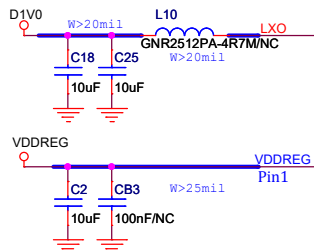


AVDD33 VCCO AVDDH
D1V0 VCCCK U AVDDL P AVDDL C AVDDL

Internal regulator



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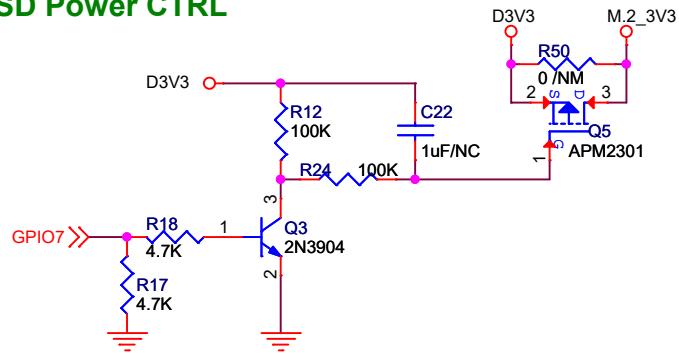
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JMS583 QFN64-8X8-BUS POWER

Title
JMS583_Main Chip

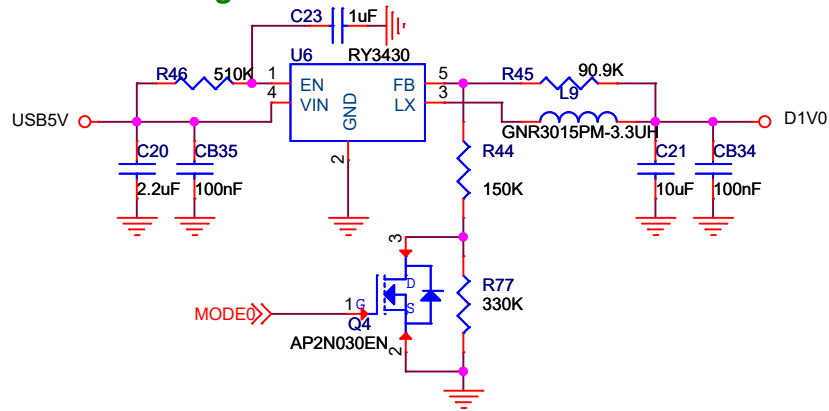
Size B Document Number SCH-18007 Rev 1.1

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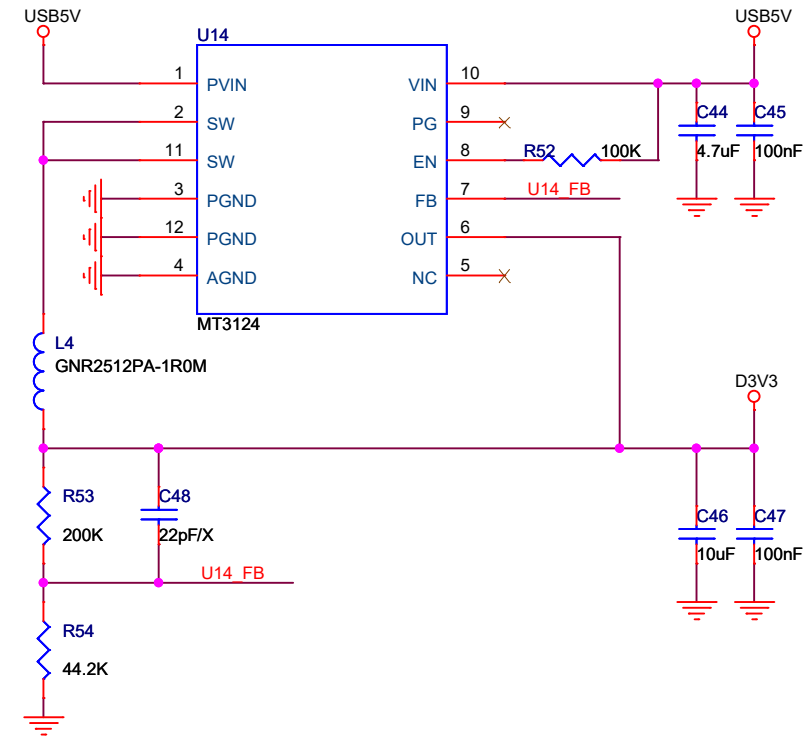
SSD Power CTRL



5V to 0.95V Regulator



5V to 3V3 Regulator



$$D3V3 = 0.6 * (1 + R53/R54) = 0.6 * (1 + 200K/44.2K) = 3.3V$$



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PCB File Name

JMS583 QFN64-8X8-BUS POWER

Title

JMS583_Main Chip

Size
A4

Document Number
SCH-18007

Rev
1.1

Date: Friday, May 18, 2018

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