



CV6600-RS232
Communication Protocol

User Manual

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1 Introduction

1.1 Purpose

This document defines a communication protocol, which will be as a generic protocol for products involving data communication with each other. Basically this generic protocol serves for communication between a HOST and one or more terminal devices.

1.2 Scope

Different aspects of the protocol will be described, which include the electrical interface, data format, and link layer. This generic protocol will be applied for

- Point to point – RS232
- Multi-drop (Point to multi-points) – RS422/RS485
- Two wires half-duplex mode and four wires full-duplex mode.

1.3 Glossary

UID – Unique Identification

LRC – Longitudinal Redundancy Check

CRC – Cyclic Redundancy Check

MAC – Message Authentication Code

ATR – Answer To Reset

1.4 Referenced Document

<Not available>

2 Physical Layer

2.1 Electrical Interface

Basically, this communication protocol does not need to be bound with any electrical interface characteristic. Typically the following types of physical link could be used:

- RS232 (Point to point only)
- CMOS-Logic Level (Point to point only)
- Half duplex, two wires RS485/RS422 (multi-drop mode supported)
- Full duplex, four wires RS485/RS422 (multi-drop mode supported)

2.2 Data Format

The data format (Start Bit, Data Bits, parity, Stop Bit) is software configurable, and can be set to match the special requirement of data transmission between two communication devices. The general data format is defined as:

Parameter	Description
Baud Rate	Selective: 9600, 19200, 38400, 57600, 1152000
Data Bits	Fixed: 8 bits
Start Bit	Fixed: 1 Bits
Stop Bit	Selective: 1 bit.
Parity	Selective: Odd, Even, None

The following is the default setting:

Baud Rate	Data Bits	Start Bit	Stop Bit	Parity
115200	8	1	1	None

The configuration for the communication setting will be stored within the reader's on board EEPROM memory and can be changed by command sent from the Host.

3 Link Layer

The communication protocol is a packet-oriented protocol - all the data exchanged between two communication devices will be based on packet format. The protocol is designed for multi-drop mode and where point-to-point mode could be treated as a special case of multi-drop mode.

The data packet starts with the control character 'STX' and ends with 'ETX', which follows the 8-bit BCC checksum. Besides the checksum is used for error checking, character (byte) time-out and packet (command) time-out are used to re-synchronise the communication.

3.1 Packet Format

There are two types of data packets. Command Message is the packet sent from the Host to the reader device. The Reply Message is the packet sent from the reader to the Host.

Packet format for Command Message (Host to Reader)

STX	SEQ	DADD	CMD	DATA LENGTH	TIME	DATA[0..N]	BCC	ETX
-----	-----	------	-----	-------------	------	------------	-----	-----

(BCC) = $SEQ \oplus DADD \oplus CMD \oplus DATALENGTH \oplus TIME \oplus DATA[0] \oplus \dots \oplus DATA[n]$, where \oplus is the "EOR".

Packet format for Reply Message (Reader to Host)

STX	SEQ	DADD	DATA LENGTH	STATUS	DATA[0..N]	BCC	ETX
-----	-----	------	-------------	--------	------------	-----	-----

(BCC) = $SEQ \oplus DADD \oplus DATA LENGTH \oplus STATUS \oplus DATA[0] \oplus \dots \oplus DATA[n]$, where \oplus is the "EOR".

The following table describes the packet fields:

Field	Length	Description	Remark
STX	1	0x02 - "Start of Text" – standard control character. It is the starting of a data packet.	
SEQ ¹	1	Packet sequence number: this field acts as an error control. Each packet sent from the Host associates with a sequence number that will be increased circularly. The reader returns the reply message with the same SEQ number. The HOST can check the SEQ for the occurrence of the 'OUT of SEQUENCE' error. Bit 7: Always set to '1' Bit 6-4: Sequence Number. Change from 0 to 7 cyclically. Bit 3-0: Extend Device Address.	The address extend bits are used to widen the device address range in case 255 addresses are not enough. ²
DADD	1	Device Address, which is used for multi-drop mode,	Address 0x00 is a

批注 [cvcheong1]: At this moment, the SEQ number is not handled.

¹ The SEQ Number is not handled at this version.

² The Address Extend Bits are reserved for future use. At this moment, the extend bits have no effect.

		only the reader (device) with matched pre-programmed device address will response the received command packet.	special address for point-to-point mode communication. The reader responds to all the packets which has a "0" address. (No Address matching checking will be made. ¹
CMD	1	Command field: the command field consists of one command byte.	Refer the Command Table for listing of commands. The CMD byte is only used for the Command Packet
DATA LENGTH	1	Length of the data bytes in the packet. The Data Length includes the TIME/STATUS and the DATA field, but not the BCC. LENGTH= Number_of_Bytes (TIME/STATUS + DATA[0..N])	
STATUS	1	Reply Status byte: The status replied from Reader to Host	This byte is only used for the Reply Packet.
TIME	1	Extend the time-out delay for special commands that the may need extra time to process. For most of the commands, this byte is set to 0.	This byte is only used for the Command Packet
DATA [0-N]	0 – 80	The Data Field is a stream of data with variable length, which depends on the Command word. There are also some COMMANDS have zero length of data field. If the Data Field of the Command/Reply Message has more then 80 bytes, the reader won't response and treats this command as an error and wait for another command.	
BCC	1	Eight-bit block check sum. The calculation of the check sum includes all the bytes within the package but excludes the STX, ETX.	
ETX	1	0x03:'END of TEXT' – standard Control Character which indicates the END of a packet. Before the ETX, an 8-bit BCC Check Sum byte is inserted for data integration checking.	

¹ A command packet with address "0" may cause communication problem if more then one readers are connected as multi-drop mode.

3.2 Data Byte Flow Control

At this moment, no flow control will be considered.

3.3 Time-out Control

A time-out mechanism is used for the packet flow control. There are two levels time-out control: byte time-out and packet time-out.

- The byte time-out is the maximum time delay between transmitted bytes. The interval may be varied from a few ms to tens of ms. (Default 30ms)
- Packet (command) Time-out: the maximum period allows for transmitting a complete packet. This time-out interval may be within the range of tens of millisecond to hundreds of millisecond. It depends on the real application and the possible largest packet size. The duration of the packet time-out is implied by the command itself. (Some commands may have shorter time-out then other commands)

4 Command Set

The commands are grouped to different categories. They are System command, I/O commands, ISO14443 standard commands, MIFARE commands, SAM commands and miscellaneous commands.

Command Table			
CMD Code ¹	Name	Description	Remark
System Commands (0x00-0x1F)			
0x06	SetAddress	Program the Device Address to the reader	
0x07	SetBaudrate	Set the reader's communication baud rate	
0x09	GetSerlNum	Get the reader's Serial Number	
0x0A	GetVerNum	Get the reader's firmware version number.	
0x0B	SetUserInfo	Set the Usr Information	A 32-byte memory space is reserved for storing the programmable user information, which could be used for customer service records or tacking of project.
0x0C	GetUserInfo	Get the User Information	
0x11,0x08	Internal Commands	Internal Commands, not open for end users	These commands are for internal testing use only. Misuse of these commands may damage the reader.
I/O Commands (0x20-0x2F)			
0x21	SetPort	Set the CV3500A I/O ports	
0x22	GetPort	Read status of the I/O ports.	
0x24	SetLED	Turn On/Off the LEDs	
0x25	ActiveLED	This command makes the selected LED blinking with a programmed time sequence.	

¹ Only CMD0 used, CMD1 always set to 0.

0x26	ActiveBuzzer	Control the buzzer to generate a sound pattern that is programmed by a set of parameters.	
0x27	RF_RST	Turn off the RF field for a time period.	The RF field will be turned on automatically after the command.
ISO14443 TTYPE A Commands (0x30-0x3F)			
0x30	REQA	ISO14443A REQUEST	
0x31	Anticoll of Cascadelevel1	ISO14443A Anti-collision	
0x32	Select of Cascadelevel1	ISO14443A Select	
0x33	Halt	ISO14443A Halt	
0x34	SLE_GEN	Send generic (transparent) command to the SLE55Rxx card	Internal Command, not open to end-user yet.
0x35	MF_PowrerSave	Let the Module Enter the Power Saving mode.	
0x38	Anticoll of Cascadelevel2	ISO14443A Anti-collision Halt	
0x39	Select of Cascadelevel2	ISO14443A Select	
0x3a	Anticoll of Cascadelevel3	ISO14443A Anti-collision Halt	
0x3b	Select of Cascadelevel3	ISO14443A Select	
Mifare Commands (0x40-0x4F)			
CMD Code	Name	Description	Remark
0x40	MF_Auth	Mifare Authentication	The key must be pre-loaded to the RC500's Master Key buffer
0x41	MF_Read	Mifare Read	
0x42	MF_Write	MIFARE Write	
0x43	MF_Transfer	MIFARE Value Block Transfer	
0x44	MF_Value	MIFARE Value related command Three sub-commands provided: increase, decrease, restore.	
0x45	MF_Loadkey	Load a key string to the Master Key buffer	

0x46	MF_LoadEEKey	Load a key from RC500's EEPROM to Master Key buffer	
0x47	MF_StoreKeyToEE	Store a key to MFRC500 chip's internal EEPROM.	
ISO14443-B Command (0x60-0x6F)			
CMD Code	Name	Description	Remark
0x60	Request_B	ISO14443B REQB command	
0x61	Anticoll_B	Start the Anticollision loop for the Type B Card	
0x62	Attrib_B	ISO14443B ATTRIB command	
0x63	Halt_B	ISO14443B Halt command	
0x65	Generic_B	ISO14443-4 transparent command Type B Card	
0x64	FoundCards	Automatically detect which type (A/B) of card is in the field.	
SAM Commands (0x70 –0x7F)			
0x70	SAM_ATR	Get Answer-to-reset String from the SAM card	Not supported yet
0x71	SetBaudRateSAM	Set the communication baud rate for SAM module interface	Not supported yet
0x72	SAM_GEN	Send generic (transparent) command to the SAM card.	Use ISO7816 T=1 protocol
Miscellaneous Commands (0xC0-0xCF)			
0x90	MF_HLRead	The High Level Read command integrates the low level commands (request, anti-collision, select, authentication, read) to achieve the reading operation with a one-step single command.	High level command
0x91	MF_HLWrite	The High Level Write command integrates the low level commands (request, anti-collision, select, authentication, write) to achieve the writing operation with a one-step single command.	High level command
0x92	MF_HLInitVal	The High Level Value Block Initialization command integrates the low level commands (request, anti-collision, select,	High level command

		authentication) to achieve the value block initialization with a one-step single command.	
0x93	MF_HLDecrement	The High Level Decrement command integrates the low level commands (request, anti-collision, select, authentication) to achieve the Decrement with a one-step single command.	High level command
0x94	MF_HLIncrement	The High Level Increment command integrates the low level commands (request, anti-collision, select, authentication) to achieve the Increment with a one-step single command.	High level command
0x98	MF_HLRequest	The High Level Request command integrates the low level commands (request, anticoll, select, anticoll2, select2, anticoll3, select3) to achieve the select card with a one-step single command.	High level command
<i>Debug Commands - For Internal use only (0xD0-0xDF)¹</i>			
<i>0xD0</i>	<i>ReadEEPROM</i>	<i>Read data from the RC500's internal EEPROM.</i>	<i>Only using for interior debug, no open to customer.</i>
<i>0xD1</i>	<i>WriteEEPROM</i>	<i>Write data to the RC500's internal EEPROM.</i>	<i>Only using for interior debug, no open to customer.</i>
<i>0xD2</i>	<i>EraseEEPROM</i>	<i>Erasing the RC500's internal EEPROM.</i>	<i>Only using for interior debug, no open to customer.</i>

¹ The debugging commands will subject to change without notice.

4.1 System Commands

4.1.1 SetAddress (0x06)

Data Field

DATA[0]: Enable Serial Number Checking
 0x00 – Don't check the Serial Number.
 0x01 – Check the Serial Number.

DATA[1]: The new Device (Reader) Address to be set

DATA[2..9] Reader Serial Number.

Response:

STATUS: 0x00 – OK

Data Field

DATA[0] The programmed device address.

Description

Program a device address to the reader and returns new device address. In order to program the device address successfully, the correct reader's pre-programmed Serial Number must be submitted. (unless the Serial Number Checking Flag is disabled).

4.1.2 SetBaudrate (0x07)

Data Field

DATA[0] Communication speed
 0x00 – 9600 bps
 0x01 – 19200 bps
 0x02 – 38400 bps
 0x03 – 57600 bps
 0x04 – 76800 bps
 0x05 – 115200 bps

Response:

STATUS: 0x00 - OK

Data Field

DATA[0] Return the new communication speed.
 0x00 – 9600 bps
 0x01 – 19200 bps
 0x02 – 38400 bps
 0x03 – 57600 bps
 0x04 – 76800 bps
 0x05 – 115200 bps

Description

Set the reader's baud rate for host communication. The baud rate will be stored in the reader's EEPROM and used as the new default baud rate.

4.1.3 GetSerNum (0x09)

Data Field N/A

Response:

STATUS: 0x00 – OK

Data Field

DATA[0..7]: 8-byte Reader Serial Number

DATA{8..9}: Device Address

Description

Get the Device Address and the Serial Number from the reader. The 8-byte serial number is pre-programmed by the factory.

4.1.4 GetVerNum (0x0A)

Data Field N/A

Response:

STATUS: 0x00 – OK

Data Field

DATA[0]: Device Address

DATA[1..N]: The Version Number of the Reader's Firmware

Description

Get the reader's firmware version number.

4.1.5 SetUserInfo (0x0B)

Data Field

DATA[0..31] 32 bytes user information.

Response:

STATUS: 0x00 – OK

Data Field n.a.

Description

Program the 32 bytes information to the reader.

4.1.6 GetUserInfo (0x0C)

Data Field N/A

Response:

STATUS: 0x00 – OK

Data Field

DATA{0..31} 32 bytes of user information returned

Description

Get the 32-byte user information from the reader.

4.2 I/O Commands

4.2.1 SetPort (0x21)

Data Field:

DATA[0] : Set the I/O ports to the corresponding state.

Bit0 = Port0
Bit1 = Port1
Bit2 = Port2
Bit3 = Port3

Response

STATUS: 0x00 –OK

Data Field N/A

Description

Set the I/O ports with the corresponding states.

4.2.2 GetPort (0x22)

Data Field

None

Response:

STATUS: 0x00 - OK

Data Field

DATA[0]: Status of the I/O ports
Bit0: input state of Port0
Bit1: input state of Port1
Bit2: input state of Port2
Bit3: input state of Port3
Bit4: output state of Port0
Bit5: output state of Port1
Bit6: output state of Port2
Bit7: output state of Port3

STATUS: 0x00 - OK

Description:

Get the status of the I/O ports.

Note: The reader equipped with four I/O ports for general purpose. This command reads the states of the I/O ports.

4.2.3 SetLED (0x24)

Data Field

DATA[0]: The two LEDs are turned on/off according to the corresponding bits
Bit 0 - Red LED (1=on, 0= off)
Bit 1 - Green LED (1= on, 0= off)

Response:

STATUS: 0x00 - OK

Data Field: N/A

Description:

Turn on/off the LEDs.

4.2.4 ActiveLED (0x25)

Data Field

DATA[0]: Select LED

0x01 - Red LED

0x02 - Green LED

0x03 – Both Red & Green LED.

DATA[1]: Units of on time. Each unit is 100ms.

DATA[2]: Number of cycles to turn on/of the LED. The cycle time is one second.

0xFF will toggle the LED continually.

Response:

STATUS: 0x00 - OK

Data Field : N/A

Description:

The selected LED will be toggled (turn on and off) with the cycles selected and the on time is defined by DATA[1]. For example, if DATA[1] = 4 and DATA[2] = 3, the selected LED will be toggled three times and each time the LED will be turned on for 400ms.

4.2.5 ActiveBuzzer(0x26)

Data Field

DATA[0]: Mode Control

0 – Turn off the buzzer

1 – Turn on the buzzer.

4 – Play a sound pattern. The sound pattern is a sequence of on-off-on-off sound and controlled by DATA[1..5].

DATA[1]: Units of first on time. Each unit is 100ms.

DATA[2]: Units of first off time. Each unit is 100ms.

DATA[3]: Units of second on time. Each unit is 100ms.

DATA[4]: Units of second off time. Each unit is 100ms.

DATA[5]: Cycle times.

Response:

STATUS: 0x00 - OK

Data Field: N/A

Description:

Control the buzzer to play sound patterns.

4.2.6 RF_RST (0x27)

Data Field:

DATA[0]: The units of time to turn off the RF field. Each time unit is equal 100us.

Response:

STATUS: 0 – OK.

DATA Field: N/A

Description:

The RF field will be turn off (reset) and then the field will be turned on again. The value 0x00 will turn off the field forever until another RF_RST is issued.

4.2.7 TriggerPort (0x20)

Data Field:

DATA[0] : I/O port number. Port0 =0, Port1 = 1, Port 2 = 2

DATA[1]: Units (1 to 254) of turn on time. Each unit is 100ms.

Response

STATUS : 0x00 –OK

Data Field N/A

Description

Turn on the selected I/O port for a time period. The I/O port will be turn off after the time period.

Data Field:

DATA[0] : I/O port number. Port0 =0, Port1 = 1, Port 2 = 2

DATA[1]: Units (1 to 254) of turn on time. Each unit is 100ms.

Response

STATUS : 0x00 –OK

Data Field N/A

Description

Turn on the selected I/O port for a time period. The I/O port will be turn off after the time period.

4.3 ISO14443 Type-A Commands

4.3.1 REQA (0x30)

Data Field

DATA[0]: Request mode
0x26 – Request Idle
0x52 – Request All (Wake up all)

Response:

STATUS: 0x00 - OK
DATA[0..1]: The two-bytes ATQ response from the card.

Description

Send the ISO14443 A REQUEST command to the card.

4.3.2 Anticoll of Cascadelevel (0x31)

Data Field: N/A

Response:

STATUS: 0x00 - OK

Data Field

DATA[0..3]: UID – the card serial number
DATA[4]: Multi-card flag.
0x00 - One card detected.
0x01 - Multiple cards detected.

Description:

Execute the ISO14443 Type A Anti-collision loop of cascadelevel1. The card's UID (serial number) of cascadelevel1 will be returned. If more than one card is detected in the field, the Multi-Card Flag will be set.

Note: only cards not halted will be detected by the Anti-collision loop.

4.3.3 Anticoll of Cascadelevel2 (0x38)

Data Field: N/A

Response:

STATUS: 0x00 - OK

Data Field

DATA[0..3]: UID – the card serial number
DATA[4]: Multi-card flag.
0x00 - One card detected.
0x01 - Multiple cards detected.

Description:

Execute the ISO14443 Type A Anti-collision loop of cascadelevel2. The card's UID (serial number) of cascadelevel2 will be returned. If more than one cards are detected in the field, the Multi-Card Flag will be set.

Note: only cards not halted will be detected by the Anti-collision loop.

4.3.4 Anticoll of Cascadelevel3 (0x3a)

Data Field: N/A

Response:

STATUS: 0x00 - OK

Data Field

DATA[0..3]: UID – the card serial number

DATA[4]: Multi-card flag.

0x00 - One card detected.

0x01 - Multiple cards detected.

Description:

Execute the ISO14443 Type A Anti-collision loop of cascadelevel3. The card's UID (serial number) of cascadelevel3 will be returned. If more than one cards are detected in the field, the Multi-Card Flag will be set.

Note: only cards not halted will be detected by the Anti-collision loop.

4.3.5 Select of Cascadelevel 1(0x32)

Data Field

DATA[0..3]: UID – the UID of the card to be selected.

Response:

STATUS: 0x00 - OK

0x46 - OK, but need next anticoll-select loop

Data Field

DATA[0..3]: UID – the UID of the card to be selected.

Description:

ISO14443 A SELECT of Cascadelevel1 command.

4.3.6 Select of Cascadelevel 2(0x39)

Data Field

DATA[0..3]: UID – the UID of the card to be selected.

Response:

STATUS: 0x00 - OK

Data Field

DATA[0..3]: UID – the UID of the card to be selected.

Description:

ISO14443 A SELECT of Cascadelevel2 command.

4.3.7 Select of Cascadelevel 3(0x3b)

Data Field

DATA[0..3]: UID – the UID of the card to be selected.

Response:

STATUS: 0x00 - OK

Data Field

DATA[0..3]: UID – the UID of the card to be selected.

Description:

ISO14443 A SELECT of Cascadelevel3 command.

4.3.8 Halt (0x33)

Data Field N/A

Response:

STATUS: 0x00

Data Field: N/A

Description:

ISO14443 A Halt command

4.3.9 CMD_SLE_Gen (0x34)

CTLR: CRC Enable Flag.

0x00 – No CRC checksum will be calculated and appended.

0x01 – The CRC checksum will be calculated and appended.

Data Field

DATA[0]: Data length to be transmitted.

DATA[1..N] Data to be transmit.

Response:

STATUS : 0x00 - OK

Data Field

DATA[0] The length of the data returned from the SLE55Rxx card.

DATA[1..N] Data returned from the SLE55Rxx card.

Description:

Send a transparent command to the SLE55Rxx. The 16-bit CRC checksum will be appended automatically if the CRC Enable flag is set.

4.4 MIFARE Commands

4.4.1 MF_Auth (0x40)

Data Field

DATA[0] Authentication Mode – Select Key A mode or Key B mode
 0x60 – the KEY will be used as KEYA for authentication
 0x61 – the KEY will be used as KEYB for authentication.

DATA[1..4] UID (Card Serial Number)

DATA[5] The Address of the memory block to be authenticated

Response:

STATUS: 0x00 - OK

Data Field: N/A

Description:

Execute a mutual authentication between the MIFARE card and the reader. The read/write/value commands only can be executed after the successful authentication. The corresponding authentication key should be loaded to the Master Key Buffer by the LOADKEY command. The Authentication Mode determines the key loaded acts as KEY A or KEY B.

Note: before execute the AUTH command, the corresponding key must be loaded to the Master Key Buffer. (either by the LoadKey or LoadEEKey command)

4.4.2 MF_Read (0x41)

Data Field

DATA[0]: Starting Block address

DATA[1]: number of blocks to be read (max. 4 blocks.)

Response:

STATUS: 0x00 - OK

Data Field

DATA[0 .. N]: Data read from the MIFARE card.

Description:

Read multiple memory blocks from the MIFARE Card. Up to four blocks could be read by a single command.

4.4.3 MF_Write (0x42)

Data Field

DATA[0]: Starting Block address

DATA[1]: number of blocks to be written (max. 4 blocks.)

DATA[2..N] Data to be written

Response:

STATUS: 0x00 - OK

Data Field N/A

Description:

Write multiple memory blocks to the MIFARE Card. The number of blocks could be up to four blocks.

4.4.4 MF_Transfer (0x43)

Data Field

DATA[0]: Address of the value block

Response:

STATUS: 0x00 - OK

Data Field

Description:

Transfer command for the MIFARE card. Transfer the value (updated by the increment/decrement command) from the MIFARE chip's internal value buffer to the real value block.

4.4.5 MF_Value (0x44)

Data Field

DATA[0]: Mode – function select - increment/decrement/restore

0xC0: Decrement

0xC1: Increment

0xC2: Restore

0xC3: ReadValue

Read back the value (not in value block format) from the value block

0xC4: InitValue

Initialize a value block.

DATA[1]: Address of the value block

DATA[2..5]: Value – the value to be increased, decreased or initialize. A zero value should be used for RESTORE.

Response:

STATUS: 0x00 - OK

Data Field

DATA[0-3] Return value, only valid for the ReadValue sub-command, the returned value will be always zero for other sub-commands.

Description:

MIFARE's value block related functions. There are five sub-functions (restore, increment, decrement, initialize value, read value) under the VALUE command.

4.4.6 MF_LoadKey (0x45)

Data Field

DATA[0..5]: The un-coded 6-bytes key string. (i.e. A0A1A2A3A4A5)

Response:

STATUS: 0x00 – OK

Data Field

Description:

Load the Key string directly to the Master Key buffer of the MFRC500 chip.

4.4.7 MF_LoadEEKey (0x46)

Data Field

DATA[0]: KEYA_B : Select to load KeyA or KeyB

0x60: Load KeyA to the Master Key Buffer

0x61: Load KeyB to the Master Key Buffer

DATA[1]: Sector/Key Number [00..15]. (The number/sector of the key to be loaded)

Response:

STATUS: 0x00 – OK

Data Field

Description:

Load the Master Key from MFRC500's EEPROM to the Master Key Buffer. Same as the LoadKey command but the key is loaded from the MFRC500's EEPROM instead of using the un-coded key string.

4.4.8 MF_StoreKeyToEE (0x47)

Data Field

DATA[0]: KEYA_B : Select to load KeyA or KeyB

0x60: Load KeyA to the Master Key Buffer

0x61: Load KeyB to the Master Key Buffer

DATA[1]: Sector/Key Number [00..15]. (The sector/key number key to be stored)

DATA[2..7]: un-coded 6-byte key string (i.e. A0A1A2A3A4A5)

Response:

STATUS: 0x00 – OK

Data Field

Description:

Store the Key to MFRC500's internal EEPROM.

4.5 ISO14443 Type-B Commands

4.5.1 CMD_Request_B (0x60)

Data Field

DATA[0]: The AFI (Application Family Identifier). Only cards with the matched AFI may answer to the REQB command. When AFI equals “00”, all the cards shall process the REQB command

DARA[1] The PARAM of Request B command. This parameter defines the SLOT number (the probability of response). Please refer the ISO14443 –Part 3. (Chapter 7.74 – Coding of PARAM) for details.

Response:

STATUS: 0x00 - OK

DATA[0]: Length of the returned ATQB string. For a successful REQB command, normally a 14 bytes ATQB string will be returned.

DATA[1..N] The returned ATQB string from the card.

Description

Send the ISO14443 B REQB command.

4.5.2 CMD_Anticoll_B (0x61)

Data Field: N/A

Response:

STATUS: 0x00 - OK

Data Field

DATA[0] Multi-card Flag

0x00 – Only one card detected within the field.

0x01- More then one card detected within the field.

Note: only the cards which are not in “HALT” state could be detected.

DATA[1..14] 14 bytes ATQB string

Description:

Run the anticollision loop and pick one TYPE B card. The ATQB string of the selected card will be returned. The multi-card flag will be set in case more than one card is found within the field

4.5.3 CMD_ATTRIB (0x62)

Data Field

DATA[0..3]: UID – the UID (Card Serial Number) of the card to be select.

DATA[4] The CID (Logical Channel ID) to be assigned to the card.

Response:

STATUS: 0x00 - OK

Data Field N/A

Description:

The simplified ISO14443 B ATTRIB command. The CID will be assigned to the selected card for further communication. Only the UID and CID are needed as parameters, the other parameters (such as param1 to 3 and the Higher Layer IINF) defined in the ISO14443-3 chapter 7.10.1 are ignored.

4.5.4 CMD_HaltB (0x63)

Data Field

DATA[0..3]: UID of the card to be halted

Response:

STATUS: 0x00

Data Field: N/A

Description:

ISO1444B Halt command.

4.5.5 CMD_Generic_B (Reserved)

Data Field

DATA[0]: Length of data to be sent to the card. (The Header and the APDU body included the CRC checksum is not counted.)

DATA[1..N] The transparent command packet sent to the card according to the ISO14443 Part 4. Typically the packet includes the Header (PCB, CID and NAD) and the APDU body. The CRC checksum will be appended automatically.

Response:

STATUS : 0x00 - OK

Data Field

DATA[0] The length of the data returned from card.

DATA[1..N] The response returned from card

Description:

Send a transparent command packet to the Type-B card and get the response back according to the ISO14443 Part 4. Typically, the transparent command included the Header (PCB, CID and the optional NAD) and the APDU body. A type B CRC checksum will be appended automatically.

The Type B low level commands such as REQB, SLOT-MARKER, ATTRIB and HALT also could be sent by the Generic_B command.

4.5.6 CMD_FoundCards (0x64)

Data Field NA

Response:

STATUS : 0x00 - OK

Data Field

DATA[0] The length of the ATQ string returned from card.

DATA[1..N] The ATQ string replied from card. In normal case, a two-bytes ATQ string will be returned if Type A card is detected. For Type B card, the ATQ string will be 14 bytes.

Description:

In order to detect what kind of cards are available within the field, the FoundCards command will send the REQA and REQB command alternatively. The corresponding ATQ string will be returned according to the type of the card detected. If there are both Type A card and Type B card within the field, the Type A card always has the priority.

4.6 SAM Commands

4.6.1 CMD_SetBaudrateSAM (0x71)

Data Field

DATA[0]: Communication speed.

 0x00 - 9600bps

 0x01 - 19200bps

 0x02 -38400bps

 0x03 - 57600bps

 0x04 - 76800bps

 0x05 - 115200bps

Response:

STATUS: 0x00 - OK

Data Field: N/A

Description:

Set the communication speed of the SAM interface.

Note: This command only set the baud rate for the reader's SAM (ISO7816) interface. The baud rate of the SAM card is also needed to be set by the PTS (Protocol Type Select) which is defined in the ISO 7816 standard.

4.6.2 CMD_SAM_ATR (0x70)

Data Field: N/A

Response:

STATUS:

Data Field: The Answer to reset string.

DATA[0..N]: The Answer to reset string, which is a variable length string of characters.

Description:

Get the Answer to Reset String from the SAM card. The SAM_ATR command will also reset the SAM card.

4.6.3 CMD_SAM_Gen (0x72)

CTLR : LRC Enable Flag .

 0x00 – No LRC checksum will be calculated and appended.

 0x01 – The LRC checksum will be calculated and appended.

Data Field

DATA[0]: The data length of the INFORMATION field in the T1 frame. The LRC is included if the LRC Flag is enabled.

DATA[1..N] The INFORMATION field of the T1 Frame.

Response:

STATUS : 0x00 - OK

Data Field

DATA[0] The length of the data returned from the SAM card. (Only the INFORMATION field of the T1 frame.)

DATA[1..N] The INFORMATION field returned from the SAM card.

Description:

Send a transparent T1 command to the SAM card. Only the INFORMATION field is needed. The reader will assemble the complete T1 frame by adding the header and the LRC checksum. (If the LRC Enable Flag is set)

This command performs all SAM specific commands (e.g. SELECT MF, ASK PARAMETER, SELECT KEYSET...)

4.7 High Level MIFARE Commands

4.7.1 CMD_MF_HLRead (0x90)

Data Field

DATA[0]: Mode Control

Bit0 : Request Mode. 0=Request Idle, 1 = Request All

Bit1 : Compare Card Serial Number.

If this bit is enable, the detected card serial number will be compared with the submitted card serial number (DATA[3-6]). The high level command will be continued only if the serial number is matched.

0=Enable, 1=Disable.

Bit2 : Key Select. Select use KeyA or Key B for Authentication

0=KeyA, 1=KeyB

Bit3-7 : Reserved for future used.

DATA[1]: Number of blocks to be read (Max 4)

DATA[2]: The Start Address of blocks to be read.

DATA[3-6]: Card Serial Number (LL LH HL HH)

The Serial Number will be ignored if Bit 1 of Mode Control (DATA[0]) is not set.

Response:

Data Field

STATUS: 0x00 – OK

DATA[0-3]: Card Serial Number (LL LH HL HH)

DATA[4..N] Data read from the card.

Description:

The High Level Read Command integrates the low level commands (Request, Anti-Collision, Select, Authentication, ..) and let the user to open the card and read data from the memory blocks by a single command. The high level command select the key stored in the RC500's internal EEPROM according to the block address. (i.e- Sector Number) and the Mode Control parameter determine the behavior of the high level command.

4.7.2 CMD_MF_HLWrite (0x91)

Data Field

DATA[0]: Mode Control – Refer the CMD_MFRead Command

DATA[1]: Number of blocks to be written (Max 4)

DATA[2]: The Start Address of blocks to be written.

DATA[3-6]: Card Serial Number (LL LH HL HH)

The Serial Number will be ignored if Bit 1 of Mode Control (DATA[0]) is not set.

DATA[7-N]: Data to be written to the memory blocks.

Response:

Data Field

STATUS: 0x00 – OK

DATA[0-3]: Card Serial Number (LL LH HL HH)

Description:

The High Level Write Command integrates the low level commands (Request, Anti-Collision, Select, Authentication, ..) and let the user to open the card and write data to the memory blocks by a single command. The high level command select the key stored in the RC500's internal EEPROM according to the block address. (i.e- Sector Number) and the Mode Control parameter determine the behavior of the high level command.

4.7.3 CMD_HL_InitVal (0x92)

Data Field

DATA[0]: Mode Control – Refer the CMD_MFRead Command

DATA[1]: Reserved for future used. Set to 0 as default value

DATA[2]: The Sector used for Value storage.

Block0 –Opened for user use.

Block1 –Value Stored Block

Block2 –Value Backup Block.

DATA[3-6]: Card Serial Number (LL LH HL HH)

The Serial Number will be ignored if Bit 1 of Mode Control (DATA[0]) is not set.

DATA[7-10]: The initial value to be stored to the value block. (Value format : LL LH HL HH)

Response:

Data Field

STATUS: 0x00 – OK

DATA[0-3]: Card Serial Number (LL LH HL HH)

Description:

The High Level Value Initialization Command integrates the low level commands (Request, Anti-Collision, Select, Authentication,) and let the user to initialize a sector for value storage use. The high level command select the key stored in the RC500's internal EEPROM according to the Sector number. The Mode Control parameter determines the behavior of the high level command.

4.7.4 CMD_HL_Decrement (0x93)

Data Field

DATA[0]: Mode Control – Refer the CMD_MFRead Command

DATA[1]: Reserved for future used. Set to 0 as default value

DATA[2]: The Sector Number of the Value Sector.

DATA[3-6]: Card Serial Number (LL LH HL HH)

The Serial Number will be ignored if Bit 1 of Mode Control (DATA[0]) is not set.

DATA[7-10]: The value to be decreased to the value block. (Value format: LL LH HL HH)

Response:

Data Field

STATUS: 0x00 – OK

DATA[0-3]: Card Serial Number (LL LH HL HH)

DATA[4-7]: Value after decreased (LL LH HL HH)

Description:

The High Level Value Decrement Command integrates the low level commands (Request, Anti-Collision, Select, Authentication, ..) and let the user to decrease the selected value.

Note: The selected sector should be pre-initialized by the High Level Value Initialization command.

4.7.5 _CMD_HL_Increment (0x94)

Data Field

DATA[0]: Mode Control – Refer the CMD_MFRead Command

DATA[1]: Reserved for future used. Set to 0 as default value

DATA[2]: Sector Number of the value sector.

DATA[3-6]: Card Serial Number (LL LH HL HH)

The Serial Number will be ignored if Bit 1 of Mode Control (DATA[0]) is not set.

DATA[7-10]: The value to be increased to the value block. (Value format: LL LH HL HH)

Response:

Data Field

STATUS: 0x00 – OK

DATA[0-3]: Card Serial Number (LL LH HL HH)

DATA[4-7]: Value after increased (LL LH HL HH)

Description:

The High Level Value Increment Command integrates the low level commands (Request, Anti-Collision, Select, Authentication,) and let the user to increase the selected value sector.

Note: The selected sector should be pre-initialized by the High Level Value Initialization command.

4.7.6 CMD_HL_Request (0x98)

Data Field

DATA[0]: Request mode
0x00 – Request Idle
0x01 – Request All (Wake up all)

Response:

Data Field

STATUS: 0x00 – OK
DATA[0-10]: Card Serial Number

Description:

The High Level Value Increment Command integrates the low level commands (Request, AntiColl1, Select1, Anticoll2, Select2, Anticoll3, Select3) and get the SNR of selected card (4bytes for selectlevel1, 7bytes for selectlevel2, 10bytes for selectlevel3).

System Error/Status Codes (0x00-0x0F)

OK	0x00	Command OK. (success)
PARA_ERR	0x01	Parameter value out of range error
TMO_ERR	0x04	Reader reply time out error
SEQ_ERR	0x05	Communication Sequence Number out of order
CMD_ERR	0x06	Reader received unknown command
CHKSUM_ERR	0x07	Communication Check Sum Error
INTR_ERR	0x08	Unknown Internal Error

Card Error/Status Codes (0x10-0x1F)

NOTAG_ERR	0x11	No card detected
CRC_ERR	0x12	Wrong CRC received from card
PARITY_ERR	0x13	Wrong Parity Received from card
BITCNT_ERR	0x14	Wrong number of bits received from the card
BYTECNT_ERR	0x15	Wrong number of bytes received from the card
CRD_ERR	0x16	Any other error happened when communicate with card

MIFARE Error/Status Codes (0x20-0x2F)

MF_AUTHERR	0x20	No Authentication Possible
MF_SERNRERR	0x21	Wrong Serial Number read during Anti-collision.
MF_NOAUTHERR	0x22	Card is not authenticated
MF_VALFMT	0x23	Not value block format
MF_VAL	0x24	Any problem with the VALUE related function

Type-B Card Error/Status Codes (0x30-0x3F)

<To be defined>

SAM Error/Status Codes (0x40-0x4F)

<To be defined>

<Revised later>

7 Revision History

7.1 UM-Comm-CV-V204

- Date 05/07/2003
- Add AnticollLevel2,3 & SelectLevel2,3 & HighLevel Request command

7.2 UM-Comm-CV-V203

- Date 04/25/2003
- Correct the mistakes in the following commands:
 - CMD SetAddress (0x06) - swap Data[0] and Data[1]
 - CMD SetBaudRate (0x07) – the new baud rate will be returned in the reply message
 - CMD RF_RST (0x27) – the reset time of the RF field is fixed.
 - CMD Select (0x32) - the card number will be returned in the reply message
 - High Level Mifare Commnads: the parameter “MODE”
 - Bit 0 - “REQUEST ALL” Flag
 - Bit 2 – “KeyA/B” Flag

7.3 UM-Comm-CV-V202

- Date 03/02/2003
- Error correction
- Add Cover Sheet and Table of Contents

7.4 UM-Comm-CV-V201

- Date 24/02/2003
- Add the High Level Commands

7.5 UM-Comm_CV-V000

- Date 26/09/2002 Ver0.01 (Internal Draft)
- Date 23/09/2002 Ver0.00 (First Draft)
- Reference: Communication Protocol for the SR170 Reader (UM-Comm-ProtocolV2.02)