Résumé

Hoang Gia NGUYEN

Date of Birth: 22/09/1990
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Citizenship: Vietnamese

Education

2015–2018 **PhD of Software Engineering**, *LIPN Laboratory*, Paris 13 University, Villetaneuse, France.

2012–2014 **Master of Software Engineering**, *Bordeaux University*, French Universities Program – Vietnam National University, Ho Chi Minh City, Vietnam.

2008–2012 **Bachelor of Software Engineering**, *Hoa Sen University*, Ho Chi Minh City, Vietnam.

2005–2008 **Gifted student of mathematics, physics and chemistry**, *Nguyen Chi Thanh High School*, Ho Chi Minh City, Vietnam.

Master Thesis

Title *Efficient Parametric Verification of Real-Time Systems*

Supervisors Assoc. Prof. Étienne ANDRÉ and Prof. Laure PETRUCCI

Cooperative Supervisors Assoc. Prof. Camille COTI and Assoc.Prof. Sami EVANGELISTA

Achievements

- **2014:** 1st class student in 2nd year of master (M2) with GPA: 15.6
- **2013:** 2nd class student 1st year of master (M1) with GPA: 14.4
- **2008-2012:** received Bachelor degree with GPA: 2.84/4.0

Publications

- Étienne André, Hoang Gia Nguyen and Laure Petrucci. **Efficient parameter synthesis using optimized state exploration strategies**. 22nd International Conference on Engineering of Complex Computer Systems,IEEE CPS ICECCS 2017.
- Étienne André, Hoang Gia Nguyen, Laure Petrucci and Sun Jun. **Parametric model checking timed automata under non-Zenoness assumption**. 9^{th} NASA Formal Methods Symposium NFM 2017.
- Étienne André, Giuseppe Lipari, Hoang Gia Nguyen and Youcheng Sun. **Reachability Preservation Based Parameter Synthesis for Timed Automata**. 7^{th} NASA Formal Methods Symposium NFM 2015.

- Étienne André, Camille Coti, Hoang Gia Nguyen, **Enhanced Distributed Behavioral Cartography of Parametric Timed Automata**, 17^{th} International Conference on Formal Engineering Methods ICFEM 2015.

Talk at International Event

- **Enhanced Distributed Behavioral Cartography of Parametric Timed Automata**, SynCoP 2015, April 2015, London, UK

Skills

Formal Methods Model Checking, Theorem Proving, Parametric Timed Automata, Temporal Logic, Petri Nets, Event-B

Model Checker Altarica-Studio, Rodin IDE, IMITATOR, SPIN, PAT, Upaal

Languages C, C++, C#, Java, Ocaml, python, T-SQL, PL/SQL, Prolog, HTML, XML, JavaScript/AJAX, CSS, batch, shell

Databases Oracle Database, MS SQL, ODBMS, MySQL

OOAD/OOP & Tools Object Oriented Analysis (OOA), Object Oriented Design (OOD), Object Oriented Programming (OOP), Unified Modeling Language (UML) ; IBM Rational Rose, Sparx, Visual Paradigm, Enterprise Architect, ArgoUML

Experience

Teaching

2015 – 2017 **Tutor**.

Courses:

- Application for network and telecommunication
- Database foundation
- Networking foundation

Vocational

2014 – 2015 **Intern**, ***LIPN Laboratory*** - ***Paris 13 University***, Paris - France.

Working with: Associate Prof. Étienne André, Prof. Laure Petrucci, Associate Prof. Sami Evangelista, Associate Prof. Camille Coti;

Main Works:

- Design and optimize new algorithms that can be executed on clusters;
- Work on new theoretical work on model checking parametric timed automata;

2014 – 2015 **Member**, ***SAVE*** - *Laboratory of Systems Analysis and VErification*, Ho Chi Minh City - Vietnam.

Member of the SAVE research Laboratory.

2010 – 2012 **System Administrator**, ***Hoang Cuong Electronic***, Ho Chi Minh City - Vietnam.

Did system administration work.

Main Duties:

- Administrate the servers;
- Design, develop and manage company e-commercial website;

Languages

Vietnamese	**Native**	*Mother Tongue*
English	**Daily practice**	*All work performed in English*
French	**Intermediate**	*Learning since 2015*

Interests

Research Model Checking and Verification, Distributed Computing, Artificial Intelligence, Data Mining and Big Data.

Leisure Exploring new technology, Swimming, hiking, reading books, watching movies