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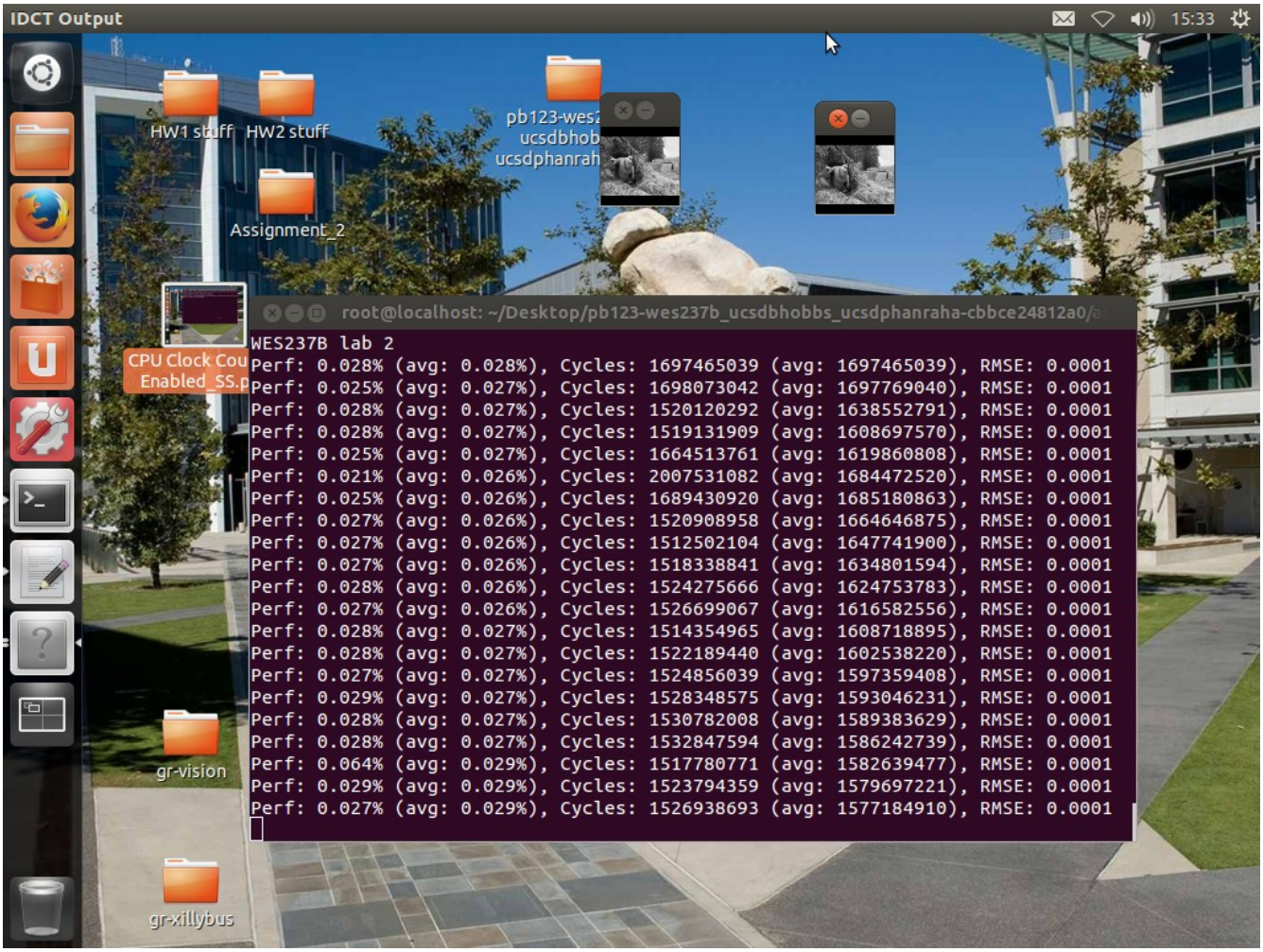
WES237b
Assignment 2 Report

Part 1: Look Up Table (LUT)

1. Performance and Cycle Count for an input size of 64 without optimizations:
    Performance Average: 0.029%
    Cycle Avg: 1,577,184,910

Figure 1: Naive DCT approach without optimizations, with input image Size 64

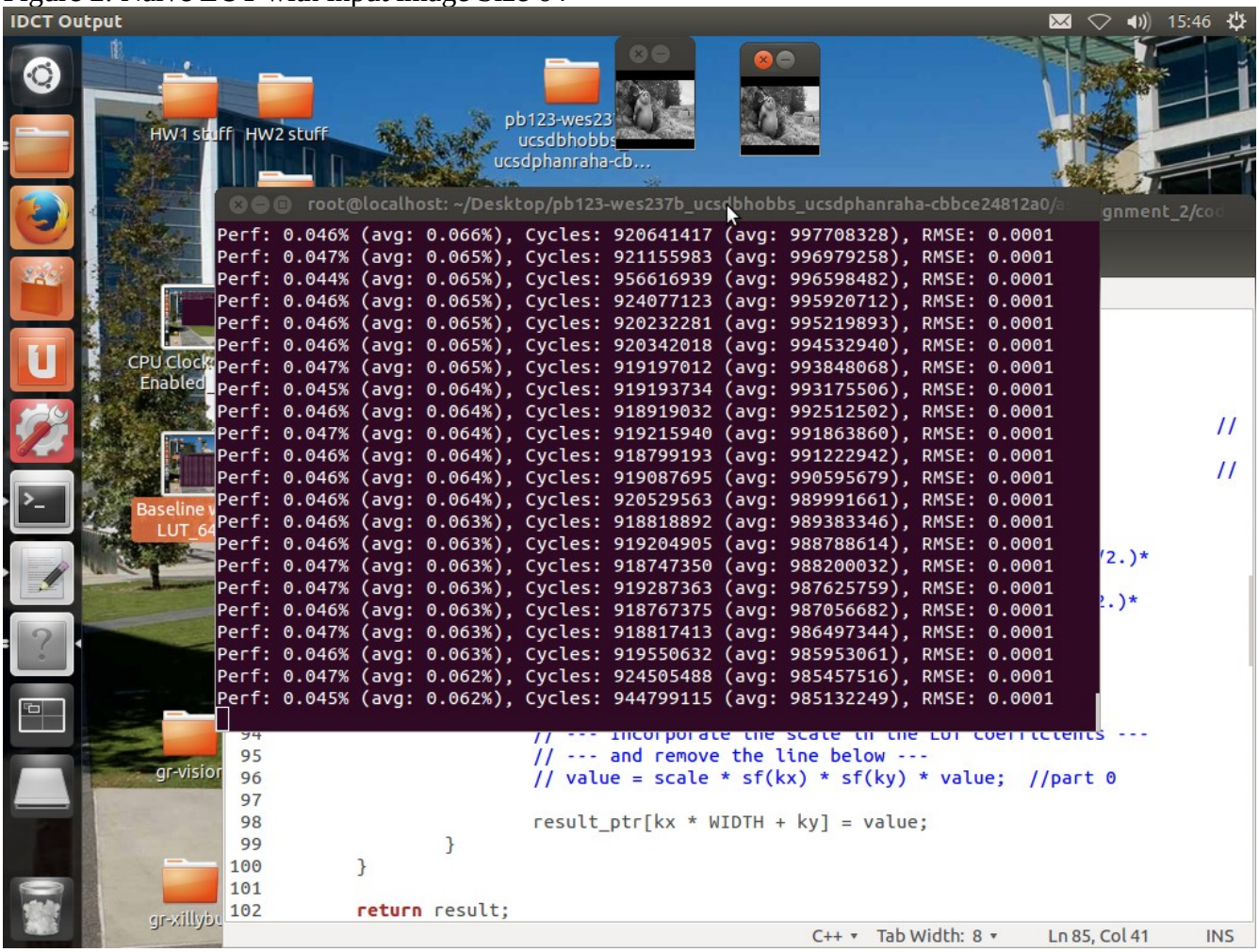


2-4. Complete LUT code with scaling may be found in our repository in the assignment_2/code/src directory.

5. Performance and Cycle Count for an input size of 64 with LUT**:
    Performance Average: 0.060%
    Cycles Avg: 978,108,351

***Note: Please uncomment the "Part 1 Naive LUT implementation" to test.*

Figure 2: Naive LUT with input image Size 64



Part 2: Matrix Multiplication (MM)

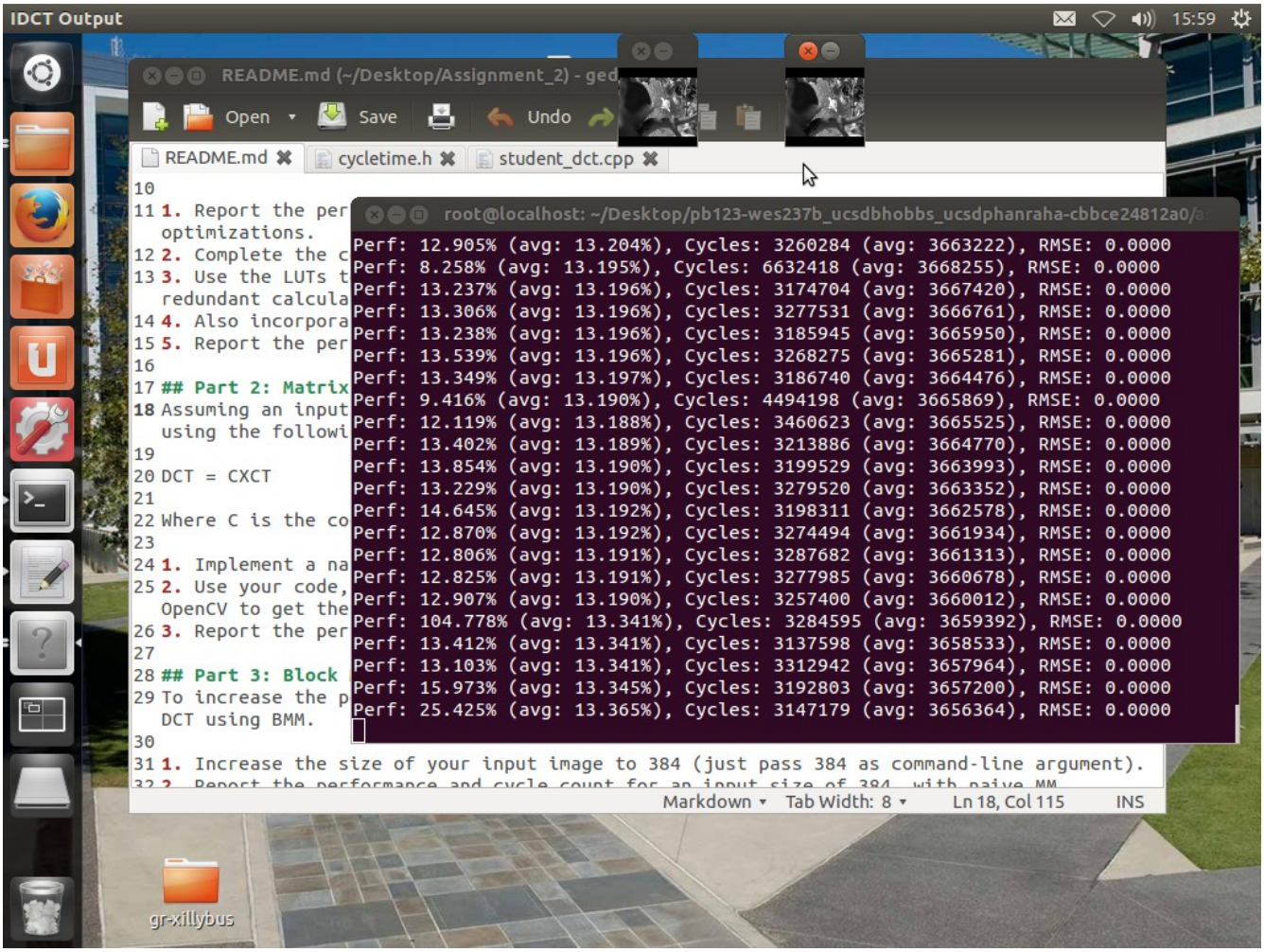
1-2. Implemented naive matrix multiplication code may be found in our repo in the assignment_2/code/src directory.

3. Performance and Cycle Count for an input size of 64 with the naive Matrix Multiplication implemented**:

Performance avg: 13.303%
Cycles Avg: 3,706,906

***Note: Please uncomment the "Part 2: DCT as matrix multiplication" to test.*

Figure 3: Matrix Multiplication with input image Size 64



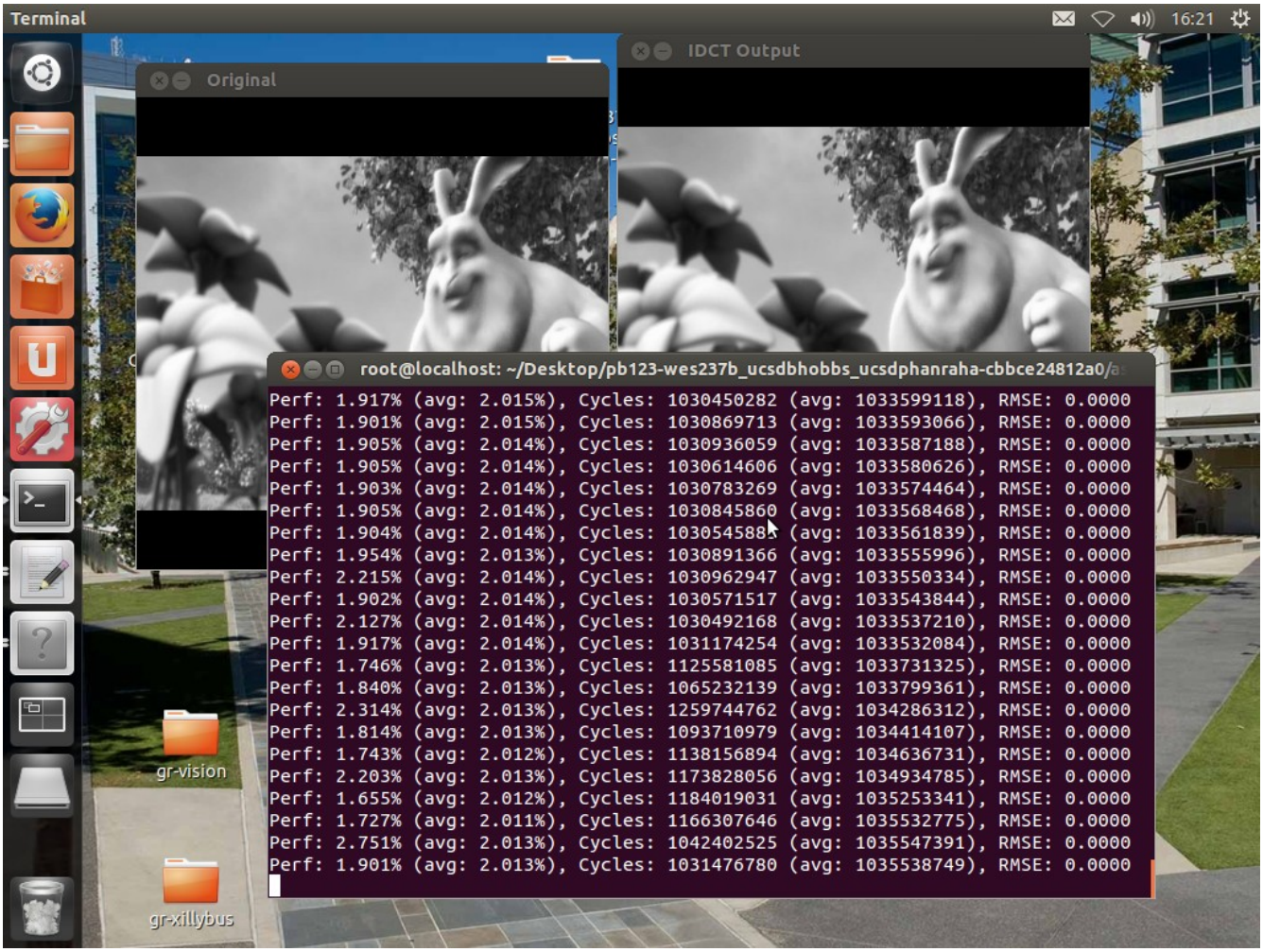
Part 3: Block Matrix Multiplication (BMM)

1-2. Performance and Cycle Count for an input size of 384 with the naive Matrix Multiplication implemented**:

Performance avg: 2.020%
Cycles Avg: 1,037,163,938
RMSE: 0.000

*****Note: Please uncomment the "Part 2: DCT as matrix multiplication" to test.*

Figure 3: Matrix Multiplication with input image Size 384



3. Implementation of the Block Multiplication Matrix code may be found in our repo in the assignment_2/code/src directory**.

***Note: Please uncomment the "BMM Part 3" to test.*

4. The optimal block matrix size was 16. This was found by trial and error.

5. We ran into difficulty getting the performance readings working for the BMM. However, we were able to get the RMSE down to 0.