TRAVEO™ T2G family AUTOSAR MCAL MAKE release notes

SRN223358 Version 1.4

About this document

Scope and purpose

Thank you for your interest in the TRAVEO™ T2G family AUTOSAR MCAL MAKE driver version 1.4. This document lists the installation requirements, software changes, limitations, and known issues.

Intended audience

This document is intended for anyone who uses the MAKE driver of the TRAVEO™ T2G family.

Table of contents

1 System requirements and recommendations

Software prerequisites	Supported version
EB tresos Studio package for Infineon	26.2.0

1.1 Supported compilers

Green Hills Software, compiler v2017.1.4

IAR Embedded Workbench 8.0, EWARM FS 8.22.3

1.2 Compiler options

This section summarizes the compiler options used to build and test the module. When changing the compiler options, the module must be considered untested.

Compiler	Option (Cortex®-M4F core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm4f -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsingle`

Compiler	Option (Cortex®-M7 core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm7 -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fhard`

Compiler	Option (Cortex®-M0+ core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm0plus -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsoft`

Compiler	Option (Cortex®-M4F core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M4 -e --fpu=VFPv4_sp -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M7 core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M7 -e --fpu=VFPv5_d16 -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M0+ core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M0+ -e -Ohs --no_size_constraints`

1.3 Library compiler options

If a binary library has been delivered with this module, it has been built using the following options:

Compiler	Option (Cortex®-M4F core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm4f -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsingle`

Compiler	Option (Cortex®-M7 core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm7 -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fhard`

Compiler	Option (Cortex®-M0+ core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm0plus -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsoft`

Compiler	Option (Cortex®-M4F core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M4 -e --fpu=VFPv4_sp -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M7 core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M7 -e --fpu=VFPv5_d16 -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M0+ core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M0+ -e -Ohs --no_size_constraints`

1.4 Memory consumption

The Make plugin does not consume memory.

1.5 Stack consumption

The Make plugin does not consume stack.

1.6 Release details

Module software version
1.4.x (x=software patch version; see the delivery notes for details)

AUTOSAR specification version (ASR)
4.2.2

Target
MXS40

MCAL configuration settings	Supported derivatives
See the resource release notes	See the resource release notes

2 Installation

See the installation manual for EB tresos Studio for Infineon AUTOSAR software products and installation manual for MCAL42-TRAVEO.

3 Deviations from AUTOSAR

This release has no deviations.

4 Limitations

This release has no limitations.

5 Known defects

The listed issues were known at the day this release note was generated. Further problems may have been discovered in the meantime. For an up-to-date list of known issues, contact your Infineon sales representative.

This release has no known issues at the time of release.

6 Documentation

All user guides for MCAL drivers are in the *doc* subdirectory of the *installation* directory. The default location is:

C:\INFINEON_ESDB\Tresos26_2_0\doc

7 Technical support

If you have questions related to the driver, contact the local support application engineer.

8 Version history

8.1 Module SW-Version 1.0

Initial module setup.

8.2 Module SW-Version 1.1

T2MC-38111 - Base module in conflict with EB Base module

Title: Base module in conflict with EB Base module

Description: Base module delivered by Infineon is containing files in conflict with the Base module delivered by EB.

Infineon Base module follows these rules:

- Keep the standard files in Base module
- Move controller/compiler specific headers in platform plugin

This could be achieved with moving files from CYT2 folder into platform Infineon plugin.

8.3 Module SW-Version 1.2

T2MC-55803 - Rename target CYT2 to MXS40

Title: Rename target CYT2 to MXS40

Description: The target name should be renamed from CYT2 to MXS40.
Target name MXS40 can be used as unified name for TRAVEO™ T2G family.

8.4 Module SW-Version 1.3

T2MC-77594 - Support IAR compiler

Title: Support IAR compiler

Description: Support IAR compiler (IAR EWARM FS 8.22.3.15992).

The following is supported in release V1.5.0.

T2MC-39519 - Support EB tresos V26.2.0

Title: Support EB tresos V26.2.0

Description: Support EB tresos V26.2.0
[Impact]
Strict AUTOSAR specification and check for parameter configuration errors are implemented in EB tresos V26.2.0.

In addition, handling of reference paths (relative paths) such as system description file (ARXML) is changed in EB tresos V26.2.0.

Therefore, if the current ECUC configuration definitions XML file contains deviations or errors, you may find errors during import to tresos26. In that case, the ECUC configuration definitions XML file must be modified appropriately.
In addition, if the current ARXML file contains unresolvable paths, you may find errors during import to tresos26. In that case, ARXML file must be modified.

The SW has been tested; no risks except for the low-level risk listed above were found.

The following are supported in release V1.15.0.

T2MC-183983 - Update copyright notice and disclaimer statement

Title: Update copyright notice and disclaimer statement

Description: Copyright notice and disclaimer statement in the file header comment are updated to follow the up-to-date specifications.

8.5 Module SW-Version 1.4

T2MC-184096 - [BASE] Make and Platform plugins support the Video UART module

Title: [BASE] Make and Platform plugins support the Video UART module

Description: Video UART added definitions and make files for the Make and Platform plugins.
Because Make and Platform plugins are common to MCAL and Video UART, therefore included the definitions added by Video UART to MCAL PJ.

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