TRAVEO™ T2G family AUTOSAR MCAL BASE release notes

SRN223355 version 1.8

About this document

Scope and purpose

Thank you for your interest in the TRAVEO™ T2G family AUTOSAR MCAL BASE driver version 1.8. This document lists the installation requirements, software changes, limitations, and known issues.

Intended audience

This document is intended for anyone who uses the BASE driver of the TRAVEO™ T2G family.

Table of contents

1 System requirements and recommendations

Software prerequisites	Supported version
EB tresos Studio package for Infineon	26.2.0

1.1 Supported compilers

Green Hills Software, compiler v2017.1.4

IAR Embedded Workbench 8.0, EWARM FS 8.22.3

1.2 Compiler options

This section summarizes the compiler options used to build and test the module. When changing the compiler options, the module must be considered untested.

Compiler	Option (Cortex®-M4F core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm4f -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsingle`

Compiler	Option (Cortex®-M7 core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm7 -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fhard`

Compiler	Option (Cortex®-M0+ core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm0plus -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsoft`

Compiler	Option (Cortex®-M4F core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M4 -e --fpu=VFPv4_sp -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M7 core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M7 -e --fpu=VFPv5_d16 -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M0+ core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M0+ -e -Ohs --no_size_constraints`

1.3 Library compiler options

If a binary library has been delivered with this module, it has been built using the following options:

Compiler	Option (Cortex®-M4F core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm4f -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsingle`

Compiler	Option (Cortex®-M7 core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm7 -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fhard`

Compiler	Option (Cortex®-M0+ core)
Green Hills Software, compiler v2017.1.4	`-cpu=cortexm0plus -thumb -thumb_lib -C99 --short_enum -align4 --no_commons --no_alternative_tokens -asm3g -preprocess_assembly_files -nostartfiles -globalcheck=normal -globalcheck_qualifiers --prototype_errors -Wformat -Wimplicit-int -Wshadow -Wtrigraphs -Wundef -reject_duplicates -c -list -Ospeed -OI -Olink -Ointerproc -Omax -fsoft`

Compiler	Option (Cortex®-M4F core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M4 -e --fpu=VFPv4_sp -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M7 core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M7 -e --fpu=VFPv5_d16 -Ohs --no_size_constraints`

Compiler	Option (Cortex®-M0+ core)
IAR Embedded Workbench 8.0, EWARM FS 8.22.3	`--debug --endian=little --cpu=Cortex-M0+ -e -Ohs --no_size_constraints`

1.4 Memory consumption

The BASE module does not consume memory.

1.5 Stack consumption

The BASE module does not consume stack.

1.6 Note on "*_Bswmd.arxml"

The BASE module does not provide a *Bswmd.arxml* file.

1.7 Release details

Module software version
1.8.x (x=software patch version; see the delivery notes for details)

AUTOSAR specification version (ASR)
4.2.2

Target
MXS40

MCAL configuration settings	Supported derivatives
See the resource release notes	See the resource release notes

Corresponding Module_MemMap.h.template stub file version
1.0.1

2 Installation

See the installation manual for EB tresos Studio for Infineon AUTOSAR software products and installation manual for MCAL42-TRAVEO.

3 Deviations from AUTOSAR

T2MC-11838 - [ECUC_MemMap_00002]Container name MemMapAddressingModeSet

Title: [ECUC_MemMap_00002]Container name MemMapAddressingModeSet

Description:

SWS Item	[ECUC_MemMap_00002]
Container Name	`MemMapAddressingModeSet`
Description	Defines a set of addressing modes which might apply to a SwAddrMethod.
Configuration Parameters	

Reason for rejection: BASE module has no configuration parameters.

T2MC-11845 - [ECUC_MemMap_00003] Container name MemMapAddressingMode

Title: [ECUC_MemMap_00003] Container name `MemMapAddressingMode`

Description:

SWS Item	[ECUC_MemMap_00003]
Container Name	MemMapAddressingMode
Description	Defines a addressing mode with a set of #pragma statements implementing the start and the stop of a section.
Configuration Parameters	

Reason for rejection: BASE module has no configuration parameters.

T2MC-11846 - [ECUC_MemMap_00004]MemMapAddressingModeStart

Title: [ECUC_MemMap_00004]`MemMapAddressingModeStart`

Description:

Name	`MemMapAddressingModeStart [ECUC_MemMap_00004]`		
Description	Defines a set of #pragma statements implementing the start of a section.		
Multiplicity	1		
Type	`EcucMultilineStringParamDef`		
Default Value			
Regular Expression			
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: local		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11847 - [ECUC_MemMap_00005]MemMapAddressingModeStop

Title: [ECUC_MemMap_00005]`MemMapAddressingModeStop`

Description:

Name	`MemMapAddressingModeStop [ECUC_MemMap_00005]`		
Description	Defines a set of #pragma statements implementing the start of a section.		
Multiplicity	1		
Type	`EcucMultilineStringParamDef`		
Default Value			
Regular Expression			
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: local		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11848 - [ECUC_MemMap_00006]MemMapAlignmentSelector

Title: [ECUC_MemMap_00006]`MemMapAlignmentSelector`

Description:

Name	MemMapAlignmentSelector [ECUC_MemMap_00006]		
Description	Defines a the alignments for which `the MemMapAddressingMode` applies. The to be used alignment is defined in the alignment attribute of `the MemorySection`. If `the MemMapAlignmentSelector` fits to alignment attribute of `the MemorySection` the set of #pragmas of the related `MemMapAddressingMode` shall be used to implement the start and the stop of a section. Please note that the same `MemMapAddressingMode` can be applicable for several alignments, e.g. "8" bit and "UNSPECIFIED".		
Multiplicity	1..*		
Type	`EcucStringParamDef`		
Default Value			
Regular Expression	[1-9][0-9]*|0x[0-9a-f]*|0[0-7]*|0b[0- 1]*|UNSPECIFIED|UNKNOWN|BOOLEAN|		
Post-Build Variant Multiplicity	False		
Post-Build Variant Value	False		
Multiplicity Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	

	Post-build time	-	
Scope / Dependency	scope: local		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11843 - [ECUC_MemMap_00007]MemMapSupportedSectionType

Title: [ECUC_MemMap_00007]`MemMapSupportedSectionType`

Description:

Name	`MemMapSupportedSectionType [ECUC_MemMap_00007]`	
Description	This constrains the usage of this addressing mode set for Generic Mappings to `swAddrMethods`. The `attribute sectionType` of a `swAddrMethod` mapped via `MemMapGenericMapping` or `MemMapSectionSpecificMapping` to this `MemMapAddressingModeSet` shall be equal to one of the configured `MemMapSupportedSectionType`'s.	
Multiplicity	0..*	
Type	`EcucEnumerationParamDef`	
Range	MEMMAP_SECTION_TYPE_CALIBRATION_OFFLINE	Program data which can only be used for offline calibration. Note: This value is deprecated and shall be substituted by `calPrm`.
	MEMMAP_SECTION_TYPE_CALIBRATION_ONLINE	Program data which can be used for online calibration. Note: This value is deprecated and shall be substituted by `calPrm`.
	MEMMAP_SECTION_TYPE_CAL_PRM	To be used for calibratable constants of ECU-functions.
	MEMMAP_SECTION_TYPE_CODE	To be used for mapping code to application block, boot block, external flash etc.
	MEMMAP_SECTION_TYPE_CONFIG_DATA	Constants with attributes that show that they reside in one segment for module configuration.
	MEMMAP_SECTION_TYPE_CONST	To be used for global or static constants.
	MEMMAP_SECTION_TYPE_EXCLUDE_FROM_FLASH	Values existing in the ECU but not dropped down in the binary file. No upload should be needed to obtain access to the ECU data. The ECU will never be touched by the instrumentation tool, with the exception of upload. These are memory areas which are not overwritten by downloading the executable.
	MEMMAP_SECTION_TYPE_USER_DEFINED	No specific categorization of `sectionType` possible.

		Note: This value is deprecated and shall be substituted by `var`, `code`, `const`, `calPrm`, `configData`, `excludeFromFlash` and the appropriate values of the `orthogonal attributes` `sectionInitializationPolicy`, `memoryAllocationKeywordPolicy` and option.	
	MEMMAP_SECTION_TYPE_VAR	To be used for global or static variables. The expected initialization is specified with the attribute `sectionInitializationPolicy`.	
	MEMMAP_SECTION_TYPE_VAR_FAST	To be used for all global or static variables that have at least one of the following properties: - accessed bit-wise - frequently used - high number of accesses in source code Some platforms allow the use of bit instructions for variables located in this specific RAM area as well as shorter addressing instructions. This saves code and runtime. Note: This value is deprecated and shall be substituted by `var` and the appropriate values of the orthogonal attributes `sectionInitializationPolicy`, `memoryAllocationKeywordPolicy` and option.	
	MEMMAP_SECTION_TYPE_VAR_NO_INIT	To be used for all global or static variables that are never initialized. Note: This value is deprecated and shall be substituted by `var` and the appropriate values of the orthogonal attributes `sectionInitializationPolicy`, `memoryAllocationKeywordPolicy` and option.	
	MEMMAP_SECTION_TYPE_VAR_POWER_ON_INIT	To be used for all global or static variables that are initialized only after power on reset. Note: This value is deprecated and shall be substituted by `var` and the appropriate values of the orthogonal attributes `sectionInitializationPolicy`, `memoryAllocationKeywordPolicy` and option.	
Post-Build Variant Multiplicity	False		
Post-Build Variant Value	False		
Multiplicity Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	

Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11842 - [ECUC_MemMap_00008]MemMapSupportedSectionInitializationPolicy

Title: [ECUC_MemMap_00008]`MemMapSupportedSectionInitializationPolicy`

Description:

Name	`MemMapSupportedSectionInitializationPolicy [ECUC_MemMap_00008]`		
Description	This constrains the usage of this addressing mode set for Generic Mappings to `swAddrMethods`. The `sectionIntializationPolicy` attribute value of a `swAddrMethod` mapped via `MemMapGenericMapping` to this `MemMapAddressingModeSet` shall be equal to one of the configured `MemMapSupportedSectionIntializationPolicy'` Please note that `SectionInitializationPolicyType` describes the intended initialization of `MemorySections` The following values are standardized in AUTOSAR Methodology: • **NO-INIT**: No initialization and no clearing is performed. Such data elements must not be read before one has written a value into it. • **INIT**: To be used for data that are initialized by every reset to the specified value (`initValue`). • **POWER-ON-INIT**: To be used for data that are initialized by "Power On" to the specified value (`initValue`). Note: there might be several resets between power on resets. • **CLEARED**: To be used for data that are initialized by every reset to zero. • **POWER-ON-CLEARED**: To be used for data that are initialized by "Power On" to zero. Note: there might be several resets between power on rese Please note that the values are defined similar to the representation of enumeration types in the XML schema to ensure backward compatibility.		
Multiplicity	0..*		
Type	`EcucStringParamDef`		
Default Value			
Regular Expression			
Post-Build Variant Multiplicity	False		
Post-Build Variant Value	False		
Multiplicity Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Value Configuration Class	***Pre-compile time***	X	All Variants

	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11840 - [ECUC_MemMap_00009]MemMapSupportedAddressingMethodOption

Title: [ECUC_MemMap_00009]`MemMapSupportedAddressingMethodOption`

Description:

Name	`MemMapSupportedAddressingMethodOption [ECUC_MemMap_00009]`		
Description	This constrains the usage of this addressing mode set for Generic Mappings to swAddrMethods. The attribute option of a `swAddrMethod` mapped via `MemMapGenericMapping` to this `MemMapAddressingModeSet` shall be equal to one of the configured `MemMapSupportedAddressMethodOption's`		
Multiplicity	0..*		
Type	`EcucStringParamDef`		
Default Value			
Regular Expression	[a-zA-Z]([a-zA-Z0-9]|_[a-zA-Z0-9])*_?		
Post-Build Variant Multiplicity	False		
Post-Build Variant Value	False		
Multiplicity Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11850 - [ECUC_MemMap_00010] Container name MemMapAllocation

Title: [ECUC_MemMap_00010] Container name `MemMapAllocation`

Description:

SWS Item	[ECUC_MemMap_00010]
Container Name	`MemMapAllocation`
Description	Defines which `MemorySection` of a BSW Module or a Software Component is implemented with which `MemMapAddressingModeSet`. This can either be specified for a set of `MemorySections` which refer to an identical `SwAddrMethod` (`MemMapGenericMapping`) or for individual

	MemorySections (MemMapSectionSpecificMapping). If both are defined for the same MemorySection the MemMapSectionSpecificMapping overrules the MemMapGenericMapping.

Configuration Parameters

Included Containers

Container Name	*Multiplicity*	*Scope / Dependency*
MemMapGeneric Mapping	0..*	Defines which SwAddrMethod is implemented with which MemMapAddressingModeSet. The pragmas for the implementation of the MemorySelectorKeywords are taken from the MemMapAddressingModeStart and MemMapAddressingModeStop parameters of the MemMapAddressingModeSet for the individual alignments. That this mapping becomes valid requires matching MemMapSupportedSectionType's, MemMapSupportedSectionInitializationPolicy's and MemMapSupportedAddressingMethodOption's. The MemMapGenericMapping applies only if it is not overruled by an MemMapSectionSpecificMapping
MemMapSectionSpecific Mapping	0..*	Defines which MemorySection of a BSW Module or a Software Component is implemented with which MemMapAddressingModeSet. The pragmas for the implementation of the MemorySelectorKeywords are taken from the MemMapAddressingModeStart and MemMapAddressingModeStop parameters of the MemMapAddressingModeSet for the specific alignment of the MemorySection. The MemMapSectionSpecificMapping precedes a mapping defined by MemMapGenericMapping.

Reason for rejection: BASE module has no configuration parameters.

T2MC-11852 - [ECUC_MemMap_00011] Container name MemMapGenericMapping

Title: [ECUC_MemMap_00011] Container name `MemMapGenericMapping`

Description:

SWS Item	[ECUC_MemMap_00011]
Container Name	`MemMapGenericMapping`
Description	Defines which `SwAddrMethod` is implemented with which `MemMapAddressingModeSet`. The pragmas for the implementation of the `MemorySelectorKeywords` are taken from the `MemMapAddressingModeStart` and `MemMapAddressingModeStop` parameters of `the MemMapAddressingModeSet` for the individual alignments. That this mapping becomes valid requires matching `MemMapSupportedSectionType's`, `MemMapSupportedSectionInitializationPolicy`'s and `MemMapSupportedAddressingMethodOption`'s. The `MemMapGenericMapping` applies only if it is not overruled by an `MemMapSectionSpecificMapping`
Configuration Parameters	

Reason for rejection: BASE module has no configuration parameters.

T2MC-11853 - [ECUC_MemMap_00012]MemMapAddressingModeSetRef

Title: [ECUC_MemMap_00012]`MemMapAddressingModeSetRef`

Description:

Name	`MemMapAddressingModeSetRef [ECUC_MemMap_00012]`		
Description	Reference to the `MemMapAddressingModeSet` which applies to the `MemMapGenericMapping`.		
Multiplicity	1		
Type	Reference to `MemMapAddressingModeSet`		
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11854 - [ECUC_MemMap_00013]MemMapSwAddressMethodRef

Title: [ECUC_MemMap_00013]`MemMapSwAddressMethodRef`

Description:

Name	`MemMapSwAddressMethodRef` [ECUC_MemMap_00013]		
Description	Reference to the `SwAddrMethod` which applies to the `MemMapGenericMapping`.		
Multiplicity	1		
Type	Foreign reference to SW-ADDR-METHOD		
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11856 - [ECUC_MemMap_00014] Container name MemMapSectionSpecificMapping

Title: [ECUC_MemMap_00014] Container name `MemMapSectionSpecificMapping`

Description:

SWS Item	[ECUC_MemMap_00014]
Container Name	`MemMapSectionSpecificMapping`
Description	Defines which `MemorySection` of a BSW Module or a Software Component is implemented with which `MemMapAddressingModeSet`. The pragmas for the implementation of the `MemorySelectorKeywords` are taken from the `MemMapAddressingModeStart` and `MemMapAddressingModeStop` parameters of the `MemMapAddressingModeSet` for the specific alignment of the `MemorySection`. The `MemMapSectionSpecificMapping` precedes a mapping defined by `MemMapGenericMapping`.
Configuration Parameters	

Reason for rejection: BASE module has no configuration parameters.

T2MC-11857 - [ECUC_MemMap_00015]MemMapAddressingModeSetRef

Title: [ECUC_MemMap_00015]`MemMapAddressingModeSetRef`

Description:

Name	`MemMapAddressingModeSetRef [ECUC_MemMap_00015]`		
Description	Reference to `the MemMapAddressingModeSet` which applies to `the MemMapModuleSectionSpecificMapping`.		
Multiplicity	1		
Type	Reference to `MemMapAddressingModeSet`		
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11858 - [ECUC_MemMap_00016]MemMapMemorySectionRef

Title: [ECUC_MemMap_00016]`MemMapMemorySectionRef`

Description:

Name	`MemMapMemorySectionRef [ECUC_MemMap_00016]`		
Description	Reference to the `MemorySection` which applies to the `MemMapSectionSpecificMapping`.		
Multiplicity	1		
Type	Foreign reference to MEMORY-SECTION		
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11841 - [ECUC_MemMap_00017]MemMapSupportedMemoryAllocationKeywordPolicy

Title: [ECUC_MemMap_00017]`MemMapSupportedMemoryAllocationKeywordPolicy`

Description:

Name	`MemMapSupportedMemoryAllocationKeywordPolicy [ECUC_MemMap_00017]`		
Description	This constrains the usage of this addressing mode set for Generic Mappings to `swAddrMethods`. The attribute `MemoryAllocationKeywordPolicy` of a `swAddrMethod` mapped via `MemMapGenericMapping` to this `MemMapAddressingModeSet` shall be equal to one of the configured `MemMapSupportedMemoryAllocationKeywordPolicy's`		
Multiplicity	0..*		
Type	`EcucEnumerationParamDef`		
Range	MEMMAP_ALLOCATION_ KEYWORD_POLICY_AD DR_METHOD_SHORT_N AME	The Memory Allocation Keyword is build with the short name of the `SwAddrMethod`. This is the default value if the atttribute does not exist in the `SwAddrMethod`.	
	MEMMAP_ALLOCATION_ KEYWORD_POLICY_AD DR_METHOD_SHORT_N AME_AND_ALIGNMENT	The Memory Allocation Keyword is build with the the short name of the `SwAddrMethod` and the alignment attribute of the `MemorySection`. This requests a separation of objects in memory dependent from the alignment and is not applicable for `RunnableEntitys` and `BswSchedulableEntitys`.	
Post-Build Variant Multiplicity	False		
Post-Build Variant Value	False		
Multiplicity Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11839 - [ECUC_MemMap_00018]MemMapCompilerMemClassSymbolImpl

Title: [ECUC_MemMap_00018]`MemMapCompilerMemClassSymbolImpl`

Description:

Name	`MemMapCompilerMemClassSymbolImpl [ECUC_MemMap_00018]`		
Description	Defines the implementation behind a `MemClassSymbol` and configures the Compiler Abstraction.		
Multiplicity	1		
Type	`EcucStringParamDef`		
Default Value			
Regular Expression			
Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11860 - [ECUC_MemMap_00019] Container name MemMapGenericCompilerMemClass

Title: [ECUC_MemMap_00019] Container name `MemMapGenericCompilerMemClass`

Description:

SWS Item	[ECUC_MemMap_00019]
Container Name	`MemMapGenericCompilerMemClass`
Description	The `shortName` of the container defines the name of the generic Compiler memclass which is global for all using modules, e.g. REGSPACE. The configures the Compiler Abstraction.
Configuration Parameters	

Reason for rejection: BASE module has no configuration parameters.

T2MC-11861 - [ECUC_MemMap_00020]MemMapGenericCompilerMemClassSymbolImpl

Title: [ECUC_MemMap_00020]`MemMapGenericCompilerMemClassSymbolImpl`

Description:

Name	`MemMapGenericCompilerMemClassSymbolImpl [ECUC_MemMap_00020]`
Description	Defines the implementation behind the generic `MemClassSymbol` and configures the Compiler Abstraction.
Multiplicity	1
Type	`EcucStringParamDef`
Default Value	
Regular Expression	

Post-Build Variant Value	False		
Value Configuration Class	***Pre-compile time***	X	All Variants
	Link time	-	
	Post-build time	-	
Scope / Dependency	scope: ECU		

Reason for rejection: BASE module has no configuration parameters.

T2MC-11708 - [SWS_COMPILER_00042]Configuration specification

Title: [SWS_COMPILER_00042]Configuration specification

Description: [SWS_COMPILER_00042] [The file Compiler.h is specific for each build scenario. Therefore there is no standardized configuration interface specified.] ()

Reason for rejection: BASE module has no parameters.

T2MC-11704 - [SWS_COMPILER_00066] Configuration specification: MemMapAddressingModeSet MemMapCompilerAddressingMode

Title: [SWS_COMPILER_00066] Configuration specification: `MemMapAddressingModeSet` MemMapCompilerAddressingMode

Description: [SWS_COMPILER_00066][The parameter `'MemMapCompilerAddressingMode'` shall contain the implementation behind a module-specific memory class symbol.] ()

Reason for rejection: BASE module has no parameters.

T2MC-11706 - [SWS_COMPILER_00067]Configuration specification: MemMapGenericCompilerClass

Title: [SWS_COMPILER_00067]Configuration specification: `MemMapGenericCompilerClass`

Description: [SWS_COMPILER_00067] [Global memory classes (e.g. REGSPACE) shall be configured in the container '`MemMapGenericCompilerClass`'.] ()

Reason for rejection: BASE module has no parameters.

T2MC-11707 - [SWS_COMPILER_00068]Configuration specification: MemMapGenericCompilerClass MemMapGenericCompilerAddressingMode

Title: [SWS_COMPILER_00068]Configuration specification: `MemMapGenericCompilerClass` `MemMapGenericCompilerAddressingMode`

Description: [SWS_COMPILER_00068][The parameter shall contain the implementation behind a global memory class symbol.] ()

Reason for rejection: BASE module has no parameters.

T2MC-11711 - [SWS_COMPILER_00999]Not applicable requirements

Title: [SWS_COMPILER_00999]Not applicable requirements

Description: [SWS_COMPILER_00999] [These requirements are not applicable to this specification.] (SRS_BSW_00300, SRS_BSW_00301, SRS_BSW_00302, SRS_BSW_00305, SRS_BSW_00307, SRS_BSW_00308, SRS_BSW_00309, SRS_BSW_00310, SRS_BSW_00312, SRS_BSW_00314, SRS_BSW_00323, SRS_BSW_00325, SRS_BSW_00327, SRS_BSW_00330, SRS_BSW_00331, SRS_BSW_00333, SRS_BSW_00334, SRS_BSW_00335, SRS_BSW_00336, SRS_BSW_00339, SRS_BSW_00341, SRS_BSW_00342, SRS_BSW_00343, SRS_BSW_00344, SRS_BSW_00346, SRS_BSW_00350, SRS_BSW_00353, SRS_BSW_00357, SRS_BSW_00358, SRS_BSW_00359, SRS_BSW_00360, SRS_BSW_00369, SRS_BSW_00371, SRS_BSW_00373, SRS_BSW_00375, SRS_BSW_00377, SRS_BSW_00378, SRS_BSW_00380, SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00390, SRS_BSW_00392, SRS_BSW_00393, SRS_BSW_00394, SRS_BSW_00395, SRS_BSW_00398, SRS_BSW_00399, SRS_BSW_00004, SRS_BSW_00400, SRS_BSW_00401, SRS_BSW_00404, SRS_BSW_00405, SRS_BSW_00406, SRS_BSW_00407, SRS_BSW_00408, SRS_BSW_00409, SRS_BSW_00410, SRS_BSW_00411, SRS_BSW_00413, SRS_BSW_00414, SRS_BSW_00415, SRS_BSW_00416, SRS_BSW_00417, SRS_BSW_00419, SRS_BSW_00422, SRS_BSW_00423, SRS_BSW_00424, SRS_BSW_00425, SRS_BSW_00426, SRS_BSW_00427, SRS_BSW_00428, SRS_BSW_00429, SRS_BSW_00432, SRS_BSW_00433, SRS_BSW_00005, SRS_BSW_00007, SRS_BSW_00009, SRS_BSW_00010, SRS_BSW_00158, SRS_BSW_00161, SRS_BSW_00162, SRS_BSW_00164, SRS_BSW_00167, SRS_BSW_00168, SRS_BSW_00170, SRS_BSW_00171, SRS_BSW_00172)

Reason for rejection: Named RQMs are not applicable.

T2MC-11757 - [SWS_COMTYPE_00022]Name: BusTrcvErrorType communication system return codes

Title: [SWS_COMTYPE_00022]Name: `BusTrcvErrorType` communication system return codes

Description: [SWS_COMTYPE_00022] [The Communication System dependent Return codes shall be named as follows:
BUSTRCV_E_<Communication System Abbreviation>_<Error Code Name>. Communication System Abbreviation:
CAN: for Controller area network
LIN: for Local Interconnect Network
FR: for FlexRay
Error Code Name: self explaining name of error return code.
Example for a CAN specific return value:
BUSTRCV_E_CAN_SINGLE: CAN bus transceiver has detected that the fault tolerant bus is in single wire mode] ().

Reason for rejection: MCAL does not use this definition, because MCAL does not support bus transceiver modules.

T2MC-11773 - [SWS_COMTYPE_00035]Not applicable requirements

Title: [SWS_COMTYPE_00035]Not applicable requirements

Description: [SWS_COMTYPE_00035] [These requirements are not applicable to this specification.] (SRS_BSW_00344, SRS_BSW_00404, SRS_BSW_00405, SRS_BSW_00345, SRS_BSW_00159, SRS_BSW_00167, SRS_BSW_00171, SRS_BSW_00170, SRS_BSW_00380, SRS_BSW_00381, SRS_BSW_00412, SRS_BSW_00383, SRS_BSW_00387, SRS_BSW_00388, SRS_BSW_00389, SRS_BSW_00390, SRS_BSW_00391, SRS_BSW_00392, SRS_BSW_00393, SRS_BSW_00394, SRS_BSW_00395, SRS_BSW_00396, SRS_BSW_00397, SRS_BSW_00398, SRS_BSW_00399, SRS_BSW_00400, SRS_BSW_00375, SRS_BSW_00101, SRS_BSW_00416, SRS_BSW_00406, SRS_BSW_00168, SRS_BSW_00407, SRS_BSW_00423, SRS_BSW_00424, SRS_BSW_00425, SRS_BSW_00426, SRS_BSW_00426, SRS_BSW_00427, SRS_BSW_00428, SRS_BSW_00429, BSW00431, SRS_BSW_00432, SRS_BSW_00433, BSW00434, SRS_BSW_00336, SRS_BSW_00337, SRS_BSW_00338, SRS_BSW_00369, SRS_BSW_00339, BSW00421, SRS_BSW_00422, BSW00420, SRS_BSW_00417, SRS_BSW_00323, SRS_BSW_00409, SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00161, SRS_BSW_00162, BSW00324, SRS_BSW_00005, SRS_BSW_00415, SRS_BSW_00164, SRS_BSW_00325, SRS_BSW_00326, SRS_BSW_00342, SRS_BSW_00343, SRS_BSW_00160, SRS_BSW_00007, SRS_BSW_00300, SRS_BSW_00347, SRS_BSW_00307, SRS_BSW_00310, SRS_BSW_00373, SRS_BSW_00327, SRS_BSW_00335, SRS_BSW_00350, SRS_BSW_00408, SRS_BSW_00410, SRS_BSW_00411, SRS_BSW_00346, SRS_BSW_00158, SRS_BSW_00314, SRS_BSW_00370, SRS_BSW_00348, SRS_BSW_00353, SRS_BSW_00361, SRS_BSW_00301, SRS_BSW_00302, SRS_BSW_00328, SRS_BSW_00312, SRS_BSW_00006, SRS_BSW_00357, SRS_BSW_00377, SRS_BSW_00304, SRS_BSW_00355, SRS_BSW_00378, SRS_BSW_00306, SRS_BSW_00308, SRS_BSW_00309, SRS_BSW_00371, SRS_BSW_00358, SRS_BSW_00414, SRS_BSW_00376, SRS_BSW_00359, SRS_BSW_00360, SRS_BSW_00329, SRS_BSW_00330, SRS_BSW_00331, SRS_BSW_00009, SRS_BSW_00401, SRS_BSW_00172, SRS_BSW_00010, SRS_BSW_00333, SRS_BSW_00374, SRS_BSW_00379, SRS_BSW_00321, SRS_BSW_00341, SRS_BSW_00334)

Reason for rejection: Named RQMs are not applicable.

T2MC-11807 - [SWS_MemMap_00018]memory mapping header files configuration support

Title: [SWS_MemMap_00018]memory mapping header files configuration support

Description: [SWS_MemMap_00018] [Each AUTOSAR basic software module and software component shall support, for all C-objects, the configuration of the assignation to one of the memory types (code,variables and constants).]*(*SRS_BSW_00306*,* SRS_BSW_00351*)*

Reason for rejection: BASE module has no configuration parameters.

T2MC-11834 - [SWS_MemMap_00024]VARIANT-PRE-COMPILE

Title: [SWS_MemMap_00024]VARIANT-PRE-COMPILE

Description: [SWS_MemMap_00024] [Variant 1 - VARIANT-PRE-COMPILE: In this configuration variant all parameters need to be configured pre compile time.]*(*SRS_BSW_00345*)*

Reason for rejection: BASE module has no configuration parameters.

T2MC-11811 - [SWS_MemMap_00026]memory mapping header files memory allocation keywords support

Title: [SWS_MemMap_00026]memory mapping header files memory allocation keywords support

Description: [SWS_MemMap_00026] [Each BSW memory mapping header file shall support the Memory Allocation Keywords to start and to stop a section for each belonging `MemorySection` defined in a `BswImplementation` which is part of the input configuration.]*(*SRS_BSW_00351*)*

Reason for rejection: The BASE module only supports of the fixed code.

T2MC-11791 - [SWS_MemMap_00029]Header file structure

Title: [SWS_MemMap_00029]Header file structure

Description: [SWS_MemMap_00029] [For each software component type which is part of the input configuration a software component type specific memory mapping header file {componentTypeName}_MemMap.h shall be provided by the Memory Mapping.] *(*SRS_BSW_00465*,* SRS_BSW_00415, SRS_BSW_00351, SRS_BSW_00464*)*

Reason for rejection: The BASE module does not provide the configuration of the Memory Mapping and *{componentTypeName}_MemMap.h* for software components, because software components are not in the scope of MCAL.

T2MC-11812 - [SWS_MemMap_00033] memory mapping header files '{Mip}_MemMap.h'

Title: [SWS_MemMap_00033] memory mapping header files '{Mip}_MemMap.h'

Description: [SWS_MemMap_00033] [All `MemorySections` defined in a `BswImplementation` belong to the *{Mip}_MemMap.h* memory mapping header file if the `BswImplementation` does NOT contain a `DependencyOnArtifact` as requiredArtifact.DependencyOnArtifact.category = MEMMAP]*(SRS_BSW_00351)*
Please note also [SWS_MemMap_00032].

Reason for rejection: The BASE module only supports of the fixed code.

T2MC-11813 - [SWS_MemMap_00034]memory mapping header files defined by the attribute

Title: [SWS_MemMap_00034]memory mapping header files defined by the attribute

Description: [SWS_MemMap_00034] [All MemorySection defined in a BswImplementation belong to the memory mapping header file defined by the attribute requiredArtifact.artifactDescriptor.shortLable if the BswImplementation does contain exactly one DependencyOnArtifact as requiredArtifact.Dependency OnArtifact.category = MEMMAP]*(SRS_BSW_00351)*
Please note also [SWS_MemMap_00028].

Reason for rejection: The BASE module only supports of the fixed code.

T2MC-11814 - [SWS_MemMap_00035]memory mapping header files associated with the identical SectionNamePrefix

Title: [SWS_MemMap_00035]memory mapping header files associated with the identical SectionNamePrefix

Description: [SWS_MemMap_00035] [All `MemorySection` defined in a `BswImplementation` and associated with the identical `SectionNamePrefix` belong to the memory mapping header file defined by the `attribute requiredArtifact.artifactDescriptor.shortLable` of `the DependencyOnArtifact` which is referenced by `the SetionNamePrefix` with a `implementedIn` reference.]*(*SRS_BSW_00351*)*

Reason for rejection: The BASE module only supports of the fixed code.

T2MC-11869 - [SWS_MemMap_00999]Not applicable requirements

Title: [SWS_MemMap_00999]Not applicable requirements

Description: [SWS_MemMap_00999] [These requirements are not applicable to this specification.] (SRS_BSW_00404, SRS_BSW_00405, SRS_BSW_00344, SRS_BSW_00159, SRS_BSW_00167, SRS_BSW_00171, SRS_BSW_00170, SRS_BSW_00419, SRS_BSW_00381, SRS_BSW_00412, SRS_BSW_00383, SRS_BSW_00388, SRS_BSW_00389, SRS_BSW_00390, SRS_BSW_00392, SRS_BSW_00393, SRS_BSW_00394, SRS_BSW_00395, SRS_BSW_00396, SRS_BSW_00397, SRS_BSW_00398, SRS_BSW_00399, SRS_BSW_00400, SRS_BSW_00375, SRS_BSW_00101, SRS_BSW_00416, SRS_BSW_00406, SRS_BSW_00168, SRS_BSW_00407, SRS_BSW_00423, SRS_BSW_00424, SRS_BSW_00425, SRS_BSW_00426, SRS_BSW_00427, SRS_BSW_00428, SRS_BSW_00429, SRS_BSW_00432, SRS_BSW_00433, SRS_BSW_00336, SRS_BSW_00337, SRS_BSW_00369, SRS_BSW_00339, SRS_BSW_00422, SRS_BSW_00417, SRS_BSW_00323, SRS_BSW_00004, SRS_BSW_00409, SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00161, SRS_BSW_00162, SRS_BSW_00005, SRS_BSW_00164, SRS_BSW_00325, SRS_BSW_00342, SRS_BSW_00343, SRS_BSW_00160, SRS_BSW_00007, SRS_BSW_00300, SRS_BSW_00413, SRS_BSW_00347, SRS_BSW_00307, SRS_BSW_00310, SRS_BSW_00373, SRS_BSW_00327, SRS_BSW_00335, SRS_BSW_00350, SRS_BSW_00408, SRS_BSW_00410, SRS_BSW_00411, SRS_BSW_00346, SRS_BSW_00158, SRS_BSW_00314, SRS_BSW_00348, SRS_BSW_00353, SRS_BSW_00301, SRS_BSW_00302, SRS_BSW_00312, SRS_BSW_00357, SRS_BSW_00377, SRS_BSW_00378, SRS_BSW_00308, SRS_BSW_00309, SRS_BSW_00371, SRS_BSW_00358, SRS_BSW_00414, SRS_BSW_00359, SRS_BSW_00360, SRS_BSW_00330, SRS_BSW_00331, SRS_BSW_00009, SRS_BSW_00401, SRS_BSW_00172, SRS_BSW_00010, SRS_BSW_00333, SRS_BSW_00341, SRS_BSW_00334, SRS_BSW_00305, SRS_BSW_00380, SRS_BSW_00438, SRS_BSW_00439, SRS_BSW_00440, SRS_BSW_00442, SRS_BSW_00447, SRS_BSW_00448, SRS_BSW_00449, SRS_BSW_00450, SRS_BSW_00451, SRS_BSW_00452, SRS_BSW_00453, SRS_BSW_00454, SRS_BSW_00456, SRS_BSW_00457, SRS_BSW_00458, SRS_BSW_00459, SRS_BSW_00460, SRS_BSW_00461, SRS_BSW_00462, SRS_BSW_00003, SRS_BSW_00304, SRS_BSW_00318, SRS_BSW_00321, SRS_BSW_00374, SRS_BSW_00379, SRS_BSW_00402, SRS_BSW_00463, SRS_BSW_00466, SRS_BSW_00467, SRS_BSW_00469, SRS_BSW_00470, SRS_BSW_00471, SRS_BSW_00472, SRS_BSW_00473)

Reason for rejection: Named RQMs are not applicable.

T2MC-11593 - [SWS_Platform_00063]Not applicable requirements

Title: [SWS_Platform_00063]Not applicable requirements

Description: [SWS_Platform_00063] [These requirements are not applicable to this specification.
] (SRS_BSW_00344, SRS_BSW_00404, SRS_BSW_00405, SRS_BSW_00345,
SRS_BSW_00159, SRS_BSW_00167, SRS_BSW_00171, SRS_BSW_00170, SRS_BSW_00419,
SRS_BSW_00381, SRS_BSW_00412, SRS_BSW_00383, SRS_BSW_00384, SRS_BSW_00387, SRS_BSW_00388,
SRS_BSW_00389, SRS_BSW_00390, SRS_BSW_00391, SRS_BSW_00392, SRS_BSW_00393, SRS_BSW_00394,
SRS_BSW_00395, SRS_BSW_00396, SRS_BSW_00397, SRS_BSW_00398, SRS_BSW_00399, SRS_BSW_00400,
SRS_BSW_00375, SRS_BSW_00101, SRS_BSW_00416, SRS_BSW_00406, SRS_BSW_00168, SRS_BSW_00407,
SRS_BSW_00423, SRS_BSW_00429, SRS_BSW_00432, SRS_BSW_00336, SRS_BSW_00337, SRS_BSW_00338,
SRS_BSW_00369, SRS_BSW_00339, SRS_BSW_00422, SRS_BSW_00420, SRS_BSW_00417, SRS_BSW_00323,
SRS_BSW_00409, SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00161, SRS_BSW_00162, SRS_BSW_00005,
SRS_BSW_00415, SRS_BSW_00164, SRS_BSW_00325, SRS_BSW_00326, SRS_BSW_00342, SRS_BSW_00343,
SRS_BSW_00160, SRS_BSW_00007, SRS_BSW_00300, SRS_BSW_00413, SRS_BSW_00347, SRS_BSW_00305,
SRS_BSW_00307, SRS_BSW_00310, SRS_BSW_00373, SRS_BSW_00327, SRS_BSW_00335, SRS_BSW_00350,
SRS_BSW_00408, SRS_BSW_00410, SRS_BSW_00411, SRS_BSW_00346, SRS_BSW_00158, SRS_BSW_00314,
SRS_BSW_00370, SRS_BSW_00348, SRS_BSW_00361, SRS_BSW_00301, SRS_BSW_00302, SRS_BSW_00328,
SRS_BSW_00312, SRS_BSW_00357, SRS_BSW_00377, SRS_BSW_00355, SRS_BSW_00306, SRS_BSW_00308,
SRS_BSW_00309, SRS_BSW_00371, SRS_BSW_00358, SRS_BSW_00414, SRS_BSW_00376, SRS_BSW_00359,
SRS_BSW_00360, SRS_BSW_00329, SRS_BSW_00330, SRS_BSW_00331, SRS_BSW_00009, SRS_BSW_00401,
SRS_BSW_00172, SRS_BSW_00010, SRS_BSW_00333, SRS_BSW_00374, SRS_BSW_00379,
SRS_BSW_00321, SRS_BSW_00341, SRS_BSW_00334] ( )

Reason for rejection: Named RQMs are not applicable.

T2MC-11477 - [SWS_Std_00018] Not applicable requirements

Title: [SWS_Std_00018] Not applicable requirements

Description: [SWS_Std_00018] [These requirements are not applicable to this specification.] (SRS_BSW_00300, SRS_BSW_00301, SRS_BSW_00302, SRS_BSW_00304, SRS_BSW_00305, SRS_BSW_00306, SRS_BSW_00307, SRS_BSW_00308, SRS_BSW_00309, SRS_BSW_00310, SRS_BSW_00312, SRS_BSW_00314, SRS_BSW_00321, BSW00324, SRS_BSW_00325, SRS_BSW_00326, SRS_BSW_00327, SRS_BSW_00328, SRS_BSW_00329, SRS_BSW_00330, SRS_BSW_00331, SRS_BSW_00333, SRS_BSW_00334, SRS_BSW_00335, SRS_BSW_00342, SRS_BSW_00343, SRS_BSW_00341, SRS_BSW_00346, SRS_BSW_00347, SRS_BSW_00350, SRS_BSW_00353, SRS_BSW_00355, SRS_BSW_00358, SRS_BSW_00359, SRS_BSW_00360, SRS_BSW_00361, SRS_BSW_00370, SRS_BSW_00371, SRS_BSW_00373, SRS_BSW_00374, SRS_BSW_00376, SRS_BSW_00377, SRS_BSW_00378, SRS_BSW_00379, SRS_BSW_00401, SRS_BSW_00408, SRS_BSW_00410, SRS_BSW_00411, SRS_BSW_00413, SRS_BSW_00414, SRS_BSW_00415, SRS_BSW_00005, SRS_BSW_00006, SRS_BSW_00007, SRS_BSW_00009, SRS_BSW_00010, SRS_BSW_00158, SRS_BSW_00160, SRS_BSW_00161, SRS_BSW_00162, SRS_BSW_00164, SRS_BSW_00172, SRS_BSW_00344, SRS_BSW_00404, SRS_BSW_00405, SRS_BSW_00345, SRS_BSW_00159, SRS_BSW_00167, SRS_BSW_00171, SRS_BSW_00170, SRS_BSW_00380, SRS_BSW_00419, SRS_BSW_00381, SRS_BSW_00412, SRS_BSW_00383, SRS_BSW_00387, SRS_BSW_00388, SRS_BSW_00389, SRS_BSW_00390, SRS_BSW_00391, SRS_BSW_00392, SRS_BSW_00393, SRS_BSW_00394, SRS_BSW_00395, SRS_BSW_00396, SRS_BSW_00397, SRS_BSW_00398, SRS_BSW_00399, SRS_BSW_00400, SRS_BSW_00375, SRS_BSW_00101, SRS_BSW_00416, SRS_BSW_00406, SRS_BSW_00168, SRS_BSW_00407, SRS_BSW_00423, SRS_BSW_00424, SRS_BSW_00425, SRS_BSW_00426, SRS_BSW_00427, SRS_BSW_00428, SRS_BSW_00429, BSW00431, SRS_BSW_00432, SRS_BSW_00433, BSW00434, SRS_BSW_00336, SRS_BSW_00337, SRS_BSW_00338, SRS_BSW_00369, SRS_BSW_00339, BSW00421, SRS_BSW_00422, BSW00420, SRS_BSW_00417, SRS_BSW_00323, SRS_BSW_00409, SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00452, SRS_BSW_00473, SRS_BSW_00458, SRS_BSW_00466)

Reason for rejection: Named RQMs are not applicable.

4 Limitations

T2MC-11733 - [SWS_COMTYPE_00029]Generate ComStack_Cfg.h

Title: [SWS_COMTYPE_00029]Generate ComStack_Cfg.h

Description: [SWS_COMTYPE_00029] [ComStack_Cfg.h shall be generated by the generator to generate the type definition of the PduIdType and PduLengthType from the EcuC Virtual Layer based on the configuration e.g. typedef uint8 PduIdType if number of PDUs are less than 256.] ( )

Limitation: The type of PduLengthType that is defined in ComStack_Cfg.h is fixed to uint32. The type of PduIdType that is defined in ComStack_Cfg.h is fixed to uint16.

T2MC-11789 - [SWS_MemMap_00028]Header file structure

Title: [SWS_MemMap_00028]Header file structure

Description: [SWS_MemMap_00028] [The Memory Mapping shall provide a BSW memory mapping header file if any of the BSW Module Descriptions is describing a DependencyOnArtifact as requiredArtifact.DependencyOnArtifact.category = MEMMAP In this case the file name of the BSW memory mapping header file name is defined by the attribute value requiredArtifact.DependencyOnArtifact.artifactDescriptor.shortLable in the BSW Module Description.](SRS_BSW_00465, SRS_BSW_00415, SRS_BSW_00351, SRS_BSW_00464)

Limitation: Only fixed file name "{Mip}_MemMap.h" is provided because the BASE module does not support the configuration.

T2MC-11790 - [SWS_MemMap_00032]Header file structure

Title: [SWS_MemMap_00032]Header file structure

Description: [SWS_MemMap_00032] [For each basic software module description which is part of the input configuration a basic software module specific memory mapping header file {Mip}_MemMap.h shall be provided by the Memory Mapping if the BSW Module Descriptions is NOT describing a DependencyOnArtifact as requiredArtifact.DependencyOnArtifact.category = MEMMAP. Hereby {Mip} is composed according <Msn>[_<vi>_<ai>] for basic software modules where

- <Msn> is the shortName (case sensitive) of the BswModuleDescription
- <vi> is the vendorId of the BSW module
- <ai> is the vendorApiInfix of the BSW module

The sub part in squared brackets [_<vi>_<ai>] is omitted if no vendorApiInfix is defined for the Basic Software Module which indicates that it does not use multiple instantiation.](SRS_BSW_00465, SRS_BSW_00415, SRS_BSW_00351, SRS_BSW_00464)

Limitation: Only fixed file name "{Mip}_MemMap.h" is provided because the BASE module does not support the configuration.

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5 Known defects

The listed issues were known at the day this release note was generated. Further problems may have been discovered in the meantime. For an up-to-date list of known issues, contact your Infineon sales representative.

This release has no known issues at the time of release.

6 Documentation

All user guides for MCAL drivers are in the *\doc* subdirectory of the *installation* directory. The default location is:

C:\INFINEON_ESDB\Tresos26_2_0\doc

7 Technical support

If you have questions related to the driver, contact the local support application engineer.

8 Version history

8.1 Module SW-Version 1.2

Initial module setup.

8.2 Module SW-Version 1.3

T2MC-38111 - Base module in conflict with EB Base module

Title: Base module in conflict with EB Base module

Description: Base module delivered by Infineon is containing files in conflict with the Base module delivered by EB.

Infineon Base module follows these rules:

- Keep the standard files in Base module
- Move controller/compiler specific headers in platform plugin

This could be achieved with moving files from CYT2 folder into platform Infineon plugin.

8.3 Module SW-Version 1.4

T2MC-43523 - [CAN, BASE] CanObjectId does not correspond to a value of 256 or more

Title: [CAN, BASE] CanObjectId does not correspond to a value of 256 or more

Description: The RxHwHandle member of the Can_RxHandleMappingType table that stores CanObjectId is uint8.
When a value of 256 or more is entered, overflow occurs.

8.4 Module SW-Version 1.5

T2MC-55803 - Rename target CYT2 to MXS40

Title: Rename target CYT2 to MXS40

Description: The target name should be renamed from CYT2 to MXS40.
Target name MXS40 can be used as unified name for TRAVEO™ T2G family.

8.5 Module SW-Version 1.6

T2MC-59571 - [CAN, BASE] Can_HwHandleType supports both uint8 and uint16.

Title: [CAN, BASE] Can_HwHandleType supports both uint8 and uint16.

Description: Can_HwHandleType must support both uint8 and uint16.
Can_GeneralTypes.h is provided by the integrator and must be compatible with MCAL CAN and BSW CAN (CANIF, PDUR, etc.).

The following is supported in release V1.2.4.

T2MC-77594 - Support IAR compiler

Title: Support IAR compiler

Description: Support IAR compiler (IAR EWARM FS 8.22.3.15992).

The following is supported in release V1.5.0.

T2MC-39519 - Support EB tresos V26.2.0

Title: Support EB tresos V26.2.0

Description: Support EB tresos V26.2.0

[Impact]
Strict AUTOSAR specification and check for parameter configuration errors are implemented in EB tresos V26.2.0.
In addition, handling of reference paths (relative paths) such as system description file (ARXML) is changed in EB tresos V26.2.0.

Therefore, if the current ECUC configuration definitions XML file contains deviations or errors, you may find errors during import to tresos26. In that case, the ECUC configuration definitions XML file must be modified appropriately.
In addition, if the current ARXML file contains unresolvable paths, you may find errors during import to tresos26. In that case, ARXML file must be modified.

The SW has been tested; no risks except for the low-level risk listed above were found.

8.6 Module SW-Version 1.7

T2MC-164778 - Support MISRA C:2012 coding rule

Title: Support MISRA C:2012 coding rule

Description: Support MISRA C:2012 coding rule.

The MISRA C:2012 coding rule checks the source code.
If a deviation from the rules is required, add the deviation comment to the code and report the result.
If a deviation is for MISRA-C:2004 only, remove the deviation comment.

8.7 Module SW-Version 1.8

T2MC-164831 - [ALL] Misleading comment in Module_MemMap.h

Title: [ALL] Misleading comment in Module_MemMap.h

Description: *{Mip}_MemMap.h* files are provided as sample template files. But, the file header comment cannot be modified, which is a contradiction. To resolve this contradiction, change the file header comment to allow user modification.

Also, to make sure that the file is not a part of the commercial product, move the *{Mip}_MemMap.h* files to the *MemMap* stub folder.

The following are supported in release V1.15.0.

T2MC-183983 - Update copyright notice and disclaimer statement

Title: Update copyright notice and disclaimer statement

Description: Copyright notice and disclaimer statement in the file header comment are updated to follow the up-to-date specifications.

The following are supported in release V1.16.0.

T2MC-184096 - [BASE] Make and Platform plugins support the Video UART module

Title: [BASE] Make and Platform plugins support the Video UART module

Description: Video UART added definitions and make files for the Make and Platform plugins.
Because Make and Platform plugins are common to MCAL and Video UART, therefore included the definitions added by Video UART to MCAL PJ.

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