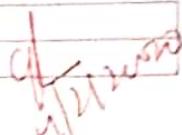


**Ramaiah Institute of Technology**  
**(Autonomous Institute, Affiliated to VTU)**  
**Department of CSE**  
**Tutorial-I**

**Programme:** B.E  
**Course:** Computer Organization

**Term:** Jan to May 2018  
**Course Code:** CS45

Name: MADHUSUDAN BALAJEE	Marks: 10 /10	Date:
USN: IMS14CS064	Signature of the Faculty:	

**Activity I:** Assembling and disassembling of a computer

**Objective:** To demonstrate the functional units of a system.

**Assembling of a system:** A PC computer is a modular type of computer, it can be assembled using hardware components made by different manufacturers, so as to have a custom built computer according to one's specific needs.

**Disassembling of a system:** When referring to hardware, **disassemble** is the process of breaking down a device into separate parts. A device may be disassembled to help determine a problem, to replace a part, or to take the parts and use them in another device or to sell them individually.

**Activity to be performed by students:** Identify the different parts of the system including its interconnection. Observe the assembly and disassembly procedure.

Answer the following questions.

1. Write down the detailed procedure to assemble a system.
2. Explain how troubleshooting a system helps to trace and correct the faults in a system
3. List out the procedure to install extra memory card to a system
4. With a diagram explain different cables used to connect function units in a system.
5. Discuss the safety precautions one should take while removing components of a system

MARKS :

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USN/Roll No. :	MS18CS064.	Sem/Sec:	IV - B
Subject :		Subject Code:	

(21) Step 1:- Remove side panels on case

After removing case from the box, the panels are removed from the case with thumb screws.

Step 2:- Insert motherboard

Before setting the board in, the I/O panel faceplate needs to be snapped into the location in the back of the case. Be sure to orient it to the board.

Once the board is resting in the case, line up the first hole.

Do not tighten all the way down until all screws are started so that the others will line up. After all are in and tightened, there should be little or no deflection if you gently press it. It is advisable that any place there is a mounting location for the board that it is secured into a standoff.

Step 3:- check clearances.

Step 4:- Front panel connections : It is time to attach the connections for the buttons, lights, USB ports and audio connections.

Step 5:- Install power supply

Normally the supply is screwed into the back panel by 4 screws, though some cases include a clamp to hold it down that way.

Step 6:- Power motherboard

With the power being the longest cable and sometimes just long enough first run the cable before plugging it into the board.

Step 7:- Installing optical drive

Step 8:- Installing hard drives

The size & number of harddrives your computer contains is completely dependent on style of use & storage needs.

Step 9:- Connect cables: The cables are keyed so they will only fit in one direction into the board

Step 10:- Install RAM

The slots are keyed as are the RAM sticks, to make sure the notch is lined up. You will know when they are set firmly as the locking tabs will snap into place and hold the RAM firmly in the slot

Step 11:- Install Graphic card Expansion card

**Step 12: Cable management**

At this step we also install fans and lights can be connected.

(Q2) Troubleshooting:- It is a systematic approach to problem solving that is often used to find and correct issues with complex machines, electronics, computers & software systems.

The first step is to gather information on the issue, such as an undesired behaviour or lack of expected functionality. Other important information includes related symptoms and special circumstances that may be required to reproduce the issue.

The next step might be to eliminate unnecessary components in the system and verify that the issue persists, to rule out incompatibility and third-party causes.

Assuming the issue remains check common causes. Depending on the issue and troubleshooter's experience, they may have some ideas.

After common causes are ruled out, the troubleshooter may resort to more systematic & logical process.

of verifying the expected functions of parts of a system.

One common method is split half troubleshooting approach with a problem resulting from a number of possible parts in series, one tests halfway down the line of components. The split half process can save time in systems that depend on many components.

- Once the problem part is identified, it may be adjusted or replaced or repaired as needed.
- Evidence is indicated when issue is no longer reproducible and function is restored one.

(Q3) 1) Determine the mode and amount of RAM your computer need:- You must know how many and what kind of RAM you need since the new RAM should match the existing RAM specifications & configurations.

2) Disconnect cables:- shut down computer & unplug the AC power cord. Disconnect all peripheral devices such as the mouse, keyboard, monitor and etc.

3) Open the computer:- You can remove computer cover by screwdrivers or your fingers.

4) Ground yourself:- Make sure you first touch an unpainted grounded metal object to discharge any static electricity stored in your clothing or body.

MARKS:

Name :		Branch:	
USN/Roll No. :		Sem/Sec:	
Subject :		Subject Code:	

- (5) Check expansion sockets :- Locating the existing memory & expansion sockets on the motherboard if no extra sockets are available, you will need to remove the old smaller capacity memory to allow more room for new higher capacity memory
- (6) Uninstall old memory :- Finding out old RAM & remove it by pushing outward on white ejector tabs
- (7) Get new memory sticks :- Check model of the memory and go to the store to get matched RAM
- (8) Insert new memory :- Pick up memory stick by the ends without touching pins or chips. Insert the memory stick straight into the available expansion socket.
- (9) Lock the memory stick :- use slight pressure to the back of the stick & make sure the small holes on each side of the stick fit into the holders.

(10) Install all the memories

Q 4) 1) VGA cable: Also known as D-sub cable analog video cable. Connect one end to computer monitor, television. Connect other end to VGA port on computer.

2) DVI cable: Connect one end to computer monitor. Connect other end to DVI port on computer.

3) HDMI cable: connect one end to computer monitor, television. Connect other end to HDMI port on computer.

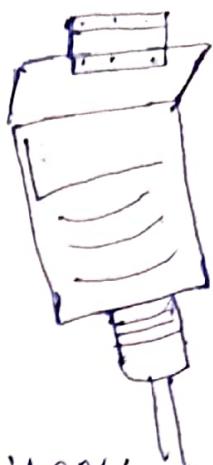
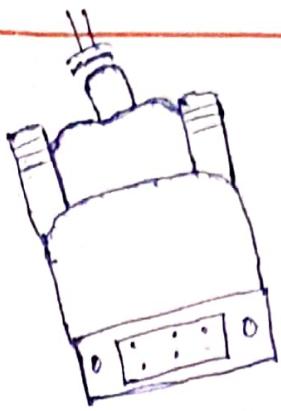
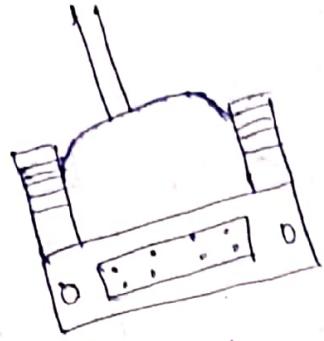
(4) PS/2 cable :- connect one end to PS/2 keyboard PS/2 mouse. Connect other to PS/2 port on computer.

Purple PS/2 port : keyboard. Green PS/2 : mouse.

(5) Ethernet cable: one end to router, network switch. Other end to ethernet port on computer.

(6) USB cable: one end to USB device. Other end to USB port on computer

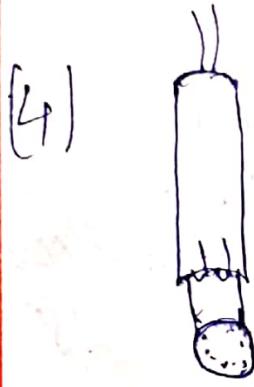
(7) Computer power cord: one end to AC power socket. Other end to power supply unit, computer monitor



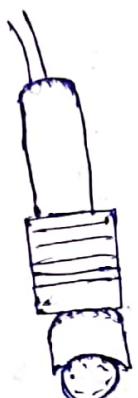
1) VGA cable

2. DVI cable

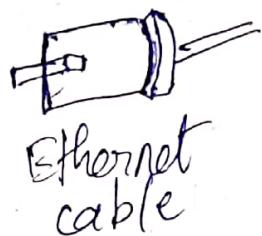
3. HDMI cable



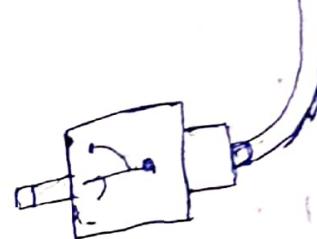
PS/2 cable



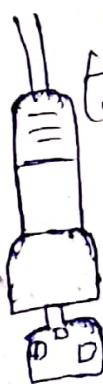
(5)



Ethernet cable



USB cable



(7) computer power cord

(25) A few precautions and warnings are

- (1) Fully shutdown & unplug the computer before you make any attempts to disconnect the tower.
- (2) Take off any metal objects on your arms or fingers such as bracelets, rings or watches. Even if your

unit is unplugged, there may still be some remaining electric charge

(3) Make sure your hands are completely dry to avoid damaging any mechanical parts as well as toward electrocution

(4) Work in cool areas to avoid perspiration.

(5) Before touching any part within the tower put your hand against another metal surface to remove static charge which may damage sensitive devices

(6) Prepare a place to keep any screws you may remove.

A container or piece of paper with labels for each part is ideal to avoid confusion b/w similar looking screws.

(7) Handle all parts with care. Place each piece you remove carefully down onto stable surface

(8) If a component does not come out easily, do not forcefully remove it.

(9) Be carefull when handling motherboard

(10) Never attempt to remove power supply

(11) when removing any cables, wires, ribbons make sure to grasp wire at base or head to keep it from breaking

(12) Be careful not to drop any small parts into unreachable areas such as into computer fan or disk drives

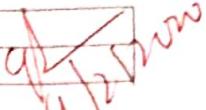
(13) Take note that the three most dangerous things to a computer are moisture, shock & dust.

Ramaiah Institute of Technology  
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Department of CSE

Tutorial -II

Programme: B.E  
Course: Computer Organization

Term: Jan to May 2020  
Course Code: CS45

Name: MADHUSUDAN BALASEE	Marks: 9 /10	Date:
USN: JMS18CS064	Signature of the Faculty:	

**Activity II:** Demonstrating Datapath and instruction execution stages using MarieSim Simulator

**Objective:** To simulate inter communication between CPU and memory.

**Simulator Description:** MarieSim is a computer architecture simulator based on the MARIE architecture. It provides users with interactive tools and simulations to help them deepen their understanding of the operation of a simple computer. One can observe how assembly language statements affect the registers and memory of a computer system.

**Activity to be performed by students:**

1. Draw the interconnection between memory and a processor.

2. List out the steps required to execute an instruction.
3. Write and execute assembly language program to compute
  - i)  $f = (g + h)^*(i + j)$
  - ii)  $d = b^2 - 4ac$
4. Describe the factors affecting the performance of a processor

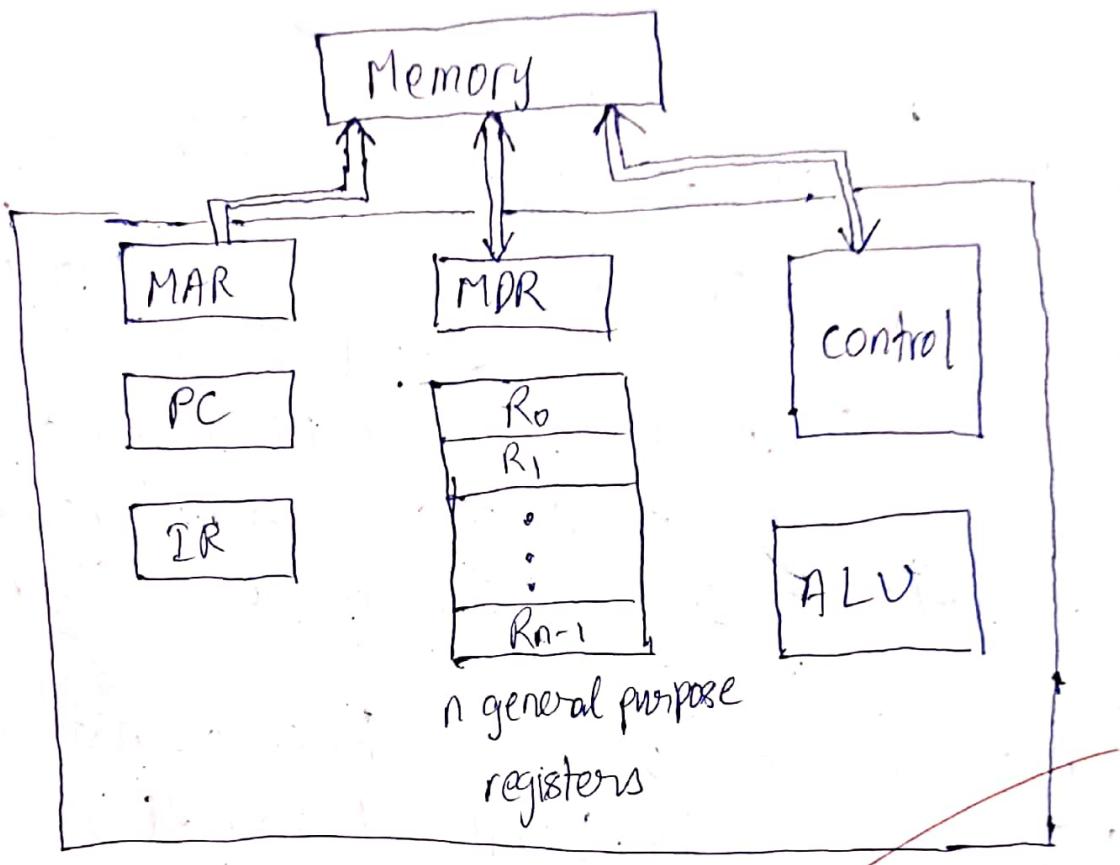
3. Results and Snapshots:

MARKS :

Name:	MADHUVUDAN PALIJEE'	Branch:	CSE
USN/Roll No.:	IMS1B CS064	Sem/Sec:	IV B
Subject:	C O . LAB	Subject Code:	

## Tutorial - II

(1)



(2) The steps involved are

- Fetch instruction: cycle starts with fetching instruction from main memory. The instruction at the current program counter (PC) will be fetched and will be stored in instruction register (IR)
- Decode instruction: the encoded instruction present in (IR) is interpreted by the decoder.
- Perform ALU operation: this is where two operands in the instruction will be operated on given operator in the instruction. Such as, if the instruction was to add two numbers, then the addition will happen. ALU takes two values and output one, the result of the operation.
- Access memory: only two kinds of instructions that access memory: LOAD and STORE. LOAD copies a value from memory to a register & STORE copies a register value to memory. Any other ~~memory~~ instruction skips this step.

- Update Register File: The output / result of the ALU is written back to the register file. The result could also be due to a LOAD from memory. Some instructions don't have results to store.
- Update the Program Counter: at the end of the execution of the current instruction, we need to update the PC to the address of the next instruction, so that we can go back to step 1 where the CPU will fetch the instruction.

#### (A) Factors affecting performance of processor

##### → Instruction set

This is the processor's built in code that tells it how to execute its duties. Its something that's coded into the chip which is manufactured & that can't be changed. But together with processor architecture, it does affect performance a given line of CPUs.

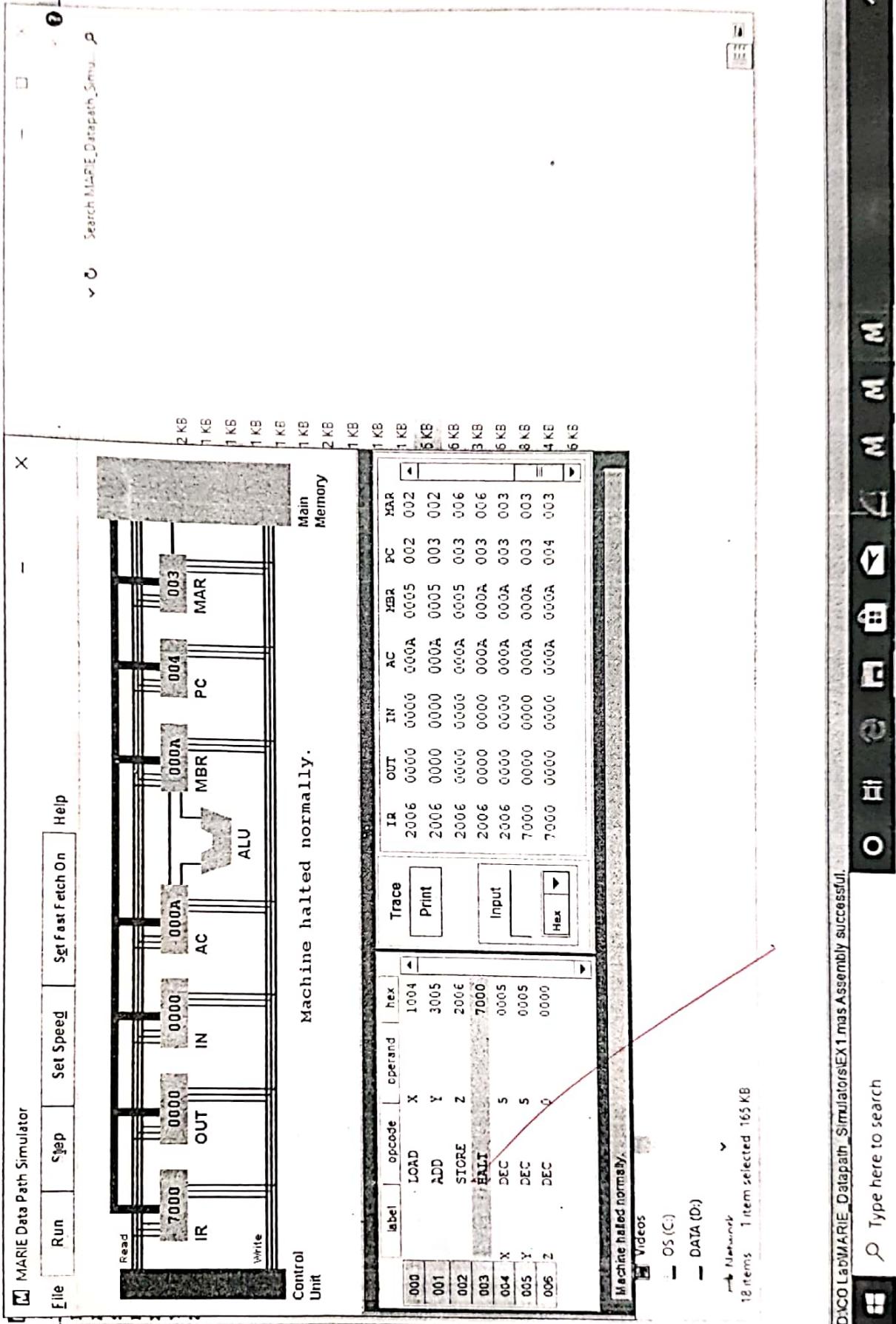
Processor architecture & instruction set determine how many cycles, or ticks are needed to execute a given instruction.

clock speed :- it is stated in megahertz or gigahertz and refers to speed at which processor can execute instructions. Faster the clock, the more instructions can complete per second.

Bandwidth: Measured in bits, it determines how much information the processor can process in one instruction.

Front Side Bus Speed: It is the interface between processor and system memory. The FSB speed limits the rate at which the CPU can process the data. The CPU's FSB speed determines maximum speed at which it can transfer data to the rest of the system.

On-board cache: It is a relatively small amount of high performance SRAM built directly into the processor. It enables the CPU to access repeatedly used data directly from its own on-board memory, rather than repeatedly requesting it from the system RAM.



Assembly Listing for EX1.mas

Assembly listing for: EX1.mas  
Assembled: Tue Jan 28 12:07:24 IST 2020

000	1004	-	LOAD X
001	3005	-	ADD Y
002	2006	-	STORE Z
003	7000	-	HALT
004	0005	-	X DEC 5
005	0005	-	Y DEC 5
006	0000	-	Z DEC 0

Assembly successful.

SYMBOL TABLE

Symbol | Defined | References

X	I	004	I	000
Y	I	005	I	001
Z	I	006	I	002

Print

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MARIE Simulator

File Run Stop Step Breakpoints Symbol Map Help

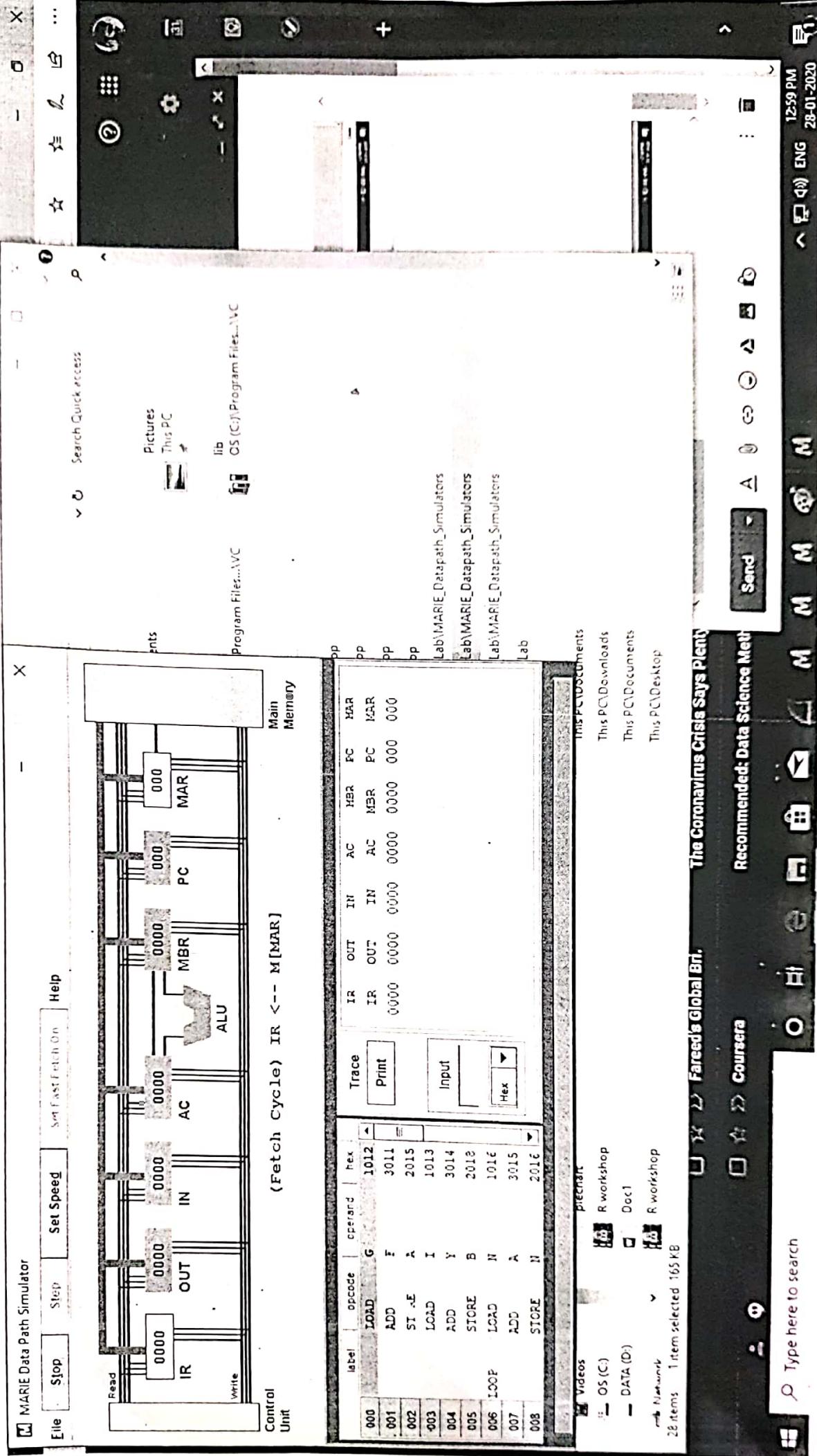
label	opcode	operand	hex
002	STORE	A	2015
003	LOAD	I	1013
004	ADD	Y	3014
005	STORE	B	2018
006	LOAD	N	1016
007	ADD	A	3015
008	STORE	N	2016
009	LOAD	I	1018
00A	SUBT	O	4017
00B	STORE	B	2018
00C	JUMP	LOOP	500E
00D	STOPCODE	400	3400

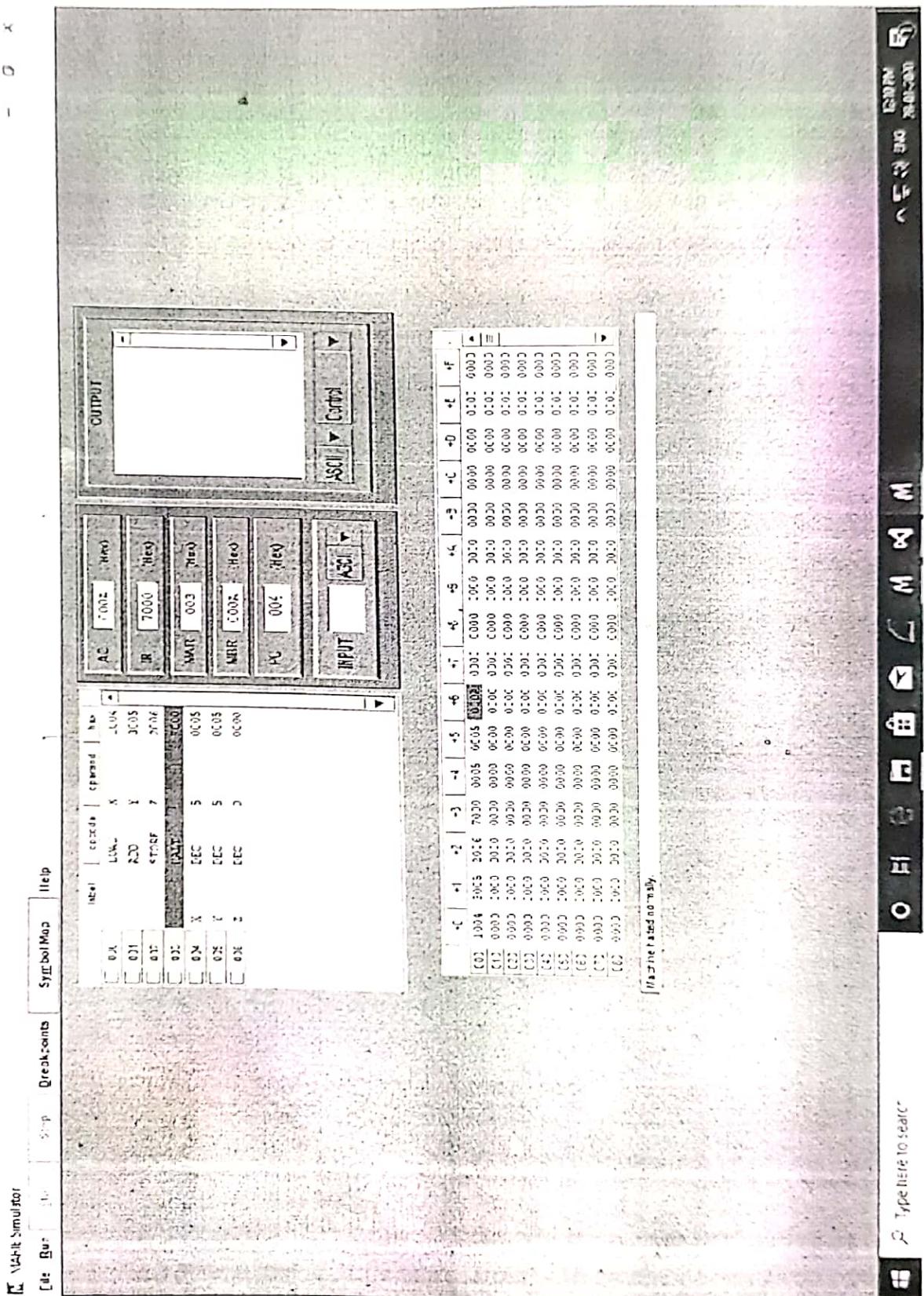
OUTPUT

AC 0004 (Hex)  
IR 8400 (Hex)  
MAR 000D (Hex)  
MBR 0004 (Hex)  
PC 000D (Hex)  
INPUT ASCII  
Control ASCII

-0 -1 -2 -3 -4 -5 -6 -7 -8 -9 -A -B -C -D -E -F

DICO Lab MARIE Datapath Simulator EX3 mas Assembly successful  
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Assembly Listing for EX3.masm

```

    00C 8400      SKIPCOND 40
    00D 9005      JUMP LOOP
    00E 1016      LOAD N
    00F 6000      OUTPUT
    010 7000      HALT
    011 0005      DEC S
    012 000A      DEC 10
    013 0005      DEC 5
    014 000A      DEC 10
    015 0000      DEC 0
    016 0000      DEC 0
    017 0001      DEC 1
    018 0000      DEC 0
    1 5

```

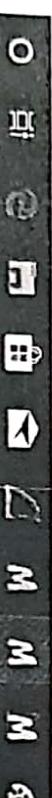
Assembly successful.

卷之三

Symbol	Defined	References
Z	1	015
B	1	018
F	1	011
G	1	012
I	1	013
LOOP	1	006
N	1	016
O	1	017
Y	1	014
		004



Pmud



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Assembly Listing for EX3.mas

Assembly listing for: EX3.mas  
Assembled: Tue Jan 28 12:53:24 IST 2020

		LOAD N	LOOP?	LOAD N	
000	1012	LOAD G			
001	3011	ADD F			
002	2015	STORE A			
003	1013	LOAD I			
004	3014	ADD Y			
005	2018	STORE B			
006	1016		003	LOAD N	
007	3015	ADD A			
008	2016	STORE N			
009	1018	LOAD B			
00A	4017	SUBT O			
00B	2018	STORE B			
00C	8400	SKIPCOND 400			
00D	9006	JUMP LOOP			
00E	1016	LOAD N			
00F	6000	OUTPUT			
010	7000	HALT			
011	0005	F DEC 5			
012	000A	G DEC 10			
013	0005	I DEC 5			
014	000A	Y DEC 10			
015	0000	A DEC 0			
016	0000	N DEC 0			
017	0001	O DEC 1			
018	0000	B DEC 0			

Assembly successful.



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Ramaiah Institute of Technology  
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Department of CSE

Tutorial -III

Programme: B.E

Course: Computer Organization Course Code: CS45

Term: Jan to May 2020

Name: MADHUSUDAN BALAJEE	Marks: 10/10	Date:
USN: 1MS18CS064	Signature of the Faculty:	CB

**Objective:** To simulate ARM Instruction set using ARMSim simulator.

**Simulator Used:** ARMSim 1.91 is a desktop application running in a Windows environment. It allows users to simulate the execution of ARM assembly language programs on a system based on the ARM7TDMI processor.

ARM enables the users both to debug ARM assembly programs and to monitor the state of the system while a program executes.

**Activity to be performed by students:**

- 1) Write an ARM program to perform basic arithmetic operations.
- 2) Write an ARM program to demonstrate the working of load and store instructions.
- 3) Write an ARM program to evaluate expression  $f=(g+h)-(i+j)$
- 4) Write an ARM program to find the sum of all elements of an array.
- 5) Write an ARM program to find the factorial of a number.

**Programs and the snapshots:**

## ARMsim - The ARM Simulator Dept. of Computer Science

File View Cache Debug Watch Help

Registers View

Floating Point

Hexadecimal

Unsigned Decimal

Signed Decimal

P1.s

00000000: E3A0100A MOV R1, #10

00000004: E3A02014 MOV R2, #20

00000008: E0813002 ADD R3, R1, R2

0000000C: E0000011 SWI 0x11

0x00000000

### CPSR Registers

Negative (N) : 0

Zero (Z) : 0

Carry (C) : 0

Overflow (V) : 0

IRQ Disable: 1

FIQ Disable: 1

Thumb (T) : 0

CPU Mode : System

0x000000df

Output View

Console Stdin/Stdout/Siderr

Execution starting ...

Execution ending, Instruction Count: 0 Elapsed Time: 00:00:00

Output View

Watch View

Type here to search



File View Cache Debug Watch Help

RegistersView 3 p1.s

General Purpose Floating Point

Hexadecimal

Unsigned Decimal

Signed Decimal

```
00000000:83401030 MOV R1, #0x0000000030
00000004:84003000 MOV R3, #0
00000008:E3A04032 MOV R4, #50
0000000C:E7814003 STR R4,[R1,R3]
00000010:E7916003 LDR R6,[R1,R3]
00000014:EF000011 SMI Orr1
```

R0	:0	
R1	:48	
R2	:0	
R3	:0	
R4	:50	
R5	:0	
R6	:50	
R7	:0	
R8	:0	
R9	:0	
R10 (s1)	:0	
R11 (fp)	:0	
R12 (ip)	:0	
R13 (sp)	:17408	
R14 (lr)	:0	
R15 (pc)	:20	
CPSR Register		
Negative (N)	:0	
Zero (Z)	:0	
Carry (C)	:0	
Overflow (V)	:0	
IRQ Disable:1		
FIQ Disable:1		
Thumb(1T)	:0	
CPU Mode	:System	

OutputView

WatchView

0xffffffff

Execution starting ...

Activate Windows  
Go to Settings to activate Windows

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^ F1 F2 F3 ENG 07:01 AM 04-02-2020

Q2

File View Cache Debug Watch Help

Q3 □ □ □ □

RegistersView p1.s

General Purpose Floating Point

Hexadecimal

Unsigned Decimal

Signed Decimal

R0 : 0

R1 : 20

R2 : 30

R3 : 40

R4 : 50

R5 : 60

R6 : 70

R7 : -40

R8 : 0

R9 : 0

R10 (s1) : 0

R11 (fp) : 0

R12 (ip) : 0

R13 (sp) : 17408

R14 (lr) : 0

R15 (pc) : 20

CPSR Register

Negative (N) : 0

Zero (Z) : 0

Carry (C) : 0

Overflow (V) : 0

IRQ Disable : 1

FIQ Disable : 1

Thumb (T) : 0

CPU Mode : System

0x0000000f

Type here to search

OutputView WatchView

Execution starting ...

Execution ending, Instruction Count: 0 Elapsed Time: 00:00:00

Activate Windows  
Go to Settings to activate Windows

07:05 AM  
04-02-2020



Q5

**RegistersView**

General Purpose	Floating Point
Hexadecimal	Signed Decimal
R0 : 00000000	R0 : 00000000
R1 : 00000001	R1 : 00000001
R2 : 00000078	R2 : 00000078
R3 : 00000000	R3 : 00000000
R4 : 00000000	R4 : 00000000
R5 : 00000000	R5 : 00000000
R6 : 00000000	R6 : 00000000
R7 : 00000000	R7 : 00000000
R8 : 00000000	R8 : 00000000
R9 : 00000000	R9 : 00000000
R10 (s1) : 00000000	R10 (s1) : 00000000
R11 (fp) : 00000000	R11 (fp) : 00000000
R12 (ip) : 00000000	R12 (ip) : 00000000
R13 (sp) : 00005400	R13 (sp) : 00005400
R14 (lr) : 00000000	R14 (lr) : 00000101E

**MemoryView**

Word Size
8Bit
00000000

**OutputView**

Console Stdin/Stdout/Stderr

Execution ending, Instruction Count:0 Elapsed Time:00:00:00.0156281

Instructions Per second:0

0x600000dF

Type here to search

**Ramaiah Institute of Technology  
(Autonomous Institute, Affiliated to VTU)**

**Department of CSE**

**Programme: B.E  
Course: Computer Organization**

**Term: Jan to May 2019  
Course Code: CS45**

**Activity IV: Executing ARM programs using ARMsim simulator.**

<b>Name:</b> MADHUSUDAN PALAJEE	<b>Marks:</b> /10	<b>Date:</b>
<b>USN:</b> IMS18CS064	<b>Signature of the Faculty:</b>	

**Objective:** To simulate ARM Instruction set using ARMsim simulator.

**Simulator Used:** ARMSim 1.91 is a desktop application running in a Windows environment. It allows users to simulate the execution of ARM assembly language programs on a system based on the ARM7TDMI processor.

ARM enables the users both to debug ARM assembly programs and to monitor the state of the system while a program executes.

**Activity to be performed by students:**

- 1) Write an ARM program to generate Fibonacci Series.
- 2) Write an ARM to search an element in an array and print Y if found and print N if not found.
- 3) Write an ARM program to find the length of a string and copying one string to another.

Results/Conclusions and Snapshots: Take the snap shot of registers file and memory view

2/18/2020

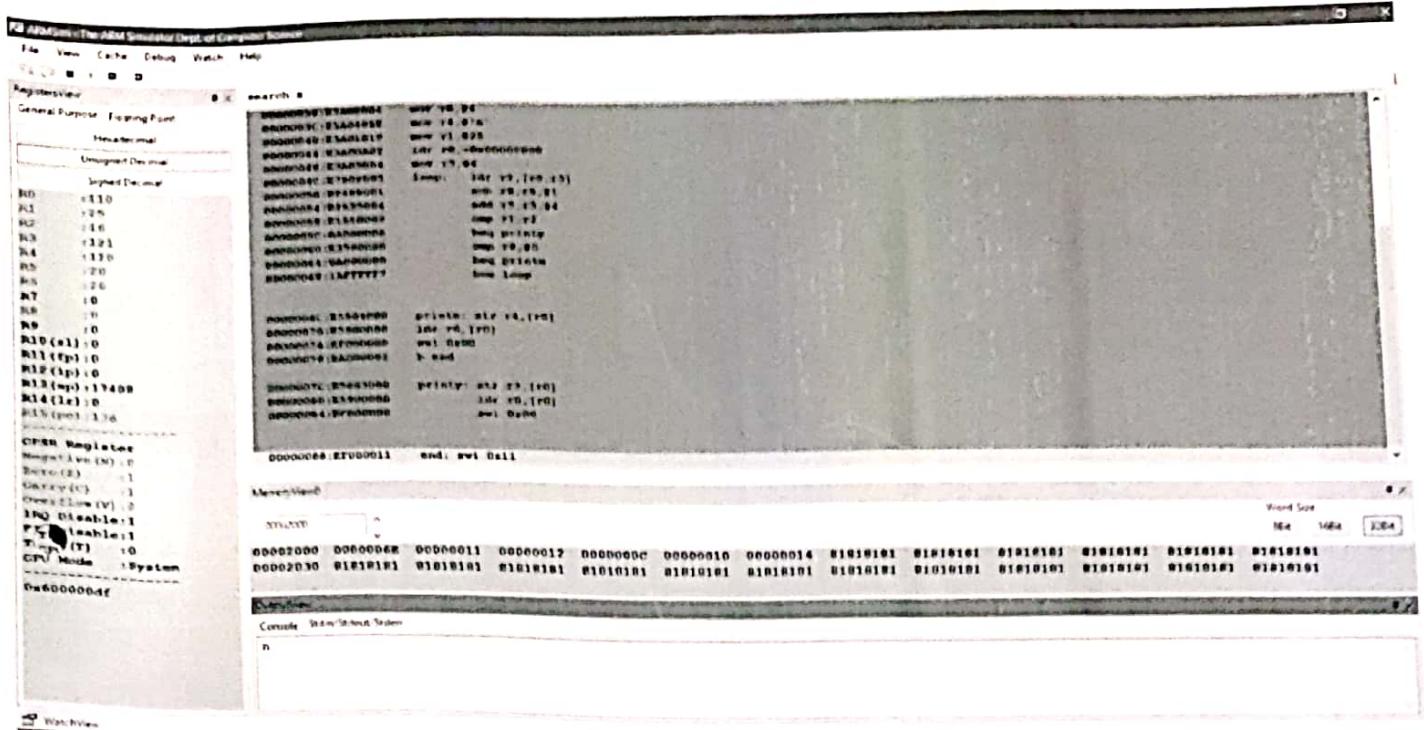
fibon-2.PNG

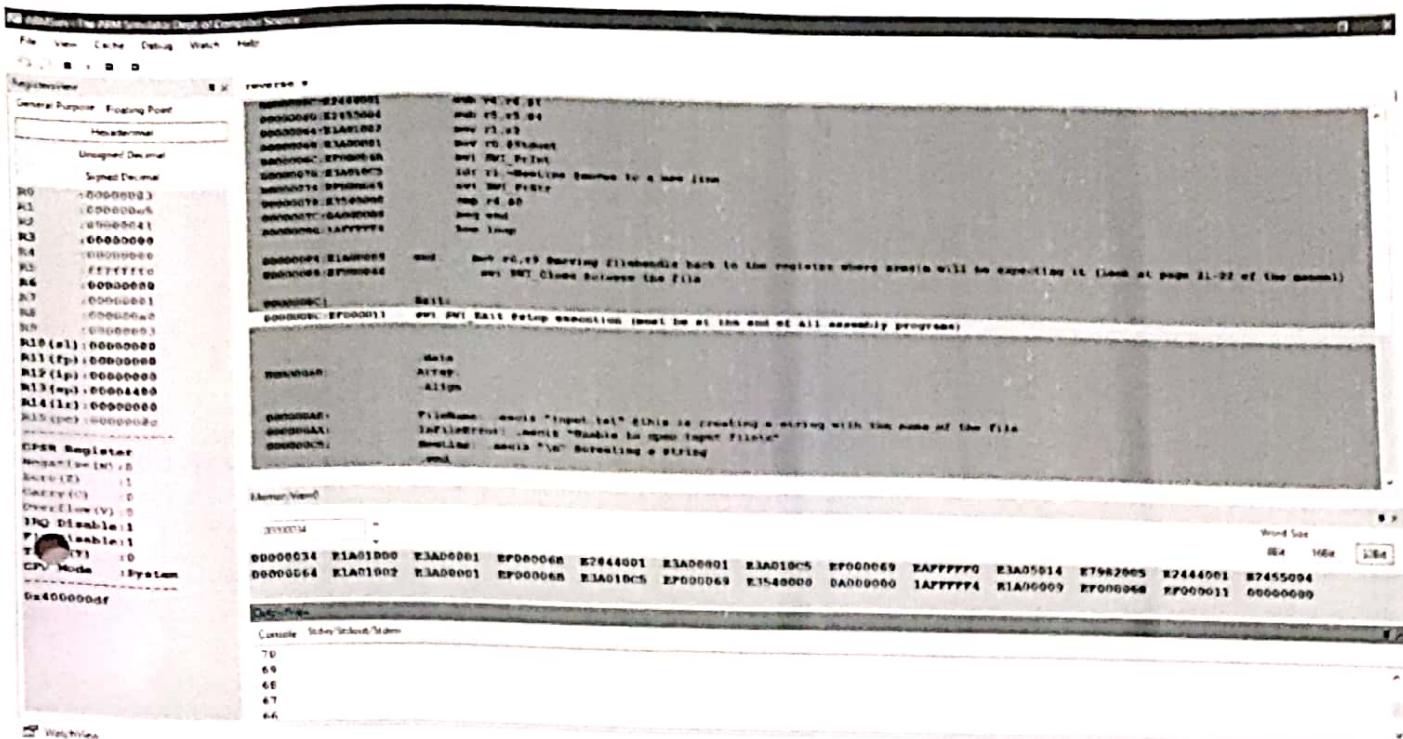
The screenshot shows the ARMulator interface with the following details:

- Registers View:** Shows general-purpose registers R0-R15 and floating-point registers F0-F15. Most registers contain values like 0x00000000 or 0x10000000.
- Memory View:** Displays memory starting at address 0x0002000. It shows a sequence of bytes: 00 00 00 00 00 00 00 00 ... followed by a series of 0x10000000 values.
- Output View:** Shows the message "Execution ending. Instruction Count:0 Elapsed Time:00:00:00.0080842 Instructions per second:0".

The assembly code for the fibon.s file is as follows:

```
00000000: E3A00000    mov r0, #0
00000004: E3A01001    mov r1, #1
00000008: E3A02014    mov r2, #0
0000000C: E3A03000    mov r3, #0
00000010: E3A04002    ldr r4, -0x000002000
00000014: E3A05000    mov r5, #0
00000018: E7F40000    ljmp   wtr r0, {r4,r5}
0000001C: E0000001    add r6, r0, r1
00000020: E1A00001    mov r0, r1
00000024: E1A01008    mov r1, r0
00000028: E7F50004    add r0, r5, #4
0000002C: E2000001    add r3, r2, #1
00000030: E1500002    CMP r3, r2
00000034: B0FFF7    bne .Lloop
00000038: EFP00002    swi 0x27
0000003C: EF000011    swi 0x11
```





NAME:

MADHUSUDAN BALAJEE

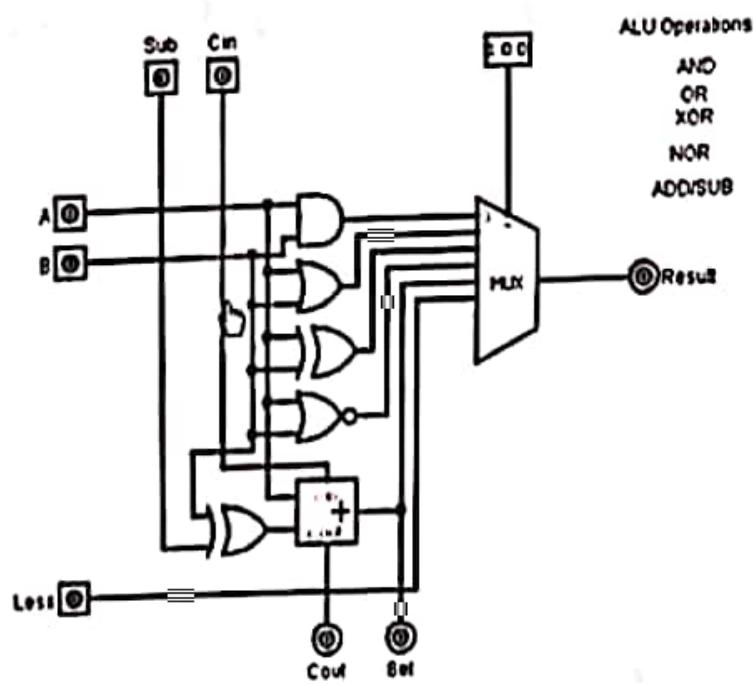
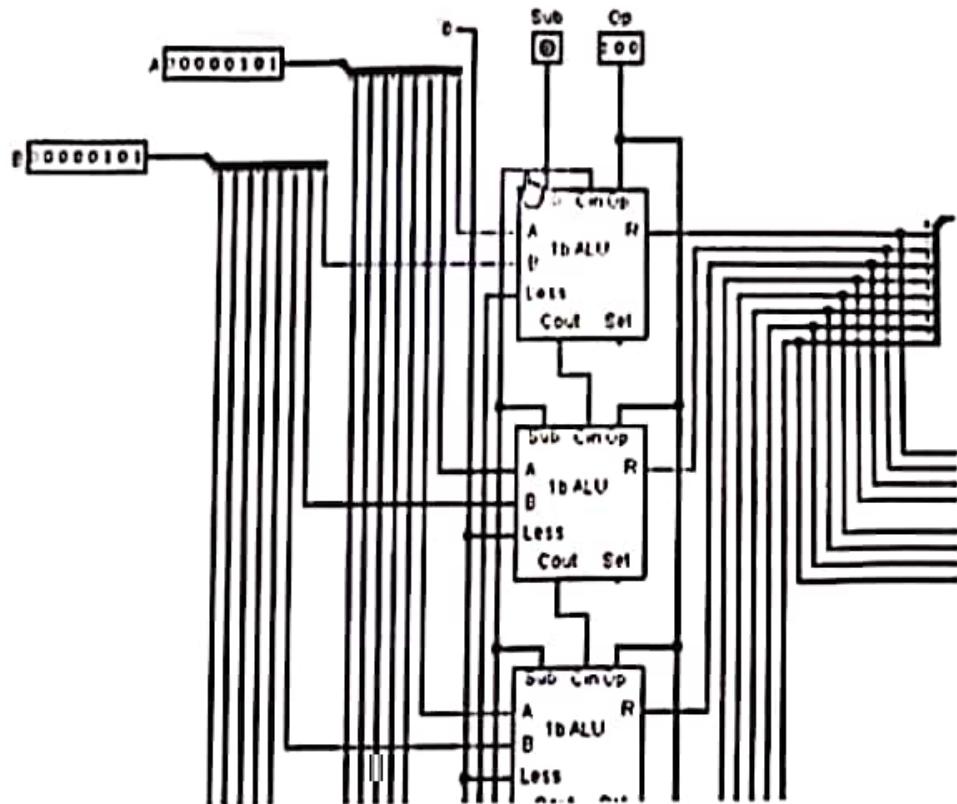
## ACTIVITY - 5

IMS18CS064

List out steps in designing ALU

1. Add the two i/p pins. Name them A and B.
2. Add or, name, ex-or, nor gates and a 1-bit adder.
3. Connect the A's and B's of all the gates to their respective pins.
4. Add an o/p pin and name it Result.
5. Add a 1-bit multiplexer with 3 select bits.
6. Connect outputs of all the gates to the mux.
7. Connect 3-bit input pins to mux.
8. Add i/p pin to Cin, and o/p pin to Cout.
9. Add an ex-or gate. Connect its o/p to Cout the first i/p must be connected to B and second to another i/p pin sub.
10. Add another i/p and name it Less. Connect it to mux.
11. Add one o/p pin and name it set, connect it to o/p of adder unit.

## Snapshots.



## ACTIVITY VI

MADHUVSUDAN BALAJEE  
MS18CS064

List out steps in designing memory system:

1. Add a RAM with separate load and store selected.
2. Add a counter and connect Q to A of the RAM.
3. Add a controller buffer and connect its o/p to the RAM.
4. Add a clock and connect to the i/p of the buffer.
5. Add a TTY unit with 32 rows and columns  
Make connects with RAM
6. Add a 7-bit random number generator,  
connect Q to D.
7. Add another controlled buffer, connect to TTY.  
Also add an i/p pin to the buffer.
8. Connect the o/p of the second buffer to the counter.
9. Connect a button to the counter.

Snapshots :

