Comparing the Experimental and Theoreotical Time of Discharge for a Rolled Capacitor in an RC Circuit

PHYS 122: Electromagnetism

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Abstract

This lab report describes an scientific experiment in which the time required for a fully charged capacitor t in a resistor-capacitor (RC) circuit to completely discharge was experimentally measured in real time using a graphical software, Logger Pro. This experimentally measured time was compared to the time that should theoretically have elapsed according a derived mathematical model that describes the time constant for an RC circuit. This mathematical model is derived from the equation that describes the voltage V_C of a discharging capacitor at a given time t, which is derived using Kirchoff's Law, Ohm's Law and the definition of capacitance, calculus, algebra and differential equations. Our results were inconclusive due to errors in correctly conducting our experimental procedure.

Keywords: capacitor, resistor, capacitance, voltage, RC circuits, potential difference, time constant

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Introduction

What is a capacitor?

A capacitor is a device that stores electric potential energy and electric charge. This is accomplished by insulating two conductors from one another - either using a vacuum or an insulator.

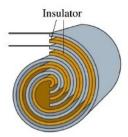


Figure 1: A rolled capacitor with a dieletric

The capacitor used in this experiment is a *rolled capacitor*, which essentially consist of two rolled metal plates that are separated by a gel called a dieletric which electrically insulates the plates from one another.

How are capacitors charged?

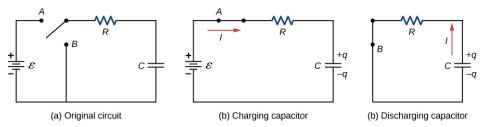


Figure 2: A series of RC circuits

Capacitors can be charged by charging the conductors such that one of the conductors has a negative charge, and the other has a positive charge of equal magnitude. This creates a fixed potential difference that is equal to the $voltage\ V_{ab}$ of whatever charged the capacitor.

In an RC circuit, the capacitor charges through the flow of current I that passes through a resistor R from a source of current ε - typically a battery - to the plates of the capacitor. When the source of current ε is removed from the circuit, the capacitor's charge q cannot be replenished, and it discharges through the resistor R.

The rate at which a capacitor charges or discharges is very similar to the mathematical relationship for the growth rate of populations.

$$\frac{dP}{dt} = kP$$

The rate of population growth $\frac{dP}{dt}$ changes proportionally to the size of the population P, where k is some arbitrary proportionality constant. This makes sense, because if there are more people in a community, there will also be more people who are able to have children and produce more people.

Similarly, the charging and discharging rate of a capacitor in an RC circuit proportionally to the amount of charge that is stored on the capacitor, because the current I must pass through a resistor R that slows the rate of charge.

What is the purpose of this report?

This report describes an attempt to investigate, through scientific experimentation, how exactly the *voltage* of a capacitor in an RC circuit changes over time.

This was accomplished by using the theoretical mathematical model that describes how the voltage of a capacitor in an RC circuit changes over time. This mathematical model can be derived using the same methods to derive the mathematical model to describe the charge of a capacitor, because voltage is directly proportional to charge.

$$V = \frac{Q}{C}$$

We then used mathematical model to derive an equation for the *theoretical* time it will take for an RC circuit based on a measured starting voltage, and compared this to the actual amount of time that it takes for a capacitor to completely discharge.

Why is this important?

It is useful to develop a better understanding of how capacitors interact with the other components of an RC circuit, because of how ubiquitous capacitors are in our day-to-day lives.

Capacitors have many uses in electronic and electrical systems - it is extremely rare for any product that uses electricity in some way to not have a capacitor. For example, capacitors are a fundamental component of digital cameras for producing camera flash.

Understanding how a capacitor charges and discharges provides us with the knowledge to be able to control how the electric energy in a circuit and use that electric energy more precisely.

Theory

Derivation of the Voltage of a Capacitor

Find an equation that describes the voltage of a capacitor in an RC circuit V_C .

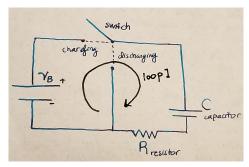


Figure 3: A general RC circuit

Using Kirchoff's loop laws, find an equation that descibes the voltage of the circuit.

$$V_B = V_R + V_C$$

= $IR + V_C$ Ohm's Law, $V = IR$

Because the resistor and the capacitor of the circuit share the same current, I_R and I_C are both equal to $C(\frac{dV_C}{dt})$. This can be substituted into the voltage equation:

$$V_B = IR + V_C$$
$$= \left[C(\frac{dV_C}{dt}) \right] R + V_C$$

This is a separable, first order differential linear equation. This means we can use algebra to rewrite this equation into a separable form and solve for V_C :

$$V_B - V_C = RC \frac{dV}{dt}$$
 Subtract V_C from both sides
$$\frac{dV}{V_B - V_C} = \frac{dt}{R_C}$$
 Rewrite each side in terms of only one variable

This equation is separated and can now be integrated:

$$\begin{split} \int \frac{dV}{V_B - V_C} &= \int \frac{dt}{RC} & \text{Integrate both sides.} \\ \ln(V_B - V_C) &= \frac{-t}{RC} + C_0 \quad C_0 \text{ is the constant of integration.} \\ e^{\ln(V_B - V_C)} &= e^{\frac{-t}{RC} + C^0} & \text{Raise both sides as a power of } e. \\ V_B - V_C &= e^{\frac{-t}{RC}} e^{C_0} & \text{Seperate the left term using exponent rules.} \\ V_B - V_C &= C e^{\frac{-t}{RC}} & \text{Rewrite } e^{C_0} \text{ as } C \text{ since it is a constant of integration.} \end{split}$$

The general solution of the differential equation has been found, but we need an initial condition to get the specific equations without a constant of integration.

We know that if the the capacitor is discharged, $V_C = 0$ at t = 0. We can use this as the initial condition for the differential equation.

Plug $V_C = 0$ and t = 0 into the general solution to find C:

$$V_B - (0) = Ce^{\frac{-(0)}{RC}}$$
 Plug in $V_C = 0; t = 0$
 $V_B = Ce^0$ e^0 is equal to 1

Now we can find the fully solved equation, by plugging in $V_B = C$:

$$V_B - V_C = Ce^{\frac{-t}{RC}}$$
 General solution. $V_C = V_B - V_B e^{\frac{-t}{RC}}$ Plug in $C = V_B$ and solve for V_C . $V_C = V_B (1 - e^{\frac{-t}{RC}})$ Factor out V_B .

Equation for the voltage of a charging capacitor V_C :

$$V_C = V_B(1 - e^{\frac{-t}{RC}})$$

To find the equation for the voltage of a discharging capacitor V_C , we need to use a different initial condition for the general solution.

If the capacitor is charged, $V_B = 0$ and V_C is equal to some value that depends on the capacitance of the capacitor C. For the purposes of this derivation, this will be called V_0 .

Plug these values into the general solution to solve for C:

$$(0) - (V_0) = Ce^{\frac{-(0)}{RC}}$$
 Plug in $V_B = 0; V_C = V_0; t = 0$
 $-V_0 = Ce^0$
 $-V_0 = C$ e^0 is equal to 1

Plug this value of C into the general solution:

$$V_B-V_C=Ce^{\frac{-t}{RC}}$$
 General solution.
$$-V_C=-V_0e^{\frac{-t}{RC}}$$
 Plug in $C=V_0$ and solve for V_C .
$$V_C=V_0e^{\frac{-t}{RC}}$$
 Divide both sides by -1.

Equation for the voltage of a discharging capacitor V_C :

$$V_C = V_0 e^{\frac{-t}{RC}}$$

Methods and Materials

Experiment Objectives

- Experimentally measure the time constant of a discharging resistor-capacitor (RC) circuit
- Compare this measured time constant to the theoretical time constant predicted with a mathematical model for the component values of the resistor and capacitor in the RC circuit

Required Materials

- A computer with the Logger Pro graphical software suite installed
- Vernier voltage probe
- Vernier LabPro interface
- 3 lead resistors with different resistance, between 30 Ω and 100 Ω
- 3 rolled capacitors with different capacitance, between 0.5 F and 1.0 F
- A digital multimeter
- Connecting wires
- Battery or a power supply

Experimental Procedure

Part 1: Set up the data collection tools

- 1. Turn on the computer with Logger Pro installed.
- 2. Power the LabPro interface by plugging it into an outlet, and connect the Vernier voltage probe to Channel 1 of the LabPro interface.



3. Connect the USB connector into the LabPro interface to the computer the port with labeled with a branching icon. Connect the USB connector into the USB port of the computer with Logger Pro installed.

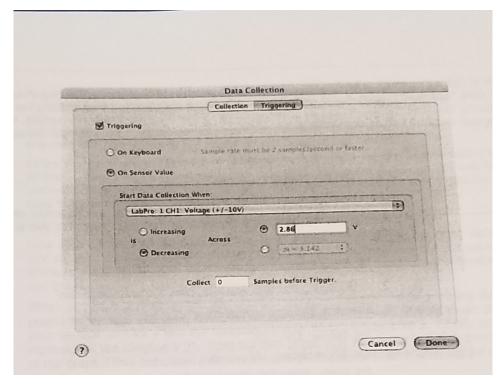


- 4. Find a file called 27 Capacitors.sql and open it using the Logger Pro software. A coordinate axis should be displayed.
- 5. Near the top of the window, click the **Experiment** menu, then click **Data** Collection.... A dialog box should appear.
- 6. Follow the following instructions in the image below and modify the settings in the dialog box as it states.
 - 1. Click the "Triggering" button (near the top of the dialog box).
 - 2. On the new page that appears, if the "Triggering" checkbox is unchecked, click it so that it has a checkmark in it.



- 3. Click the "On Sensor Value" radio button.
- Make sure that the correct port is selected in the drop-down list (if it isn't, choose the port
 you have the voltage probes connected to).
- 5. Click the "Decreasing" radio button.
- 6. Click the "V" radio button to enable the text field where you will enter a triggering voltage. This choice lets you start data collection when port one falls below a preset value. Enter a reads 2.86V, enter 2.84V here.
- 7. Enter 0 in the field labeled "Collect __ Samples before Trigger" (should already be 0).
- 8. When everything is set up correctly, the dialog box should look like the figure below (except that your triggering voltage will be different).

7. Verify that the dialog box now looks like the following image, then click "Okay."



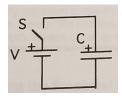
8. Keep this window open throughout the rest of the experiment.

Part 2: Charge the capacitor using a power supply

1. Measure and use the following table to record **only the resistance and the capacitance** of the resistor and the capacitor. Use a digital multimeter to measure resistance.

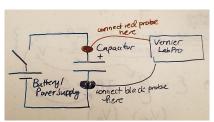
Circuit	Charging Voltage V_0 (V)	Capacitance (F)	Resistance (Ω)

2. Prepare a circuit that will charge the capacitor according to the following diagram. Do not complete the circuit by connecting the unconnected wire at this time.



The circuit should have:

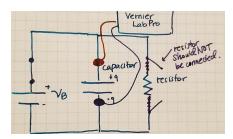
- \bullet a power supply or battery V
- a capacitor C
- connecting wires
- \bullet a "switch" represented by an unconnected wire S
- 3. Connect the Vernier voltage probes to the segments of wire just outside of the capacitor so they will only measure the capacitor's voltage. See the following diagram.



- 4. Connect the unconnected wire in the RC circuit from Step 1 to begin charging the capacitor in the RC circuit.
- 5. Wait until the voltage of the capacitor no longer changes. This will mean that it has reached its charging voltage.
- 6. Record this as the charging voltage in the table from Step 1 of this section of the experimental procedure. Do not disconnect any components from the circuit until instructed to do so.

Part 3: Discharge the capacitor until its voltage reaches 0

1. Add connecting wires and a resistor to the circuit so that it resembles the following diagram. You should now have an RC (resistor-capacitor) circuit. Do not modify the circuit in any other way.



- 2. Make sure the Vernier voltage probes are still connected to the circuit in the same place and do not connect the resistor to the circuit yet.
- 3. Go back to the LoggerPro window and double-click anywhere on the graph to open a graph options dialogue box.
- 4. Adjust the time scale of the graph so it is large enough so it will continue measuring the voltage until the voltage reaches 0. Use the following equation to calculate the number of seconds for the time scale:

Data collection duration in seconds(s) = 4*(C*R)

C is the capacitance in F and R is the resistance in Ω .

- 5. Click the green "Start" button at the top of the LoggerPro window. If you see "Waiting for trigger or data" at the top of the graph, the LoggerPro application is set-up correctly.
- 5. Connect the resistor to the circuit. Data collection should begin immediately as the voltage drops past the trigger value.
- 6. Take a screenshot or image of your graph.
- 7. Export your data as a CSV file. Save the image of your graph and the CSV file to a USB drive, cloud storage service like Google Drive or some other storage system.

Part 4: Repeat the experiment with different components

- 1. Get a new resistor and capacitor with different resistance and capacitance values.
- 2. Repeat all of the steps in the previous two sections, "Charge the capacitor using a power supply" and "Discharge the circuit until the voltage reaches 0" with the the new resistors two more times, with new resistors and capacitors with each iteration.

Data

Tables

The following tables are selected rows of the data sets that were created by LoggerPro as it recorded the voltage of each circuit in real time. The most relevant data for the purposes of our experiment is the starting data and the ending data, which is what is included in the tables.

Time (s)	Potential (V)	ln(V)
0	4.994 V	1.608
0.02	4.999 V	1.609
0.04	4.999 V	1.609
0.06	4.999 V	1.609
0.08	4.994 V	1.608
299.96	0.403 V	-0.909
299.98	$0.403~\mathrm{V}$	-0.909
300	0.403 V	-0.909

Table 1: Circuit 1

Time (s)	Potential (V)	ln(V)
0	4.994 V	1.608
0.02	4.999 V	1.609
0.04	4.999 V	1.609
0.06	4.999 V	1.609
0.08	4.994 V	1.608
	•••	
299.96	0.379 V	-0.972
299.98	0.379 V	-0.972
300	0.379 V	-0.972

Table 2: Circuit 2

Time (s)	Potential (V)	ln(V)
0	4.999 V	1.609
0.02	4.999 V	1.609
0.04	4.999 V	1.609
0.06	4.994 V	1.609
0.08	$4.994~\mathrm{V}$	1.608
	•••	
299.96	0.369 V	-0.998
299.98	0.369 V	-0.998
300	0.364 V	-1.011

Table 3: Circuit 3

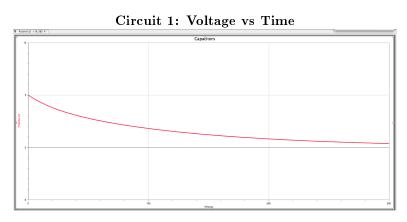
Graphs

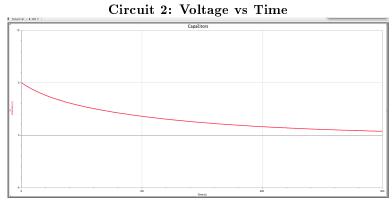
For each circuit, we measured the charging voltage V_b , the capacitance of the capacitor C and the resistance of the resistor R.

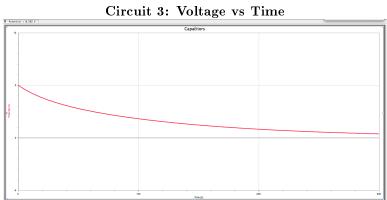
Circuit	Starting Potential V_0 (V)	End Potential V_C	C (F)	$R(\Omega)$
1	4.994 V	0.403 V	1.0 F	98.0
\parallel 2	4.999 V	0.379 V	1.0 F	98.0
3	4.999 V	0.364 V	1.0 F	98.0

Table 4: Measured values for RC circuits, Trials 1 - 3

The following figures are graphs of the logarithmic data generated by Logger Pro for each circuit.







Results

Time Constants

Determining Experimental Time Constants

The LoggerPro software did not measure the voltage of the circuit until it completely discharged, so it is not an exact measurement of the time that it would take for the circuit would completely discharge until 0 V.

For each trial, the experimental amount of time in seconds required for the voltage in each RC circuit to completely discharge is:

Experimental Data

Circuit	Start Potential (V_0)	End Potential (V_C)	$R(\Omega)$	C(F)	Time Elapsed (s)
1	4.994 V	0.403 V	98.0Ω	1.0 F	300.0 s
2	4.994 V	0.379 V	98.0Ω	1.0 F	$300.0 \mathrm{s}$
3	4.999 V	$0.364 \mathrm{\ V}$	98.0Ω	1.0 F	$300.0 \mathrm{\ s}$
Average	4.996 V	0.382 V	98.0Ω	1.0 F	300.0 s

Determining Theoretical Time Constants

Derivation of the Time Constant of the Discharging Capacitor

Recall the previously derived equation for the voltage of a discharging capacitor:

$$V_C = V_0 e^{\frac{-t}{RC}}$$

In order to get the theoretical time constant, we need to find an equation that expresses t in terms of variables that we have experimentally measured.

We can algebraically manipulate this equation to get an equation that describes the time elapsed t in order to discharge a capacitor in an RC circuit with a given capacitance C and a given resistance R a particular potential difference V_0 :

$$\begin{array}{ll} V_C = V_0 e^{\frac{-t}{RC}} & \text{Voltage of a discharging capacitor.} \\ \frac{V_C}{V_0} = e^{\frac{-t}{RC}} & \text{Divide both sides by } V_0 \\ \ln(\frac{V_C}{V_0}) = \ln(e^{\frac{-t}{RC}}) & \text{Take the natural logarithm of both sides.} \\ \ln(\frac{V_C}{V_0}) = \frac{-t}{RC} & \text{The e term gets cancelled out.} \\ \frac{\ln(\frac{V_C}{V_0})}{RC} = -t & \text{Divide both sides by the product RC.} \\ -\frac{\ln(\frac{V_C}{V_0})}{RC} = t & \text{Divide both sides by -1.} \end{array}$$

Equation for the time constant of a discharging capacitor t:

$$t = -\frac{\ln(\frac{V_C}{V_0})}{RC}$$

Example Calculation of Time Constant

For
$$V_C = 0.379V$$
, $V_0 = 4.994V$, $R = 98.0\Omega$, $C = 1.0F$:

$$t = -\frac{\ln(\frac{(0.379V)}{(4.994V)})}{(98.0F)(1.0F)}$$

= 263.11 seconds.

Although we should be measuring the theoretical time that it takes for the capacitor to completely discharge, meaning that we should calculate the time constant t using an end voltage V_C of 0, because LoggerPro stopped measuring the voltage at 0.379 V, we need to use that as our end voltage instead.

For each trial, the theoretical amount of time in seconds required for the voltage in each RC circuit to completely discharge is:

Theoretical Data

Circuit	Start Potential (V_0)	End Potential (V_C)	$R(\Omega)$	C(F)	Time Elapsed (s)
1	4.994 V	0.403 V	98.0Ω	1.0 F	256.84 s
2	4.994 V	0.379 V	98.0Ω	1.0 F	263.11 s
3	4.999 V	0.364 V	98.0Ω	1.0 F	267.33 s
Average	4.996 V	0.382 V	98.0Ω	1.0 F	262.42 s

Percent Error

The equation for calculating percent error is:

% Error =
$$\frac{|\text{experimental value} - \text{accepted value}|}{\text{accepted value}} \times 100\%$$

For the mean time constants for the experimental set of data and theoretical set of data, the percent error is:

% Error of Time Constant =
$$\frac{|300.00seconds - 262.42seconds|}{262.42 \text{ seconds}} \times 100\%$$
$$= \mathbf{14.31}\%$$

Experimental Uncertainty

Uncertainty in Measurements

The following values are the values that were used to calculate the results of the experiment. These values were measured by hand or with tools, such as the digital multimeter or the Logger Pro software, which inherently introduces a degree of uncertainty.

Value	Uncertainty (%)	Uncertainty Dif-	Possible Sources of
		ference	Uncertainty
Resistance (Ω)	± 6%	5.88 Ω	The manufacturer of the digital multimeter we used
			listed an estimated uncertainty of 6%.
Capacitance (F)	± 15%	0.15 F	The manufacturer of the ca-
			pacitors that we used listed
			an estimated uncertainty of
			15%.
Voltage (V)	$\pm~8\%$	0.299 V	The manufacturer of the
			digital multimeter we used
			listed an estimated uncer-
			tainty of 6%. An extra 2%
			of uncertainty is added to
			account for slightly incon-
			sistent start times in start-
			ing the data collection for voltage.
Time Constant (s)	± 5%	18s	This percentage accounts
			for possible errors with
			the LoggerPro software cor-
			rectly detecting the voltage
			trigger values.

Uncertainty in Calculations

The equation used for propogating uncertainty for general functions (including logarithms) is:

$$\delta R = \sqrt{\left(\frac{\delta R}{\delta X} \cdot \delta X\right)^2 + \left(\frac{\delta R}{\delta Y} \cdot \delta Y\right)^2 + \left(\frac{\delta R}{\delta Z} \cdot \delta Z\right)^2 + \dots}$$

where δR is the quantity we are attempting to calculate uncertainty for. X, Y, Z, etc are the composite quantities with their own individual uncertainties that are used to calculate R.

We need to use this equation to calculate the uncertainty of the time constant, since the equation for the time constant involves a logarithm.

Value	Uncertainty (%)	Possible Sources of Uncertainty
Theoretical Time Constant (s)	$\pm 18.02\%$	Propogated uncertainty using the
		above equation.

Conclusion

Although the time constants obtained from our experimental data were reasonably similar to the time constant obtained from our theoretical calculations, the high amount of uncertainty due to errors in conducting our experimental procedure means we cannot make a scientifically robust comparison between the experimental time constant and the theoretical time constant. Informally, it is fair to assume that the mathematical model that we derived for measuring the time-constant required for the capacitor of a RC circuit to fully discharge is reasonably accurate.

Because we used the same resistor and same capacitor for every RC circuit, we were unable to observe and infer the impact that different resistance and capacitance values might have on the time constant required for a capacitor in an RC circuit to completely discharge.

Researchers interested in investigating this subject further may find interesting results by replicating this experimental procedure and using different capacitor and resistor values.