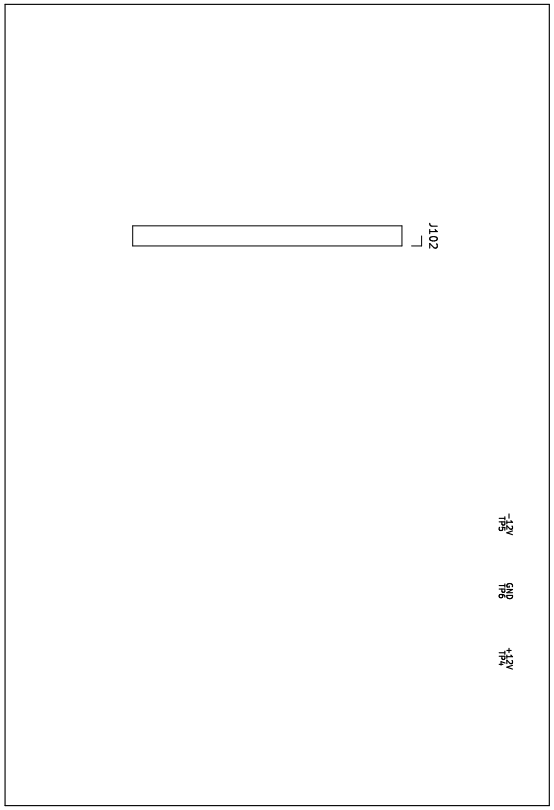
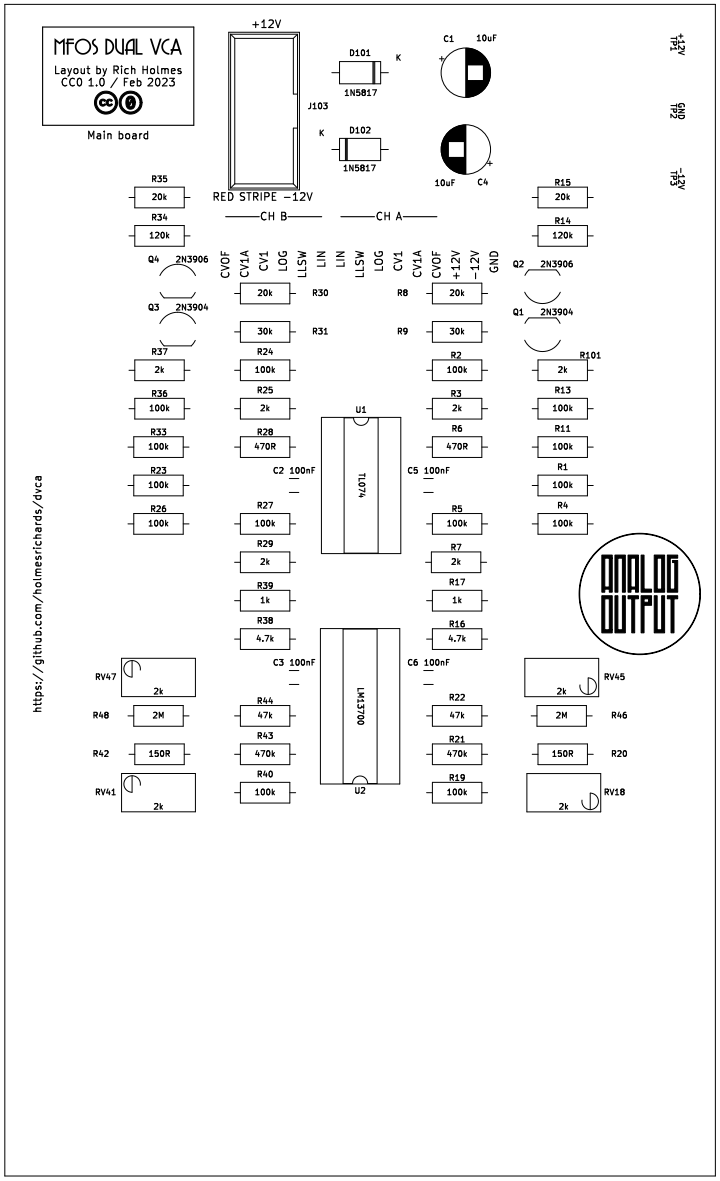

J102
+12V
GND
-12V


MFOS DUAL VCA
Layout by Rich Holmes
CC0 1.0 / Feb 2023
Main board
+12V
J103
RED STRIPE -12V
D101
1N5817
D102
1N5817
C1 10uF
C4 10uF
CH B
CH A
CVOF
CV1A
CV1
LOG
LLSW
LIN
LIN
LLSW
LOG
CV1
CV1A
CVOF
+12V
-12V
GND
Q4 2N3906
Q3 2N3904
Q2 2N3906
Q1 2N3904
R35 20k
R34 120k
R15 20k
R14 120k
R30 20k
R31 30k
R8 20k
R9 30k
R37 2k
R36 100k
R33 100k
R23 100k
R26 100k
R24 100k
R25 2k
R28 470R
R27 100k
R29 2k
R39 1k
R38 4.7k
R44 47k
R43 470k
R40 100k
R2 100k
R3 2k
R6 470R
R5 100k
R7 2k
R17 1k
R16 4.7k
R22 47k
R21 470k
R19 100k
R101 2k
R13 100k
R11 100k
R1 100k
R4 100k
C2 100nF
C3 100nF
C5 100nF
C6 100nF
U1
TL074
U2
LM13700
RV47 2k
R48 2M
R42 150R
RV41 2k
RV45 2k
R46 2M
R20 150R
RV18 2k
ANALOG OUTPUT
https://github.com/holmesrichards/dvca