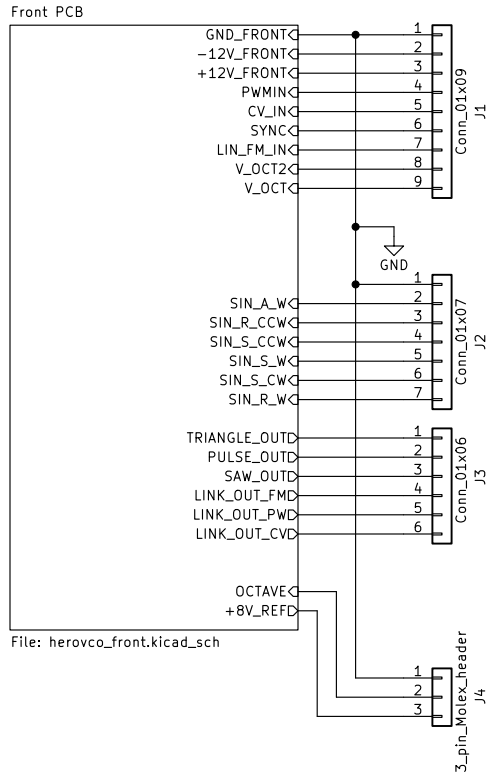
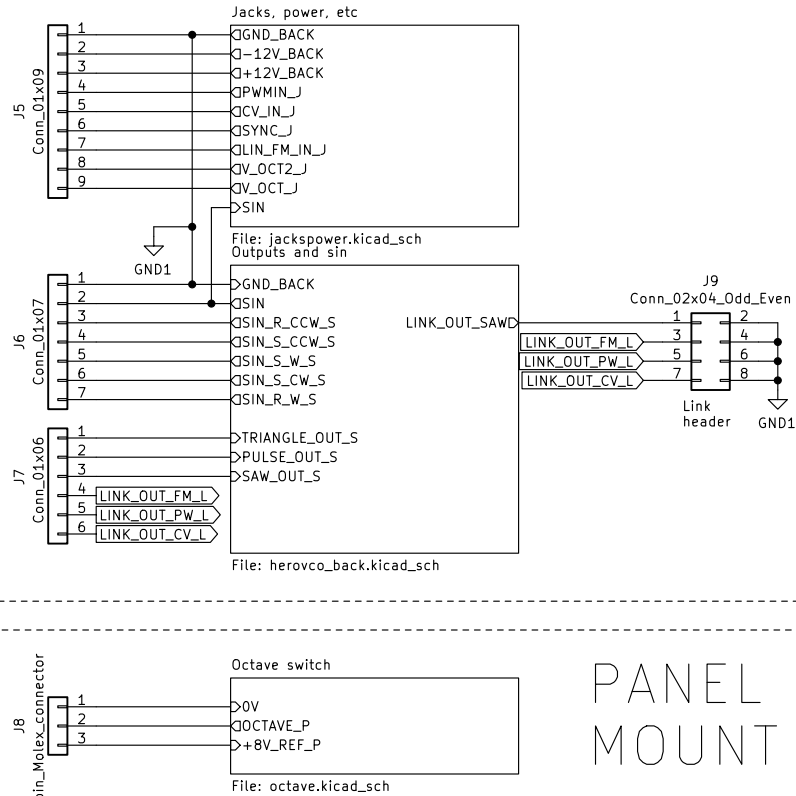


FRONT PCB



BACK PCB



Partly based on designs by Kassutronics, Thomas Henry, and LMNC
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Rich Holmes / Analog Output

Sheet: /
File: herovco.kicad_sch

Title: Hero VCO

Size: USLetter Date: 2021-11-16

Rev:

KiCad E.D.A. kicad 6.0.0-rc1-unknown-d785a97940~144~ubuntu20.04.1 Id: 1/5



3340

Regulated -5V is recommended by Alfa, and tests show it mostly or entirely suppresses PW frequency shift.

I did some tests that indicated 3M is better than 10M for suppressing high frequency oscillations, but in later tests 10M looked OK and 3M had a bad effect on minimum pulse width. I don't understand seeming inconsistency.

Use AS3340 or AS3340A. "AS3340A [has] much more output frequency stability against Vcc and VEE[and] benefits from improved symmetry of rising and falling edge of triangle signal (approx. 50%)."

+8V REFERENCE

-5V

SINE TRIMMERS

CONTROL VOLTAGES

Can dispense with this op amp if there's a better use for it somewhere else

These resistors! precision or hand matched

These resistors! precision or hand matched

ACME GRAF1 Holes

B1 Board

LINK CV goes through 100k to CV_NODE (summed inside 3340)
 LINK FM goes through 10nF + 1M to LIN_FM (summed inside 3340)
 LINK PW goes through 100k to PWM summing amp (summed outside 3340)
 LINK SAW goes to SYNC

POWER CONNECTIONS

For VCC=12V, PWM_IN value for 100% PW is typically 4.0 V, maximum 4.3 V. With PWMIN=0, PULSE_WIDTH ±12 V becomes ±5 V at op amp output. With PULSE_WIDTH=0, PWM_IN ±5 V becomes ±5 V at op amp output. Either way LINK_OUT_PW becomes ±5 V. At top of trimmer V is 5.5 to 0 V. Adjust trimmer to get correct upper limit. Zener clamps to 4.7 to 0 V.

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Rich Holmes / Analog Output

Sheet: /Front PCB/
 File: herovco_front.kicad_sch

Title: Hero VCO

Size: USLetter Date: 2021-11-16
 KiCad E.D.A. kicad 6.0.0-rc1-unknown-d785a97940~144~ubuntu20.04.1 Id: 2/5

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Rich Holmes / Analog Output

Sheet: /Front PCB/
File: herovco_front.kicad_sch

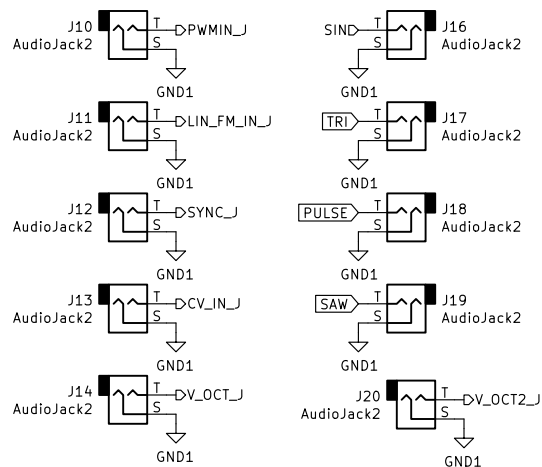
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Size: USLetter	Date: 2021-11-16
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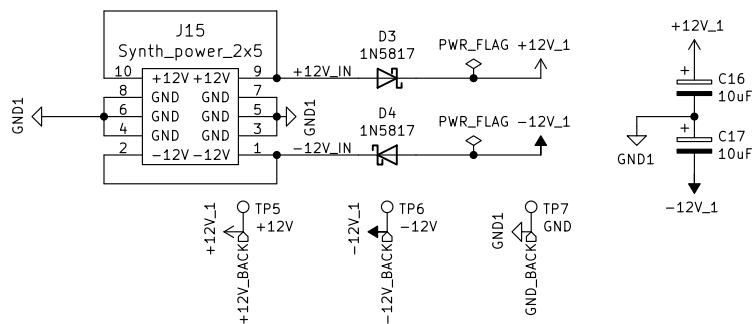
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JACKS



POWER



- H4 MountingHole
- H5 MountingHole
- H6 MountingHole

ACME

GRAF2
Holes

B2
Board

ACME

GRAF3
AO logo

ACME

GRAF4
CC0 logo

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Rich Holmes / Analog Output

Sheet: /Jacks, power, etc/

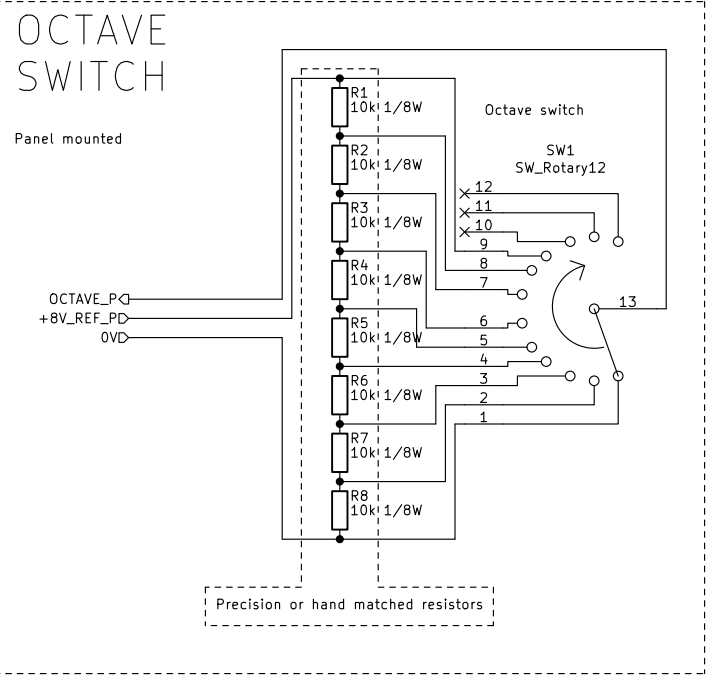
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Title: Hero VCO

Size: USLetter Date: 2021-11-16

Rev:

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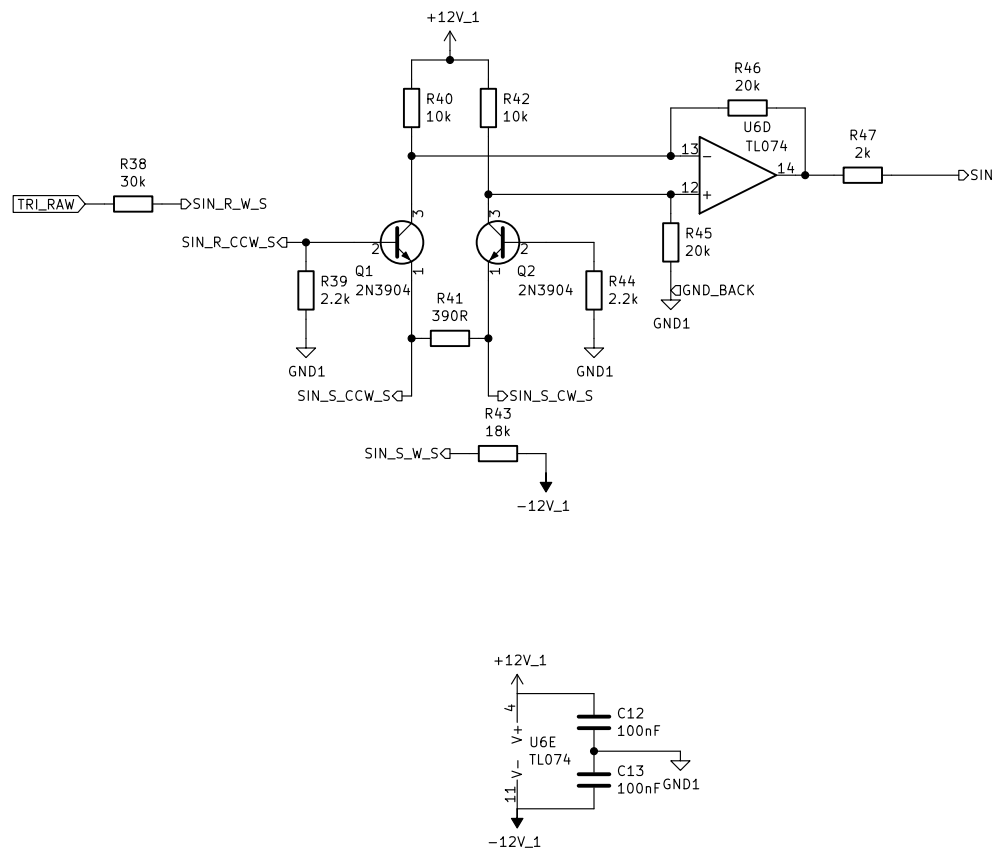
Rich Holmes / Analog Output

Sheet: /Octave switch/
File: octave.kicad_sch

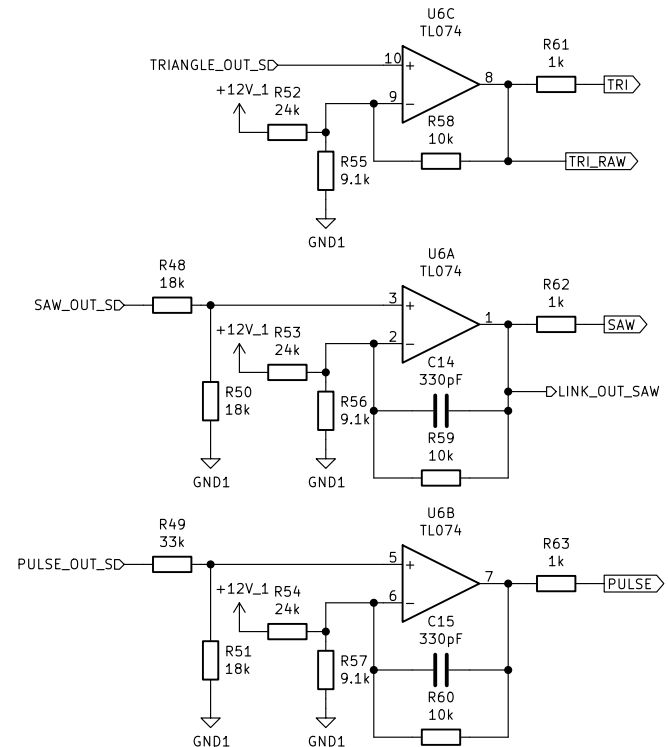
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Size: USLetter	Date: 2021-11-16	Rev:
KiCad E.D.A. kicad 6.0.0-rc1-unknown-d785a97940~144~ubuntu20.04.1		Id: 4/5

SINE SHAPING



OUTPUT CONDITIONING



Kassutronics used 100k feedback resistors and similarly large values for voltage divider resistors. AS3340 datasheet calls for a 51k pull-down on pin 4, so I reduced the voltage divider resistances by a factor ~ (51k/(180k+100k)). Op amp resistances were reduced by a factor of 10. Then stabilization caps were increased more to make $1/(2\pi RC) \sim 50\text{kHz}$.

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Rich Holmes / Analog Output

Sheet: /Outputs and sin/
 File: herovco_back.kicad_sch

Title: Hero VCO

Size: USLetter Date: 2021-11-16

Rev:

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