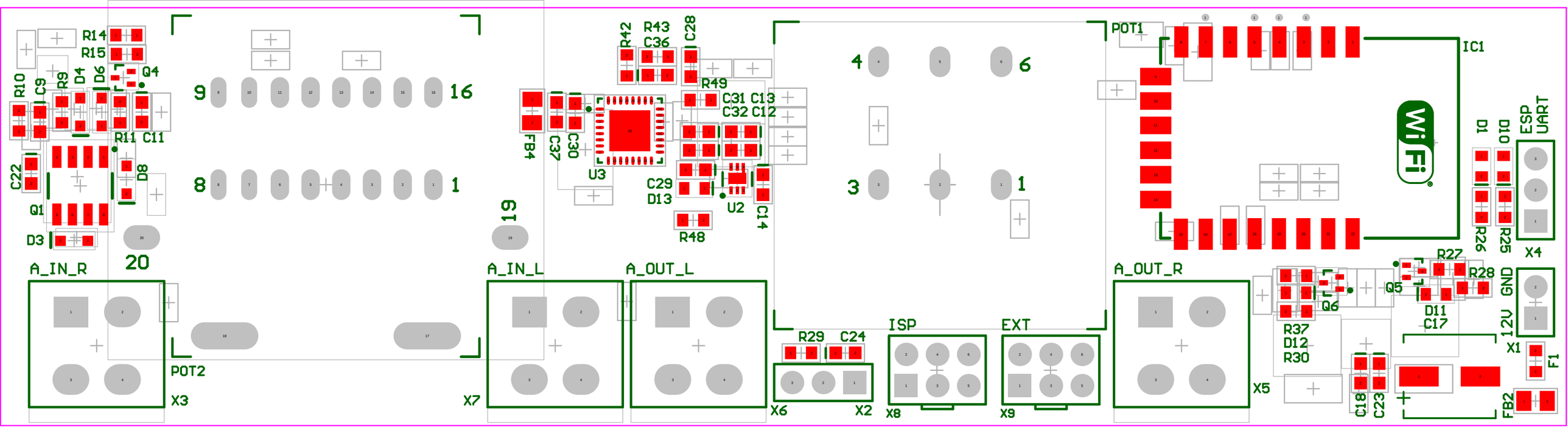
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R15
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C9
R9
D4
D6
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R11
C11
C22
D8
Q1
D3
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16
8
1
20
19
A_IN_R
POT2
X3
A_IN_L
X7
A_OUT_L
R42
R43
C36
C28
R49
FB4
C37
C30
U3
C31
C13
C32
C12
C29
D13
U2
C14
R48
4
6
3
1
R29
C24
X6
X2
ISP
X8
EXT
X9
POT1
IC1
A_OUT_R
X5
R37
D12
R30
Q6
Q5
R27
R28
D11
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R25
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F1
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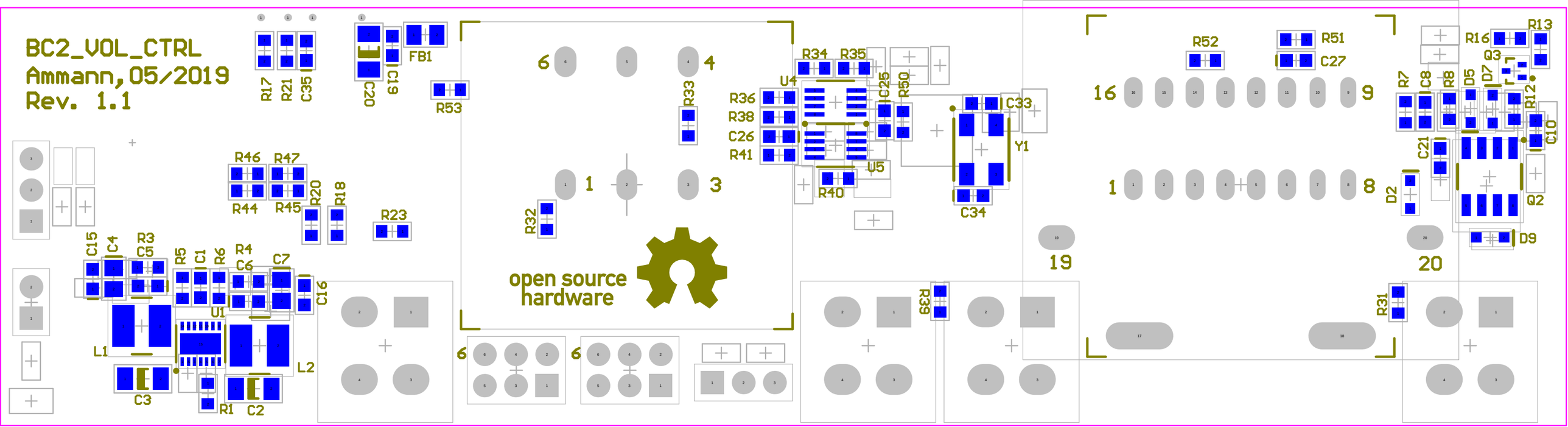
BC2_VOL_CTRL
Ammann,05/2019
Rev. 1.1
open source
hardware
R17
R21
C35
C20
C19
FB1
R53
R46
R47
R44
R45
R20
R18
R23
C15
C4
R3
C5
R5
C1
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R4
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C7
C16
U1
L1
L2
C3
R1
C2
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R33
R32
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R38
C26
R41
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U5
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R51
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C8
R8
D5
D7
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R13
Q3
R12
C10
C21
D2
Q2
D9
R31