

HOLE

MARKS

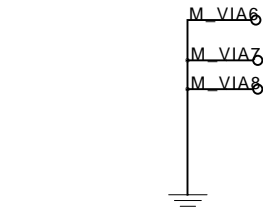
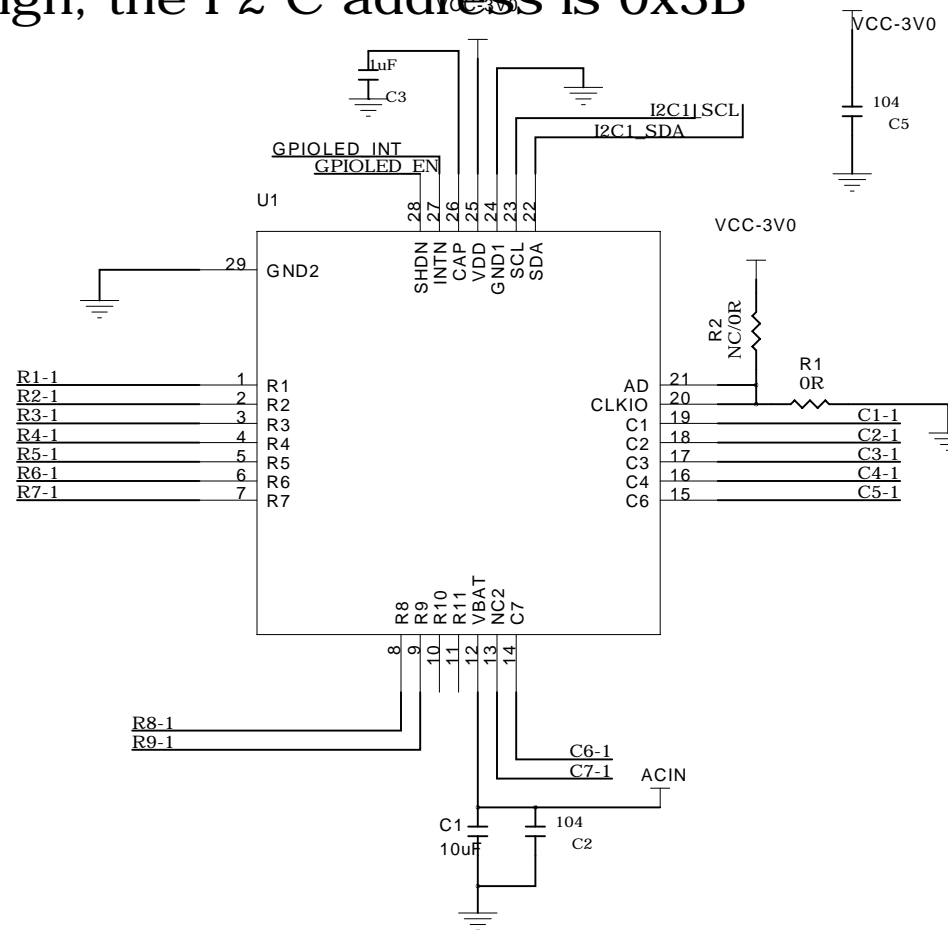
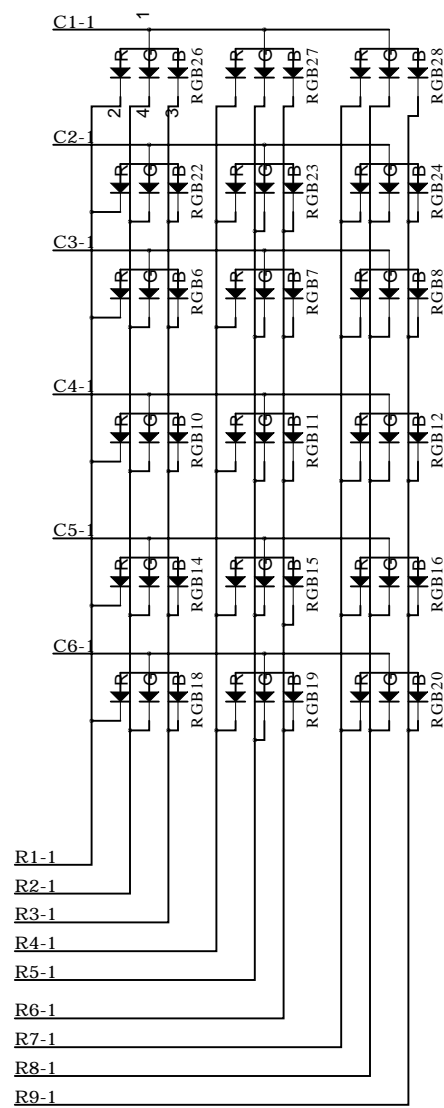


Diagram illustrating the marking process for four items (M1, M2, M3, M4). Each item is represented by a square box, and a vertical line connects the box to the word "MARK" below it.

When AD level is low, the I2C address is 0x3A
when AD level is high, the I2C address is 0x3B



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