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SM 1 SM 1 EECE 488 – Set 1: Introduction and Background. EECE488: Analog CMOS Integrated Circuit Design Introduction and Background. Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca. Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged.

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Behzad Razavi. IEEE Donald O. Pederson Award "For his pioneering contributions to the design of high speed CMOS communication circuits".

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The input offset voltage of a comparator is the input voltage at which its output changes from one logic level to the other. It may be caused by device mismatch or may be inherent to the design of a comparator. Random circuit noise can cause the output to change from one logic level to the other, even when the comparator input is held constant.

Analog Integrated Circuit Design 2nd Edition
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1.1 Introduction. The analysis and design of integrated circuits depend heavily on the utilization of suitable models for integrated-circuit components. This is true in hand analysis, where fairly simple models are generally used, and in computer analysis, where more complex models are encountered.

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