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Next assume A=I and CI=O. On the arrival of an idle slot in the downstream direction, 1) Place the frame in the idle slot, setting the busy bit; 2) If there is a waiting frame in the supplementary queue, put it in the vinu::ti queue, place C2in counter 1 and set C2to 0; 3) If there is no waiting frame, set A=O. On the arrival of a request bit in the upstreamdirection, increment C2.

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