

# *Cortex A15 Technical Reference Manual*

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### **Cortex A15 Technical Reference Manual**

This book is for the Cortex-A15 processor. Note The Cortex-A15 processor has between one and four processors in a single MPCore device. Product revision status The rnpn identifier indicates the revision status of the product described in this book, where: rn Identifies the major revision of the product.

### **Cortex-A15 Technical Reference Manual - ARM architecture**

ARM Cortex-A15 MPCore Technical Reference Manual, TRM. This book gives reference documentation for the Cortex-A15 MPCore processor. It contains programming details for registers and describes the memory system, caches, debug trace, and interrupts.

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### **Cortex-A15 Technical Reference Manual | manualzz.com**

This preface introduces the Cortex-A15 MPCore Technical Reference Manual. It contains the following sections: • About this book on page vii. • Feedback on page xi. Note • The out-of-order design of the Cortex-A15 MPCore processor pipeline makes it impossible to provide accurate timing information for complex instructions. The timing

### **Cortex -A15 MPCore - ARM architecture**

• ARM® Versatile™ Express Boot Monitor Technical Reference Manual (ARM DUI 0465) • ARM® Cortex®-A15 Technical Reference Manual (ARM DDI 0438) • Cortex®-A7 MPCore Technical Reference Manual (ARM DDI 0464) • AMBA® Network Interconnect (NIC-301) Technical Reference Manual (ARM DDI 0397)

### **ARM CoreTile Express A15×2 A7×3 Technical Reference Manual**

The ARM Cortex-A15 MPCore is a 32-bit processor core licensed by ARM Holdings implementing the ARMv7-A architecture. It is a multicore processor with out-of-order superscalar pipeline running at up to 2.5 GHz.

### **ARM Cortex-A15 - Wikipedia**

AM572x Sitara Processor Technical Reference Manual (Rev. K) Errata. AM572x Sitara Processors Silicon Errata (Silicon Revision 2.0, 1.1) (Rev. L) Description & parametrics Technical ... Programmability is provided by dual-core Arm Cortex-A15 RISC CPUs with Neon™ extension, and two TI C66x VLIW floating-point DSP cores. ...

### **AM5728 Sitara Processor: Dual Arm Cortex-A15 ... - TI.com**

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### **AM3359 Errata, Application notes, User guides, Selection ...**

• ARMv7-M Architecture Reference Manual (ARM DDI 0403) • ARM Cortex-M4 Integration and

Implementation Manual (ARM DII 0239) • ARM ETM-M4 Technical Reference Manual (ARM DDI 0440)  
• ARM AMBA® 3 AHB-Lite Protocol (v1.0) (ARM IHI 0033) • ARM AMBA™ 3 APB Protocol  
Specification (ARM IHI 0024) • ARM CoreSight™ Components Technical ...

### **Cortex-M4 Technical Reference Manual - ARM architecture**

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### **Cortex-A5 Technical Reference Manual**

13 June 2007 E Non-Confidential Minor update with no technical changes 11 April 2008 F  
Confidential Limited release for SC300 r0p0 26 June 2008 G Non-Confidential First Release for r2p0

### **Cortex-M3 Technical Reference Manual - Keil**

This manual is written to help system designers, system integrators, verification engineers, and software programmers who are implementing a System-on-Chip (SoC) device based on the Cortex-M3 processor.

### **Cortex-M3 Technical Reference Manual - ARM architecture**

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• ARM® Cortex®-A53 MPCore Processor Technical Reference Manual (ARM DDI 0500). • ARM® Cortex®-A53 MPCore Processor Advanced SIMD and Floating-point Extension Technical Reference Manual (ARM DDI 0502). • ARM® Architecture Reference Manual, ARMv8, for ARMv8-A architecture profile (ARM DDI 0487).

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