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Raytheon Sentinel - Wikipedia

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Date Version Changes May 2016 2016.05.03 August 2014 Initial release Added Sections: Updates: Clock Manager Block Diagram. Updated mux output route. NOC clock added. Peripheral Clocks block update C15 input for PLL1 has been removed throughout document. Date Version Changes August 2014 2014.08.18 Initial release.

Intel Arria 10 Hard Processor System Technical Reference ...

Introduction to the Hard Processor System Document Version Changes 2016.05.03 1.3 Minor updates. 1.2 Updated address spaces section. 1.1 Added peripheral region address map. 1.0 Initial release. Document Version Changes FREF, FVCO, and FOUT Equations section updated.

Arria V Hard Processor System Technical Reference Manual

application [product] Product version and description. It seems all fex files at this moment are at version 1.0 and use the default evaluation board name.

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Overview Main components of the A10 . CPU: Cortex-A8 1GHz (ARM v7) Processor which have both

VFPv3 and NEON co-processors: FPU: standard ARM VFPv3 FPU Floating Point Unit SIMD: NEON (ARM's extended general-purpose SIMD vector processing extension engine)

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2019 CUSEC New Madrid Seismic Zone Tabletop Exercise March 20, 2019 | Springfield, IL. In collaboration with FEMA's National Exercise Program, CUSEC participated in a tabletop exercise on March 20, 2019 in Springfield, IL.

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Eurofighter Typhoon - Wikipedia

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