

## M.Sc. CS / AI & CS – Term 1

### Computer Systems

### MIPS, Stacks, RPN Expressions and Complexity

**Exercise#1:** Given the MIPS Instruction set, convert the following MIPS assembly code into Java-like code. Note that “bgt” instruction is for “branch greater than” and is used to jump to a label, if a register value is more than the provided constant. You are expected to consider the following register assignment while writing Java code.

MIPS Register	Java Variable
\$1	i
\$2	tmp
\$3	sum

#### MIPS Assembly Code

```

and $1,$1,$0
and $2,$2,$0
and $3,$3,$0
label:
bgt $1,14,exit
mult $2,$1,2
add $3,$3,$2
addi $1,$1,1
j label
exit:

```

Load/Store	Load word	lw \$1 , &a	Load contents of address &a into register r1
	Store word	sw \$1 , &a	Store contents of register r1 into address &a
	Load immediate	li \$1 , 100	Load value 100 into \$1
Arithmetic	Add	add \$1,\$2,\$3	r1 = r2 + r3
	Subtract	sub \$1,\$2,\$3	r1 = r2 - r3
	Add immediate	addi \$1,\$2,100	r1 = r2 + 100
	Add unsigned	addu \$1,\$2,\$3	r1 = r2 + r3
	Subtract unsigned	subu \$1,\$2,\$3	r1 = r2 - r3
	Add immediate unsigned	addiu \$1,\$2,100	r1 = r2 + 100
Multiply/Divide	Multiply	mult \$1,\$2,\$3	r1 = r2 x r3
	Multiply Unsigned	multu \$1,\$2,\$3	r1 = r2 x r3
	Multiply Immediate	multi \$1,\$2,4	r1 = r2 x 4
	Divide	div \$1,\$2,\$3	r1 = r2 / r3
	Divide unsigned	divu \$1,\$2,\$3	r1 = r2 / r3
	Divide Immediate	divi \$1,\$2,5	r1 = r2 / 5
Logical	AND	and \$1,\$2,\$3	r1 = r2 & r3
	OR	or \$1,\$2,\$3	r1 = r2   r3
	NOR	nor \$1,\$2,\$3	r1 = !(r2   r3)
	AND immediate	andi \$1,\$2,100	r1 = r2 & 100
	OR immediate	ori \$1,\$2,100	r1 = r2   100
	XOR immediate	xori \$1,\$2,100	r1 = r2 ^ 100
	Shift left logical	sll \$1,\$2,10	r1 = r2 « 10
	Shift right logical	srl \$1,\$2,10	r1 = r2 » 10



**Exercise#2:** Suppose a letter means push and an asterisk means pop in the following sequence. Give the sequence of values returned by the pop operations when this sequence of operations is performed on an initially empty LIFO stack.

A U E \* \* I R \* \* K \* \*

E A S \* Y \* Q U E \* \* \* S T \* \* \* I O \* N \* \* \*

**Exercise#3:** Suppose that an intermixed sequence of push and pop operations are performed. The push operation pushes the integers 0 through 9 in order; the pop operations print out the return value. Which of the following sequences could not occur?

- (a) 4 3 2 1 0 9 8 7 6 5
- (b) 4 6 8 7 5 3 2 9 0 1
- (c) 2 5 6 7 4 8 9 3 1 0
- (d) 4 3 2 1 0 5 6 7 8 9

**Exercise#4:** Convert the following infix expressions to postfix (RPN) expressions using the Shunting Yard algorithm. With the help of a Stack and using the RPN Expression evaluation scheme, evaluate them as well.

- (a)  $4 + ( 6 * 2 ) - 15$
- (b)  $4 * ( 4 + 10 / 2.5 - 8 )$
- (c)  $17 + 21 * 3 / 7 + 21$

**Exercise#5:** Run the above RPN expressions through with a stack to check they calculate the correct answer.