Switches are set to 10'b 00 0101 1010 (16'h005A) to jump to the start address x005A of the Sort Test program

Reset the entire SLC-3 processor Switches are first set All registers are cleared Set SW to 10'b 00 0000 0010 FSM is set to Halted state (16'h0002) to call the SORT FUNCTION to 10'b 00 0000 0000 Continue is triggered to Original Data stored in the memory from address execute the SORT FUNCTION 74 to 89 before SORT FUNCTION executes 008c 00db 0046 10007 001f 1 10003 0003 006b 1 004e 0007 Sorted Data stored in the memory from address 74 to 89 after SORT FUNCTION finishes execution Cursor 1 9939.173 ns