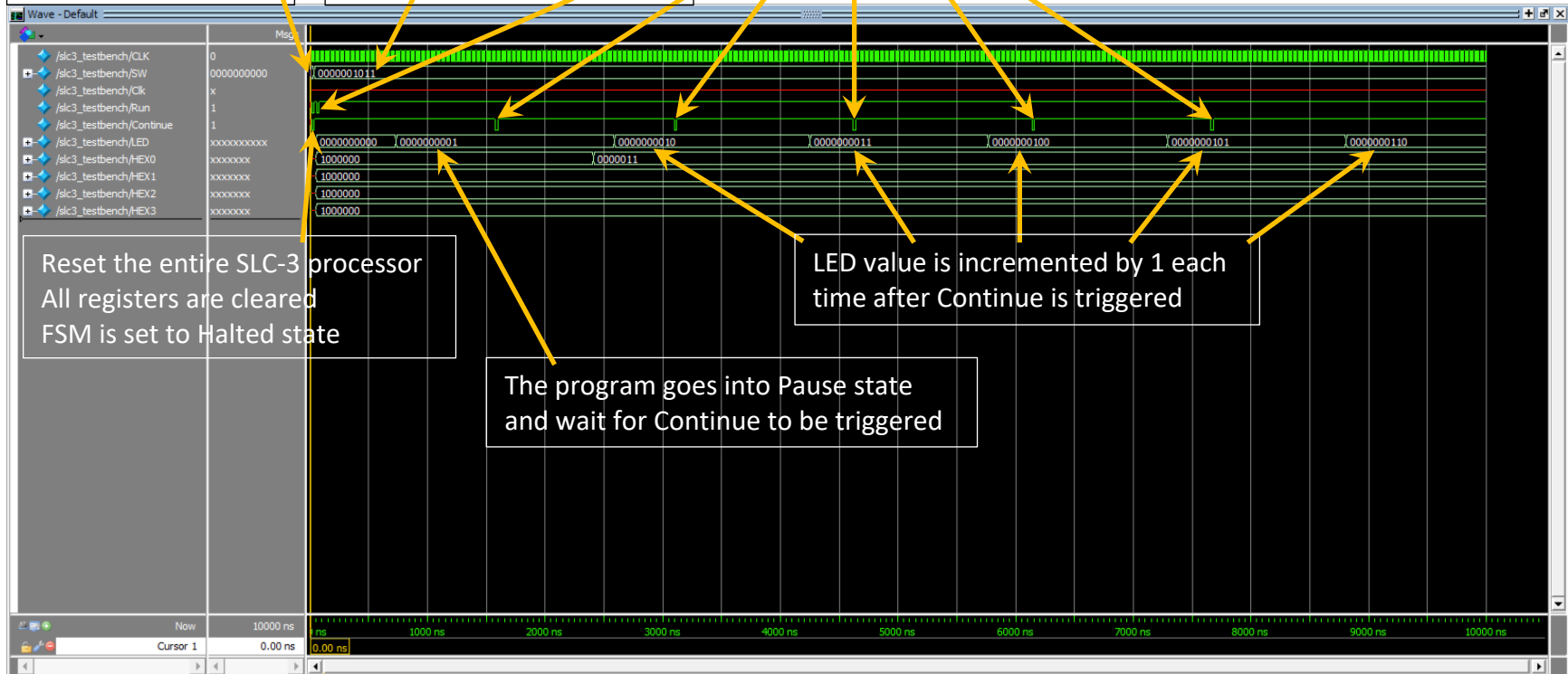


Switches are set to 10'b 00 0000 1011  
(16'h000B) to jump to the start address x000B  
of the Self-Modifying Code Test program

Switches are first set  
to 10'b 00 0000 0000

Run is triggered to start the  
Basic I/O Test 2 program

Trigger Continue



Reset the entire SLC-3 processor  
All registers are cleared  
FSM is set to Halted state

The program goes into Pause state  
and wait for Continue to be triggered

LED value is incremented by 1 each  
time after Continue is triggered