Switches are set to 10'b 00 0000 0011 Switches are set to Switches are set to Switches are set to (16'h0006) to jump to the start address 10'b 00 0000 0001 10'b 00 0000 0111 10'b 00 0000 0101 x0006 of the Basic I/O Test 2 program (4 in decimal) (1 in decimal) (5 in decimal) Run is triggered to start the Continue must be triggered to display Switches are first set to 10'b 00 0000 0000 Basic I/O Test 2 program real time SW value on 7-segment display 24sf (0...) (00) dc0 (0ffc (623f (0...) (0001 (d.02 (0ffc (623f (0...) (0005 dc02 0000 (5020) 623f / . (c040) d801 c040 d801 Reset the entire SLC-3 processor MAR = 16'hFFFF & Reading **HEXO** is displaying HEXO is displaying All registers are cleared Load SW value instead of M[xFFFF] the SW value 16'h4 the \$W value 16'h5 FSM is set to Halted state MAR = 16'hFFFF & Writing HEXO is displaying the Store value in internal register and SW value 16'h1 display instead of storing to M[xFFFF] Cursor 1