

Switches are first set to 8'b 0000 0000

Switches are set to 8'b 0000 0111 (7 in decimal)

Switches are set to 8'b 0011 1011 (59 in decimal)

First Half of the final result stored in register A
8'h01 = 8'b 0000 0001

Reset is set to 1 to clear all the registers: register X, register A, and register B since it's an active-high signal

