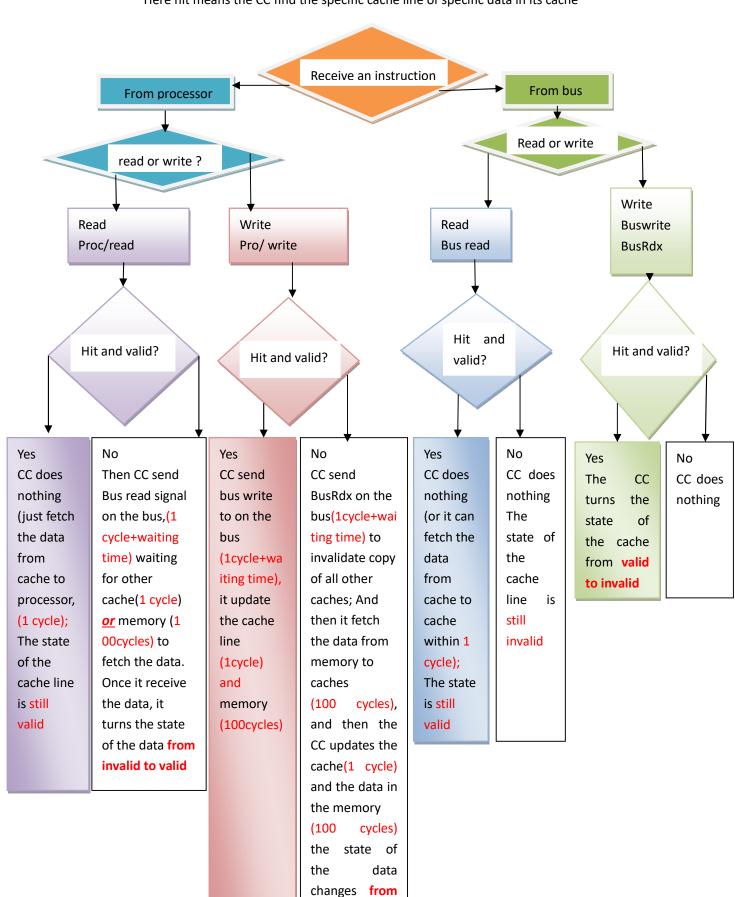
Cache controller(CC) aspect

Waiting time=time to obtain the control of the bus, it might be 0 cycles; Here hit means the CC find the specific cache line of specific data in its cache



invalid to valid