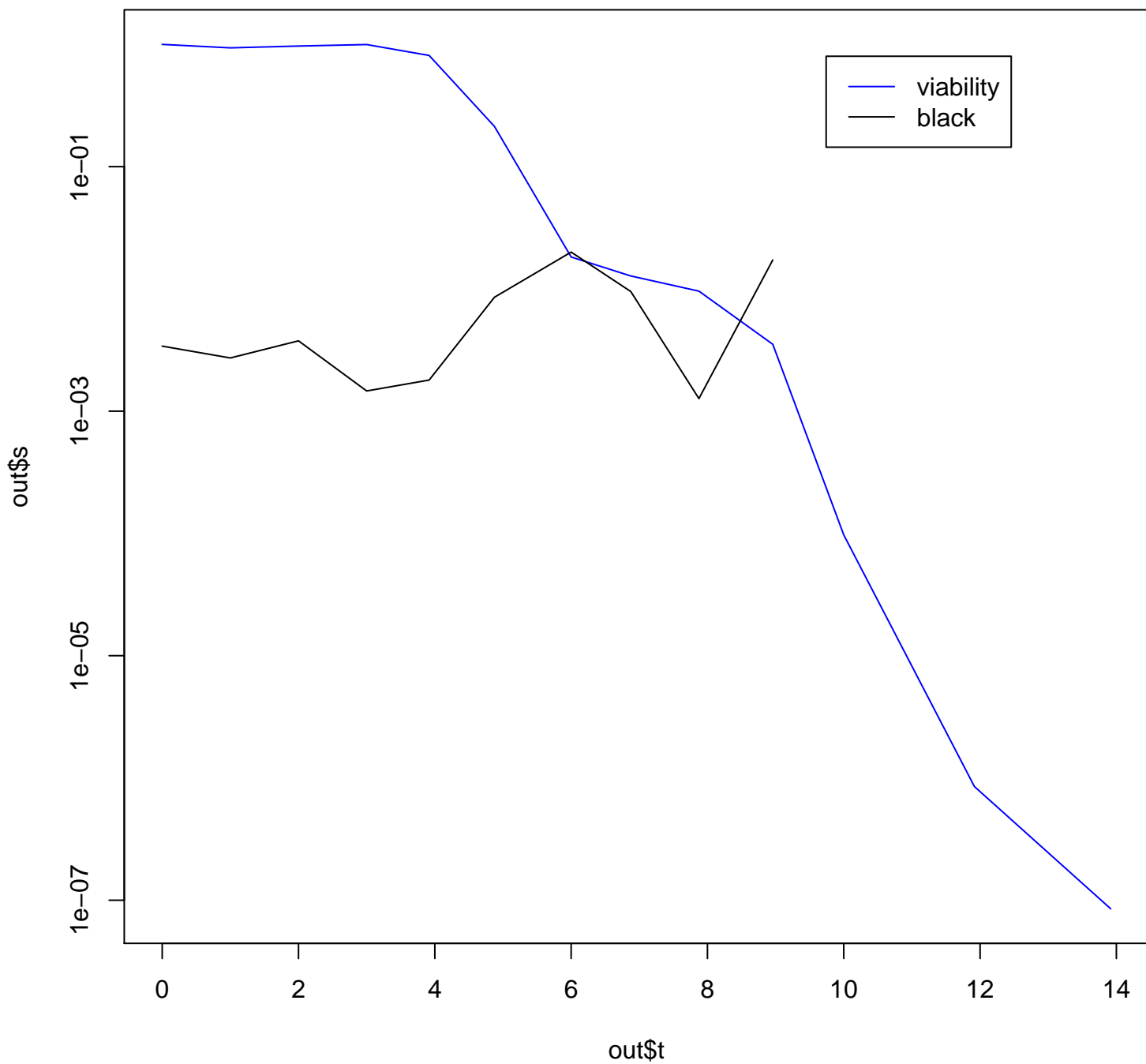
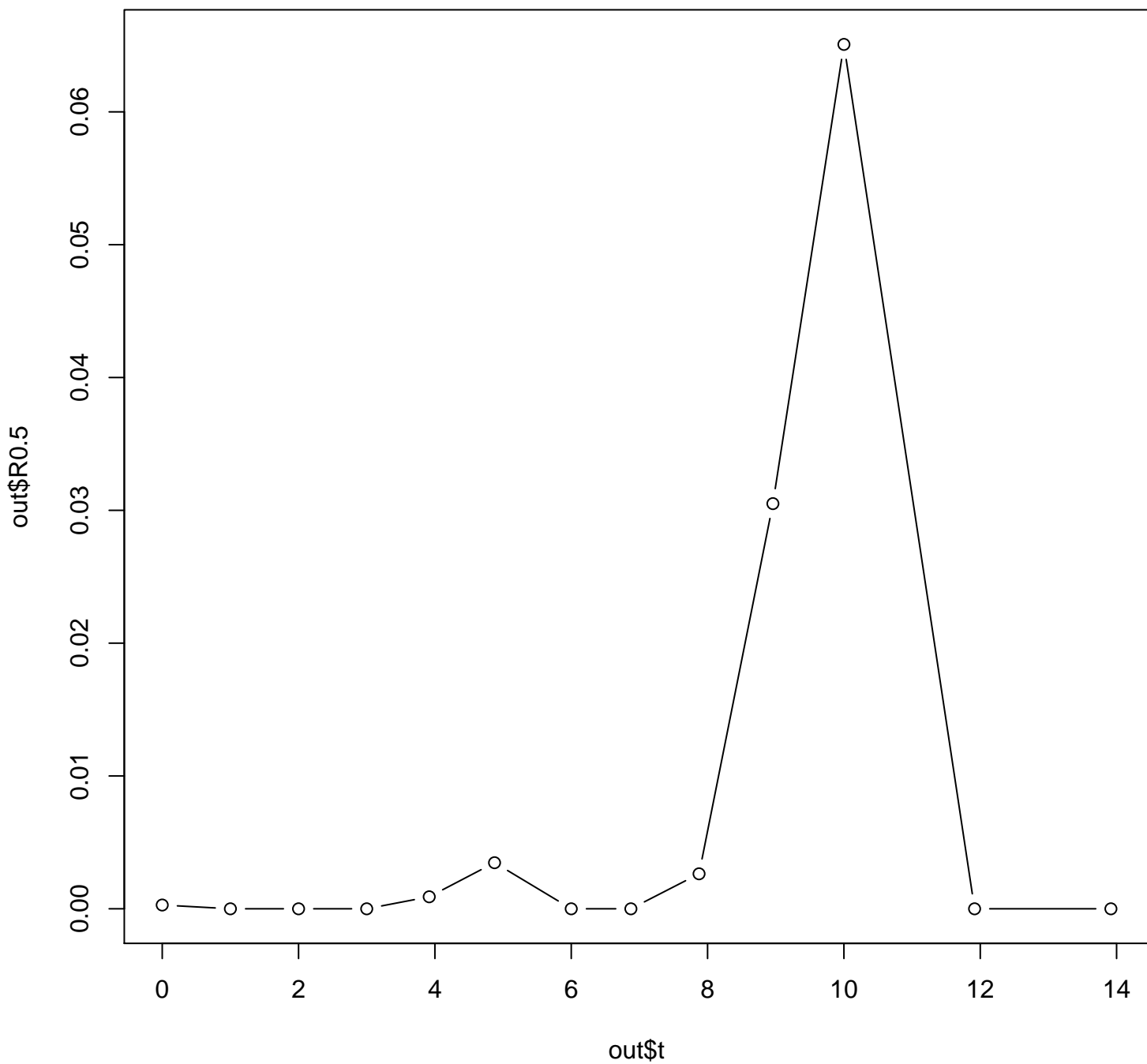


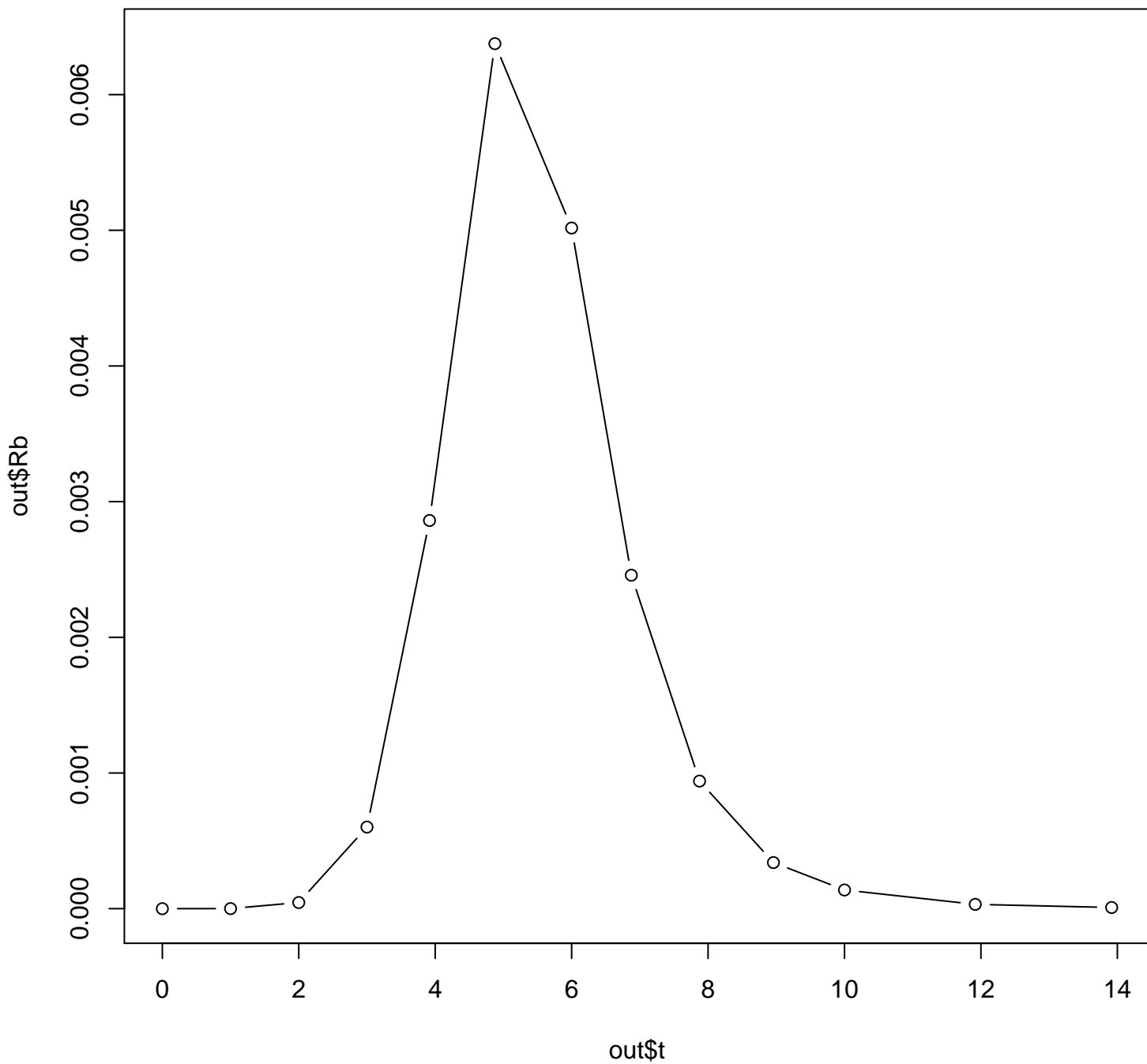
101S



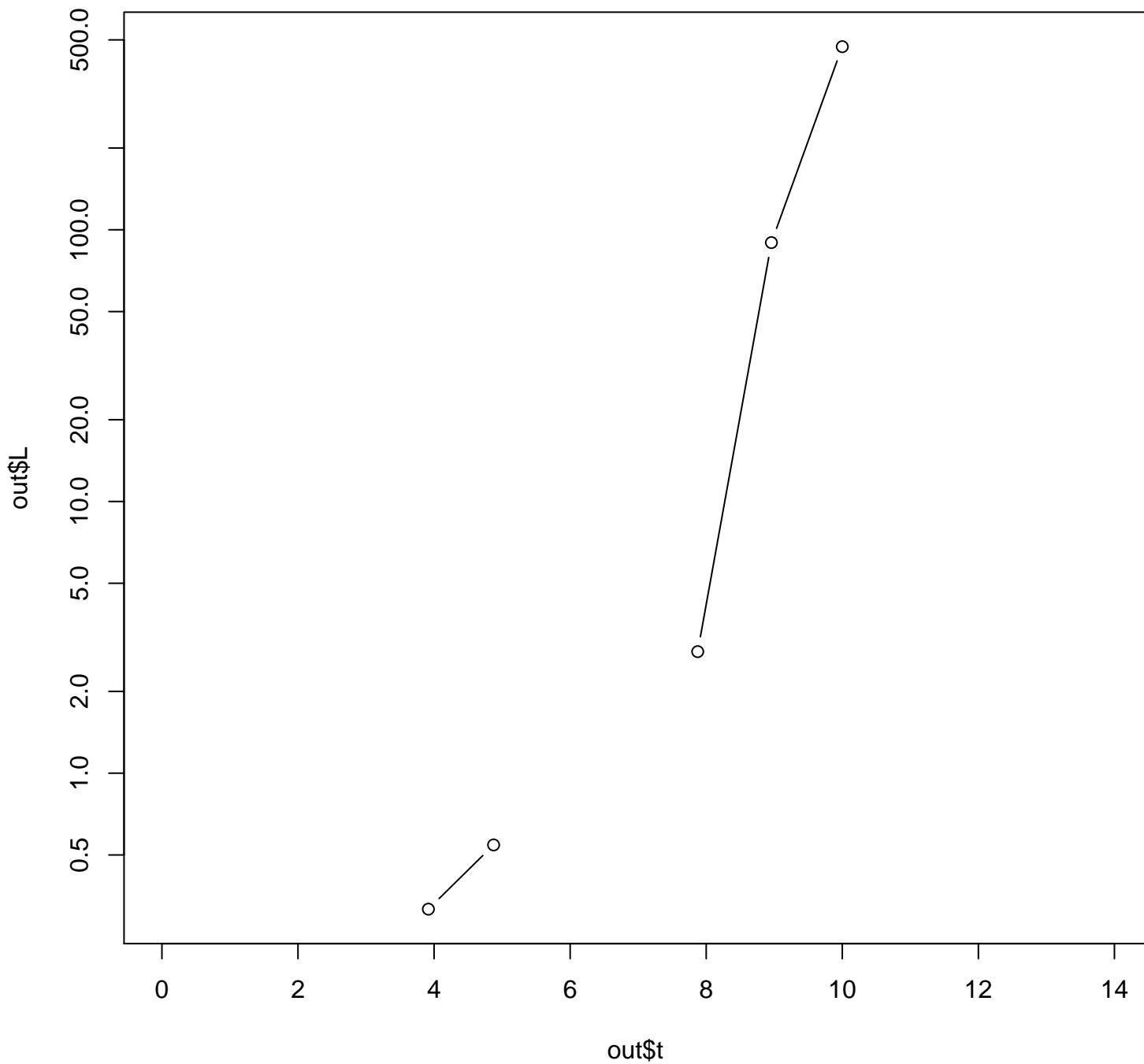
101S 050806



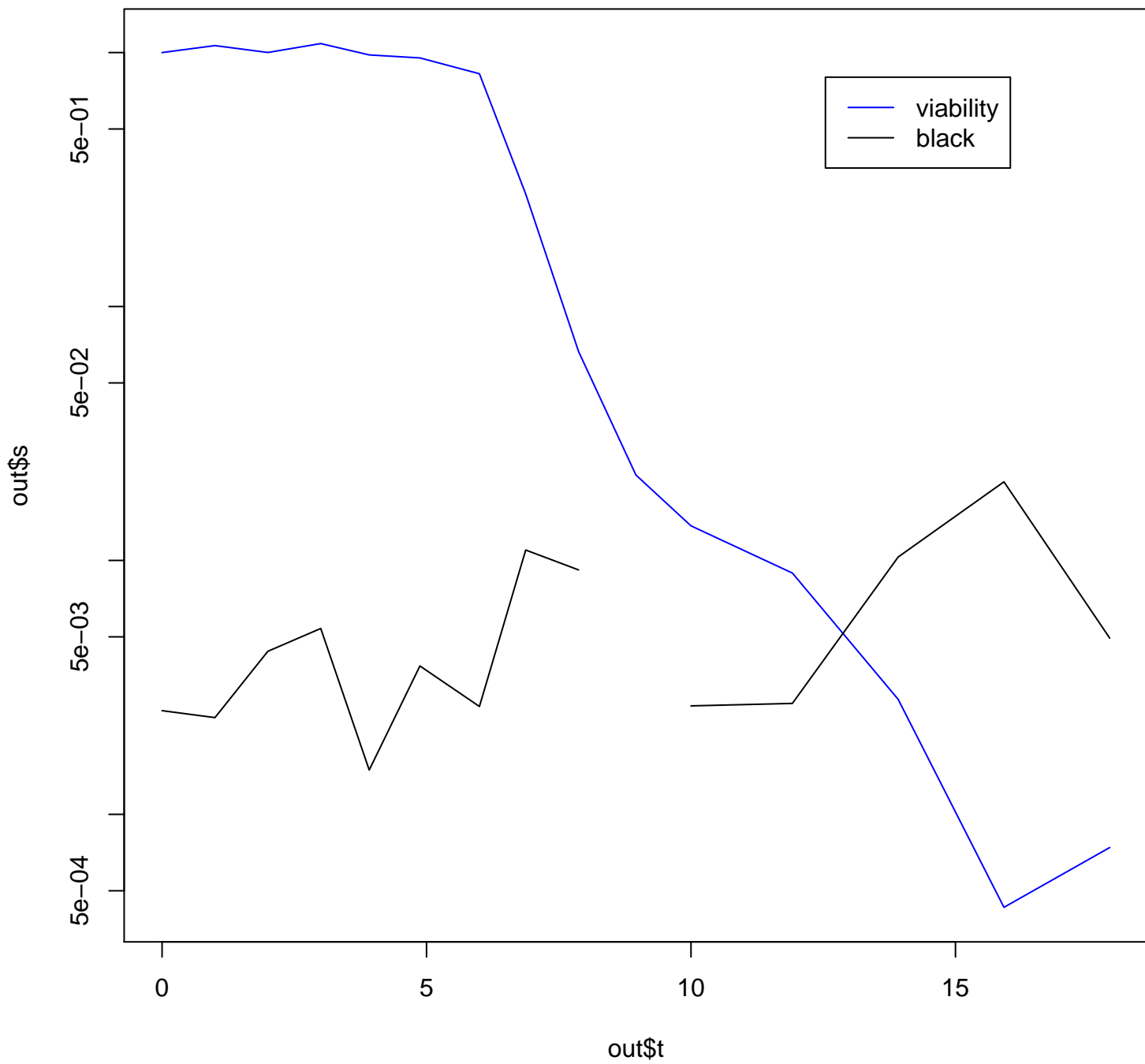
101S 050806



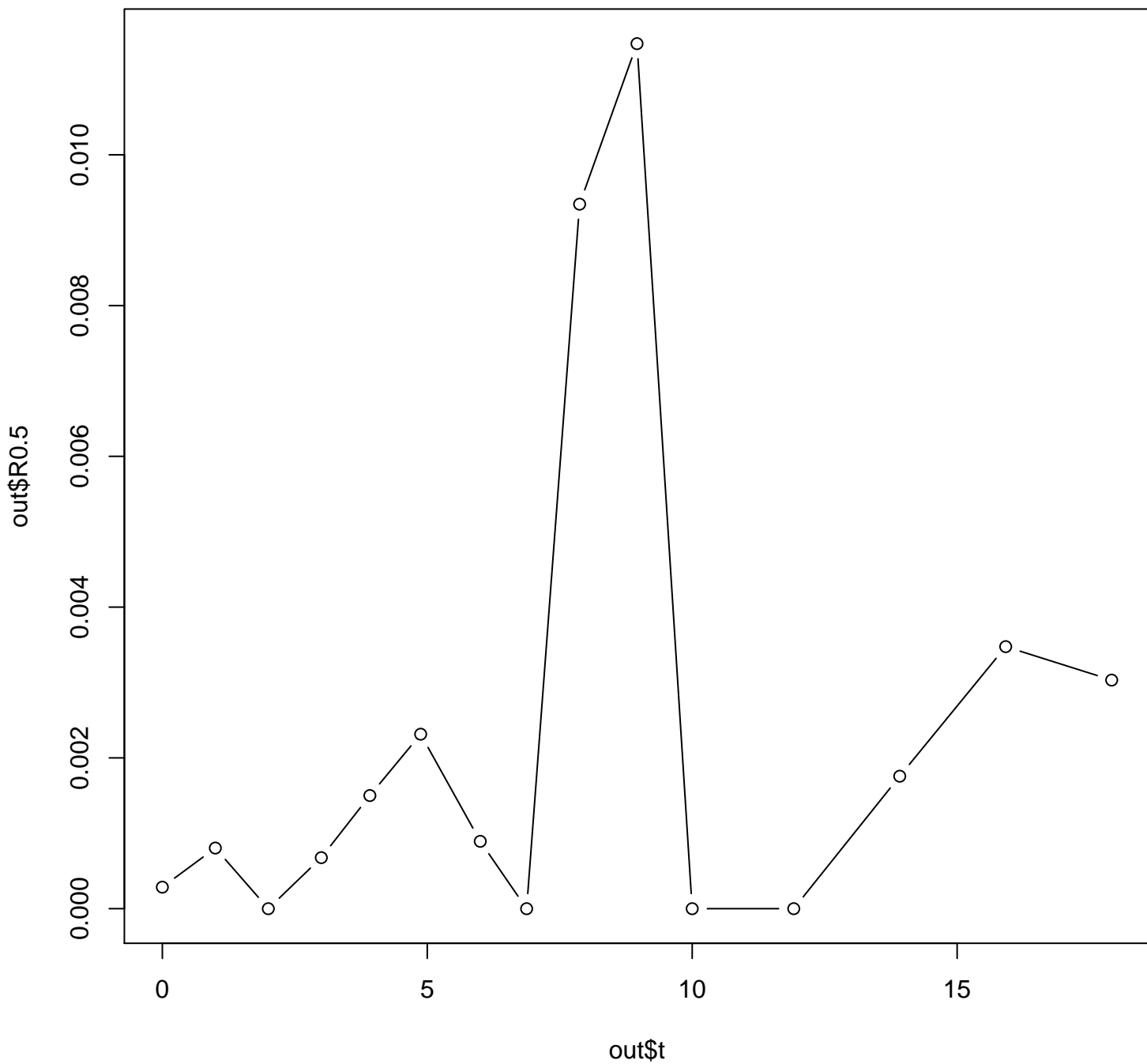
101S 050806



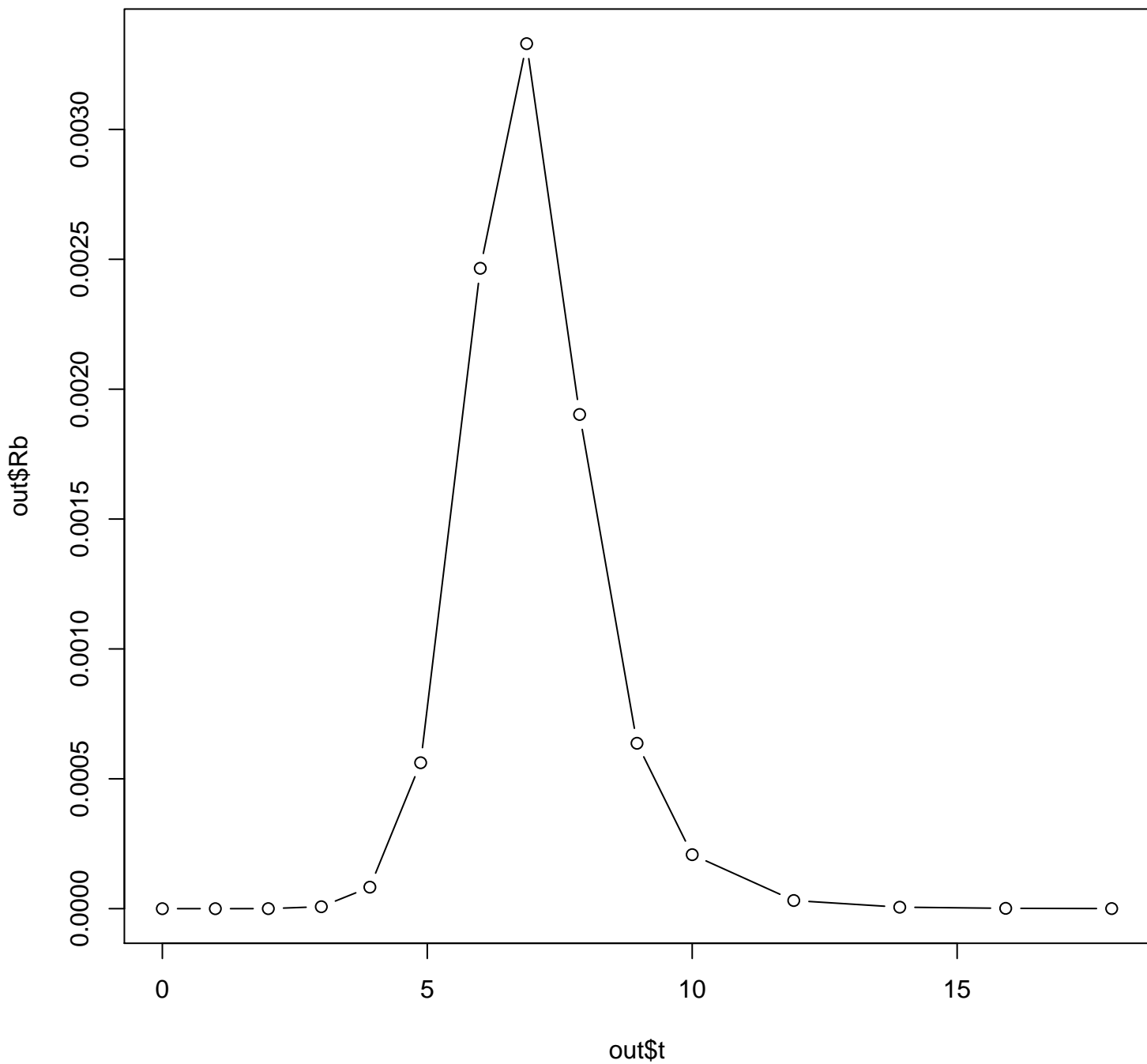
M1-2



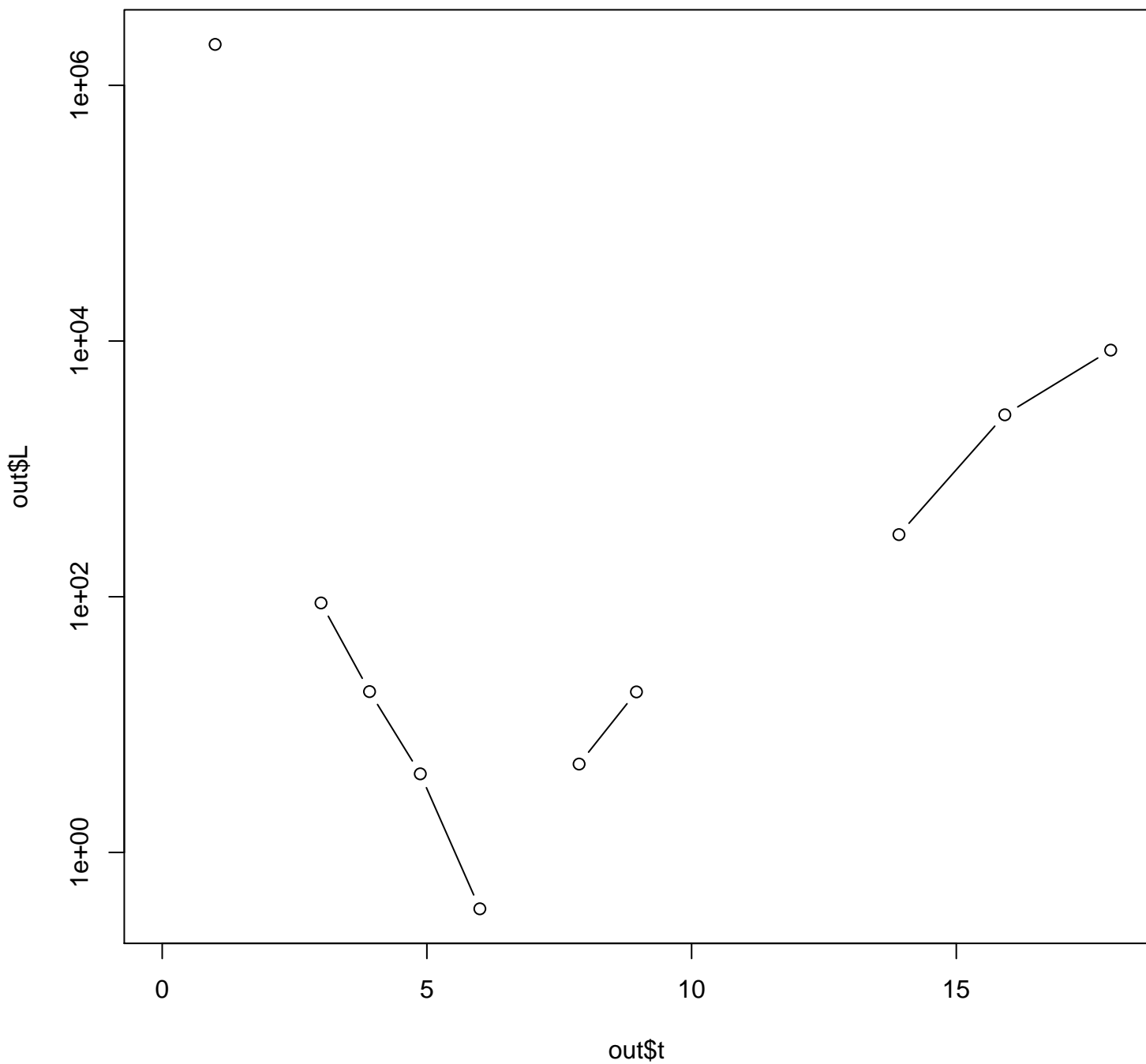
M1-2 050806



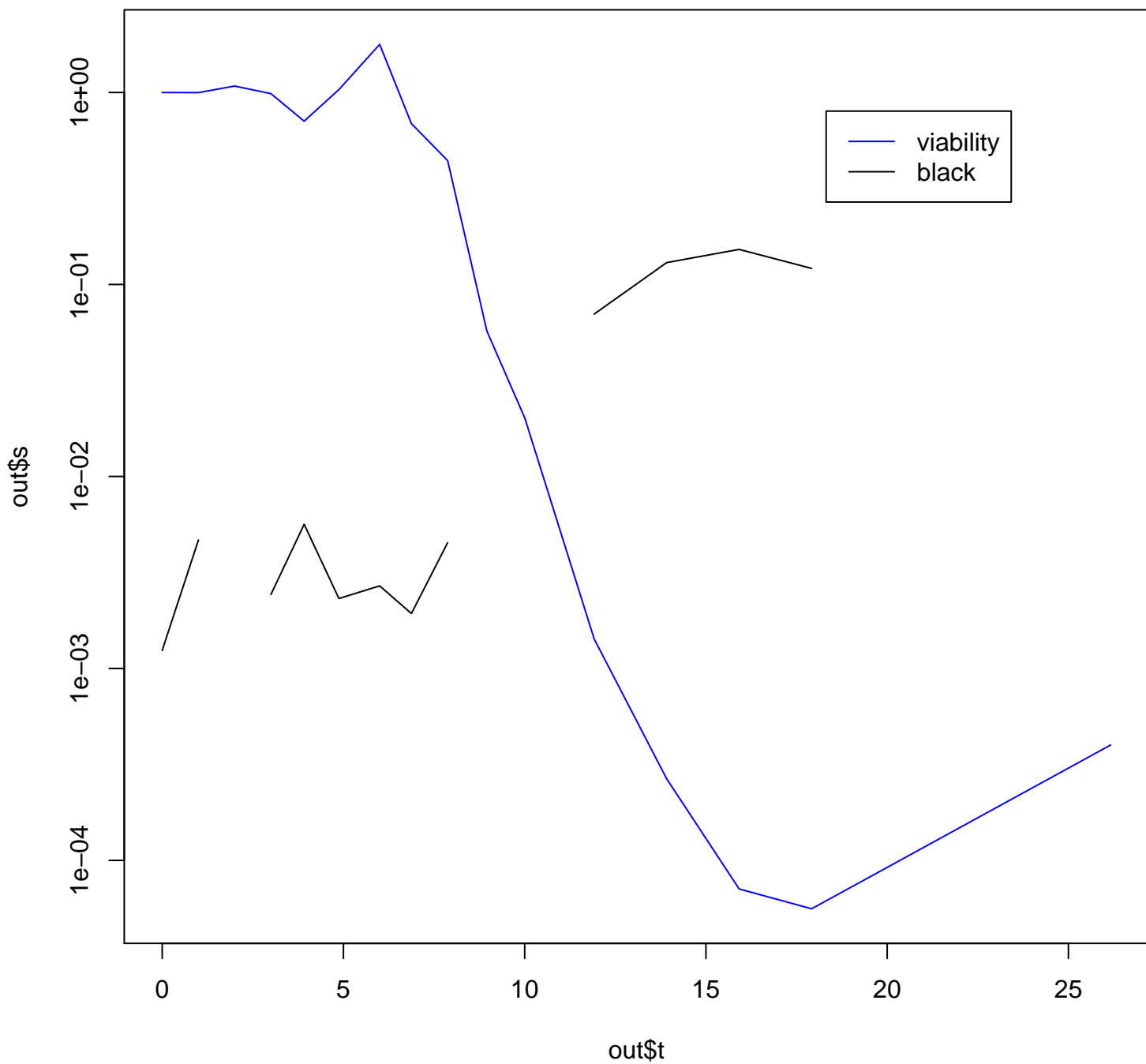
M1-2 050806



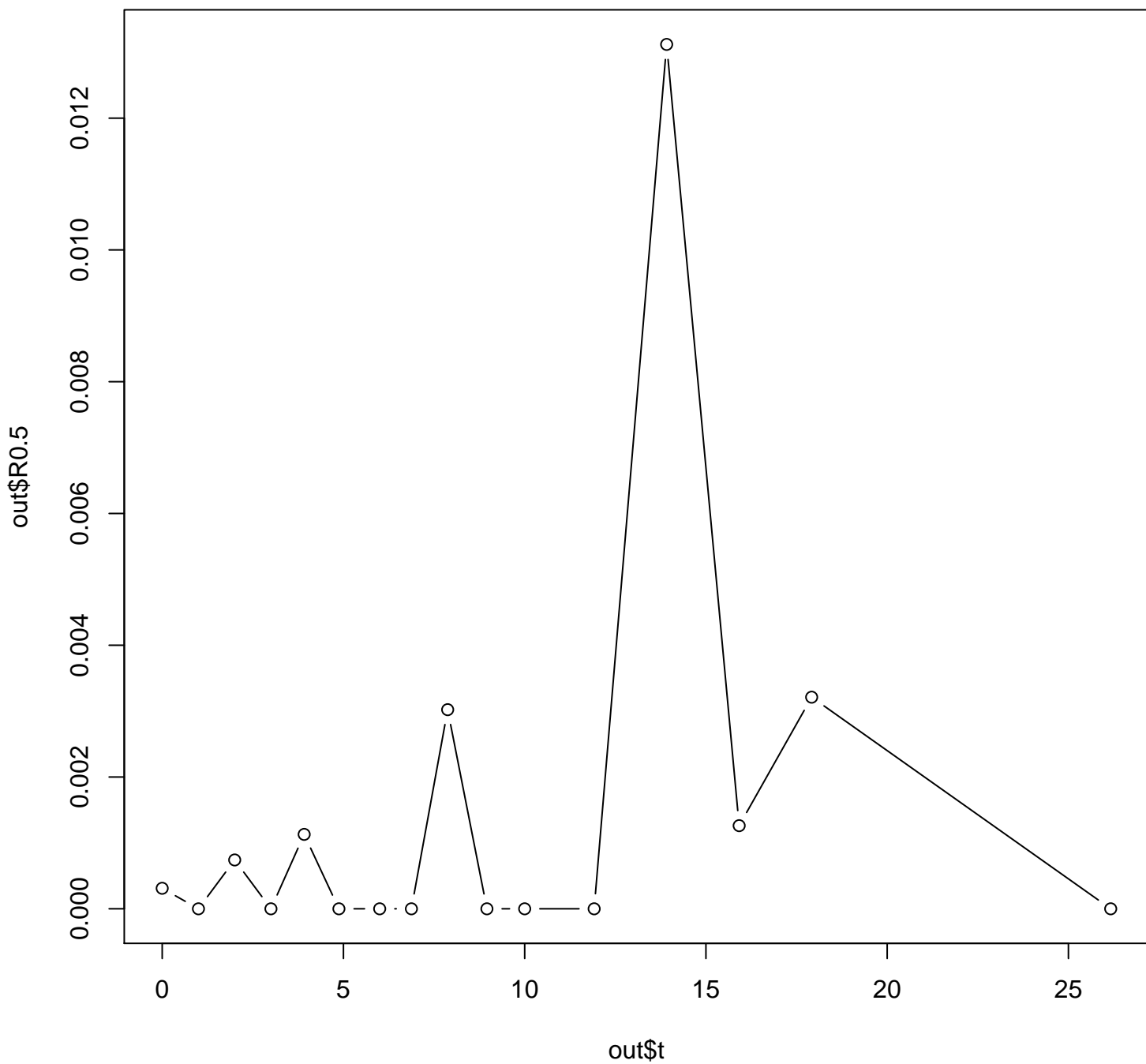
M1-2 050806



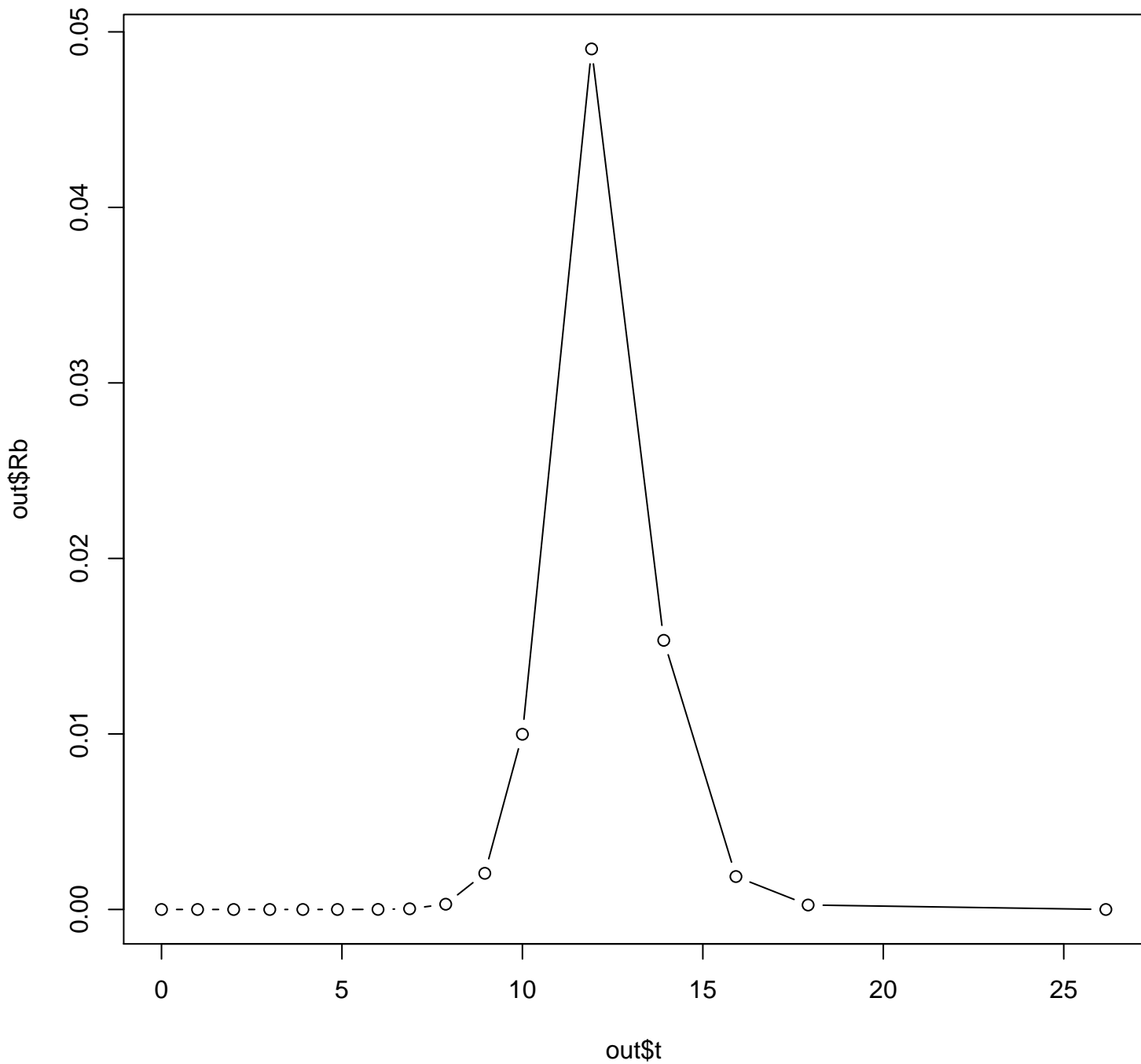
M13



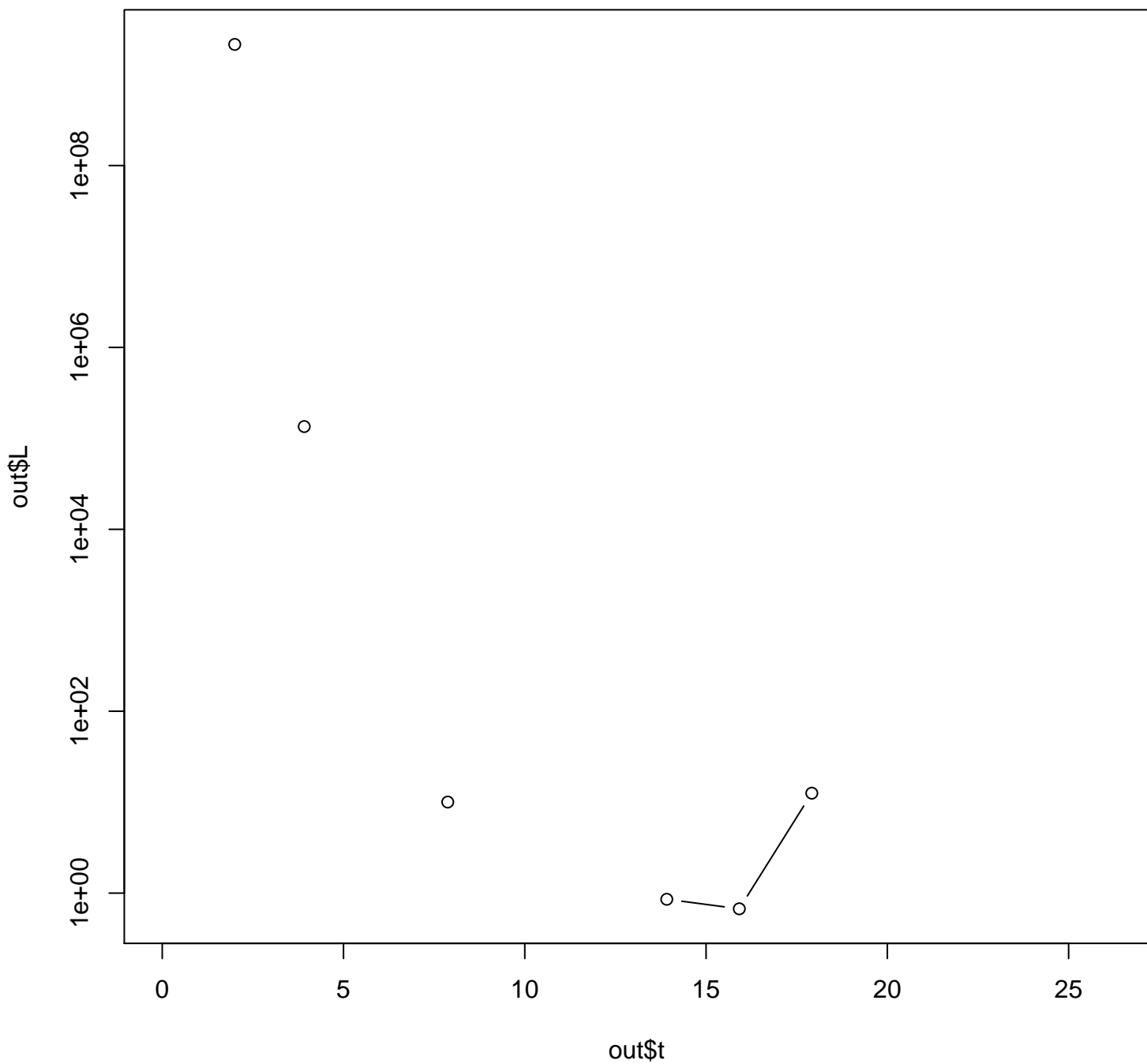
M13 050806



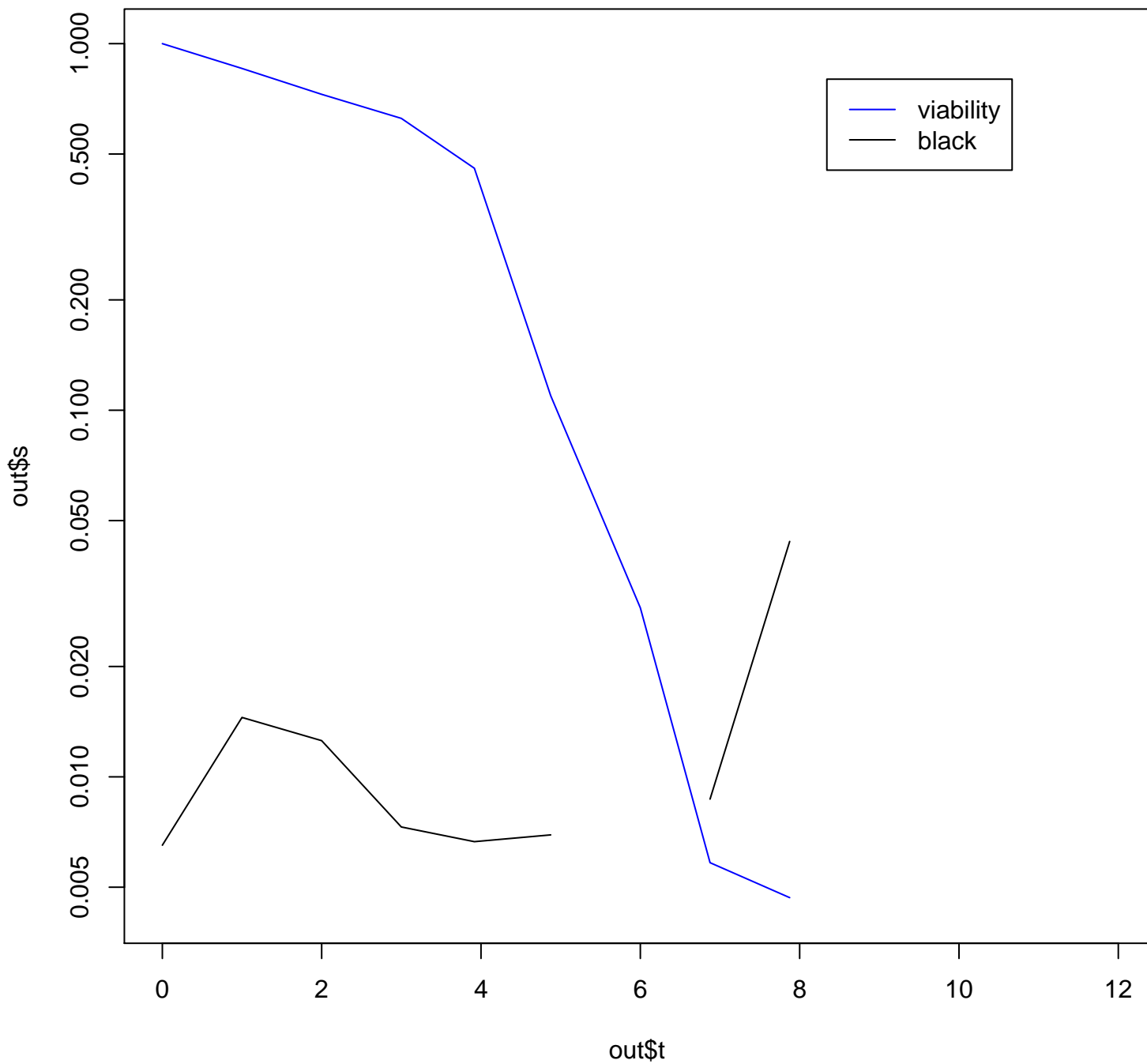
M13 050806



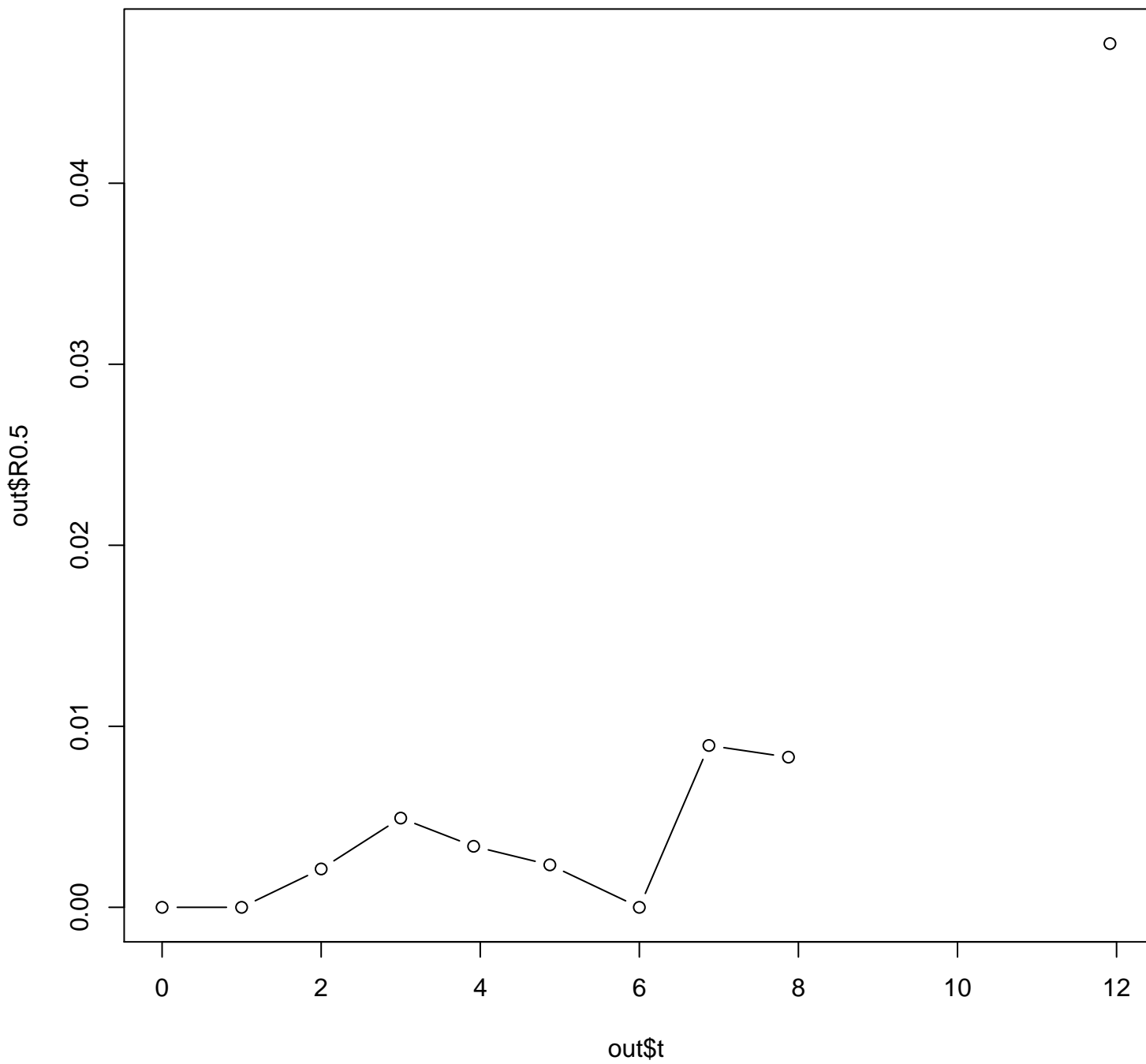
M13 050806



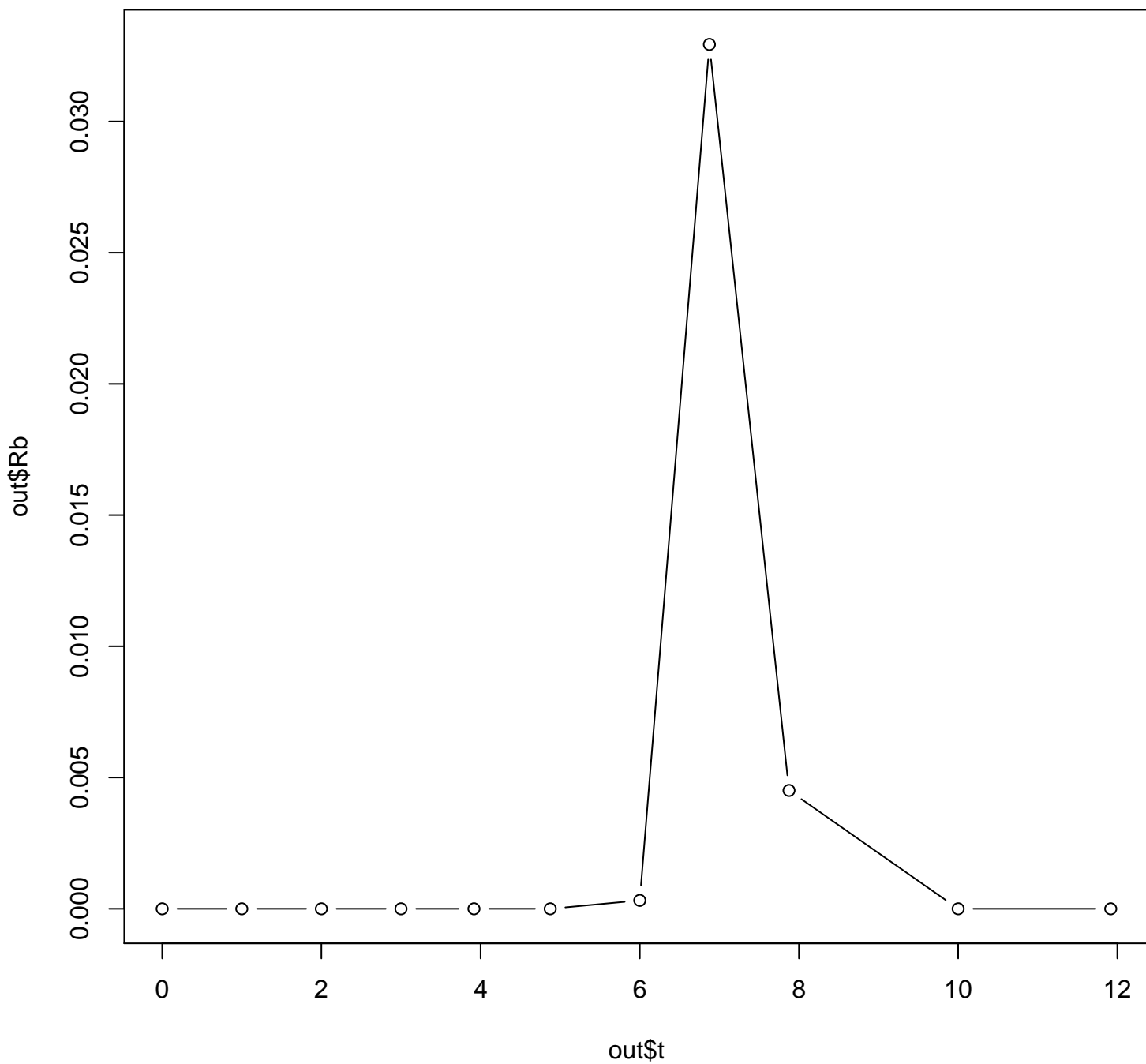
M14



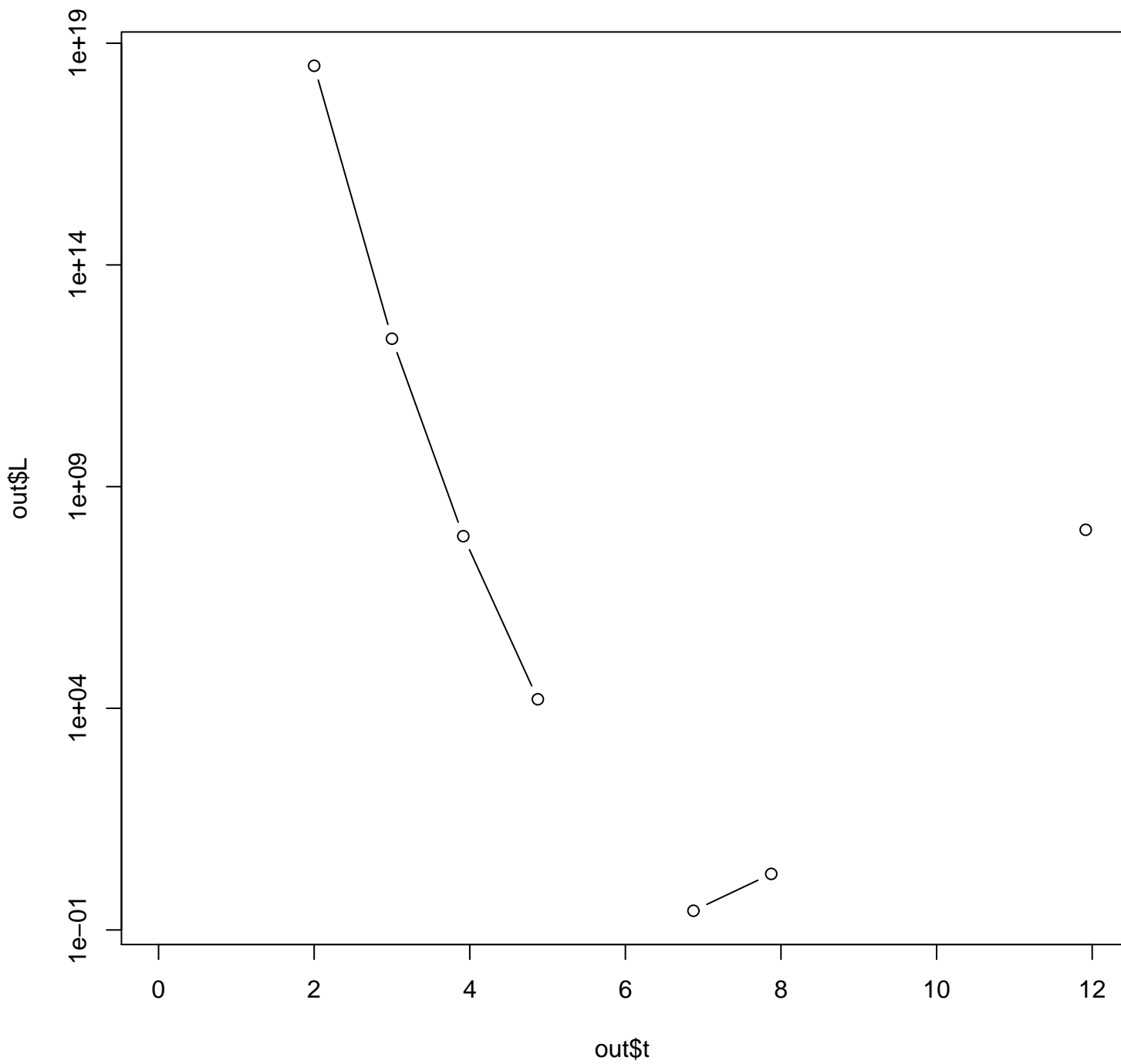
M14 050806



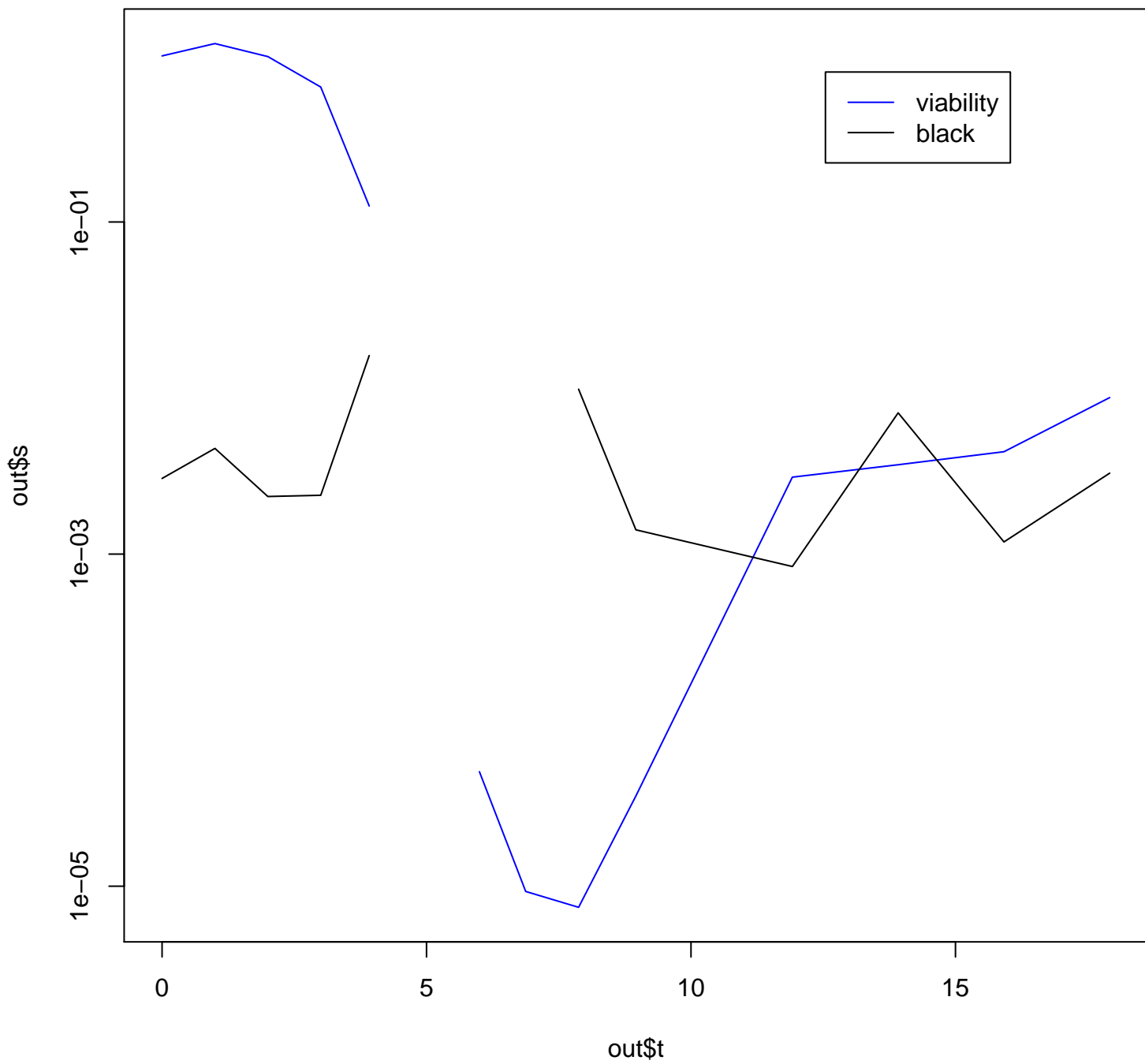
M14 050806



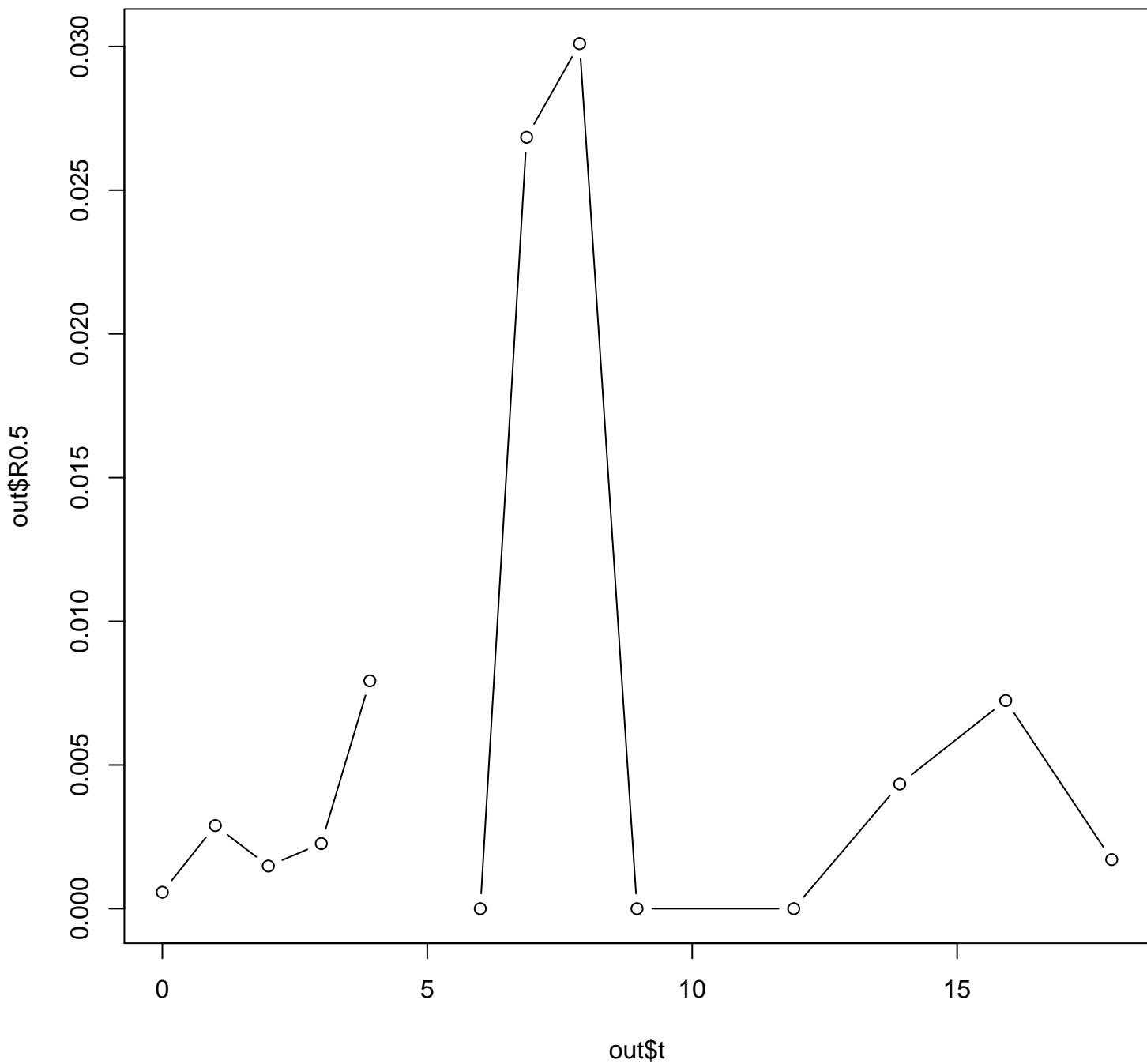
M14 050806



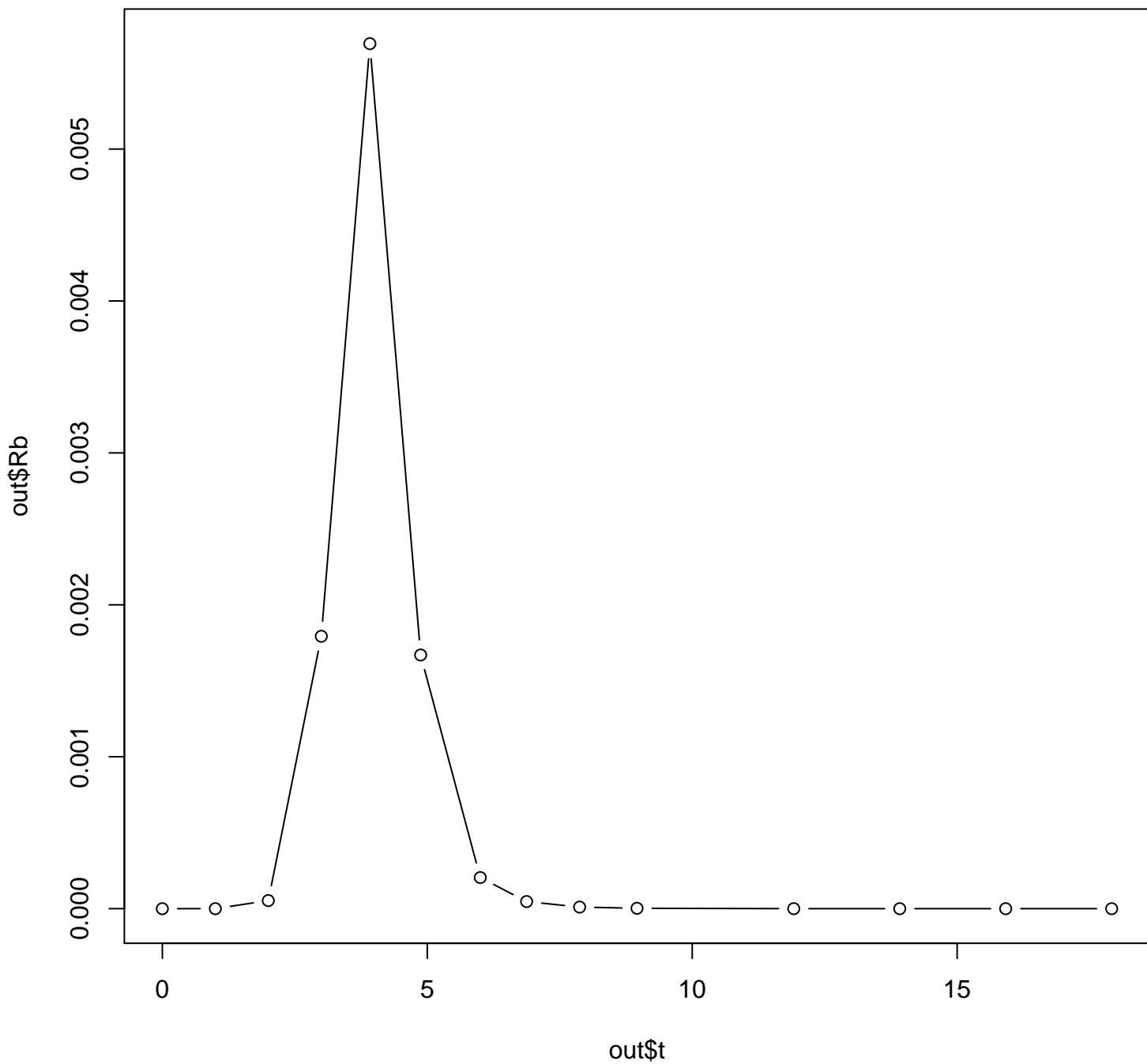
M2-8



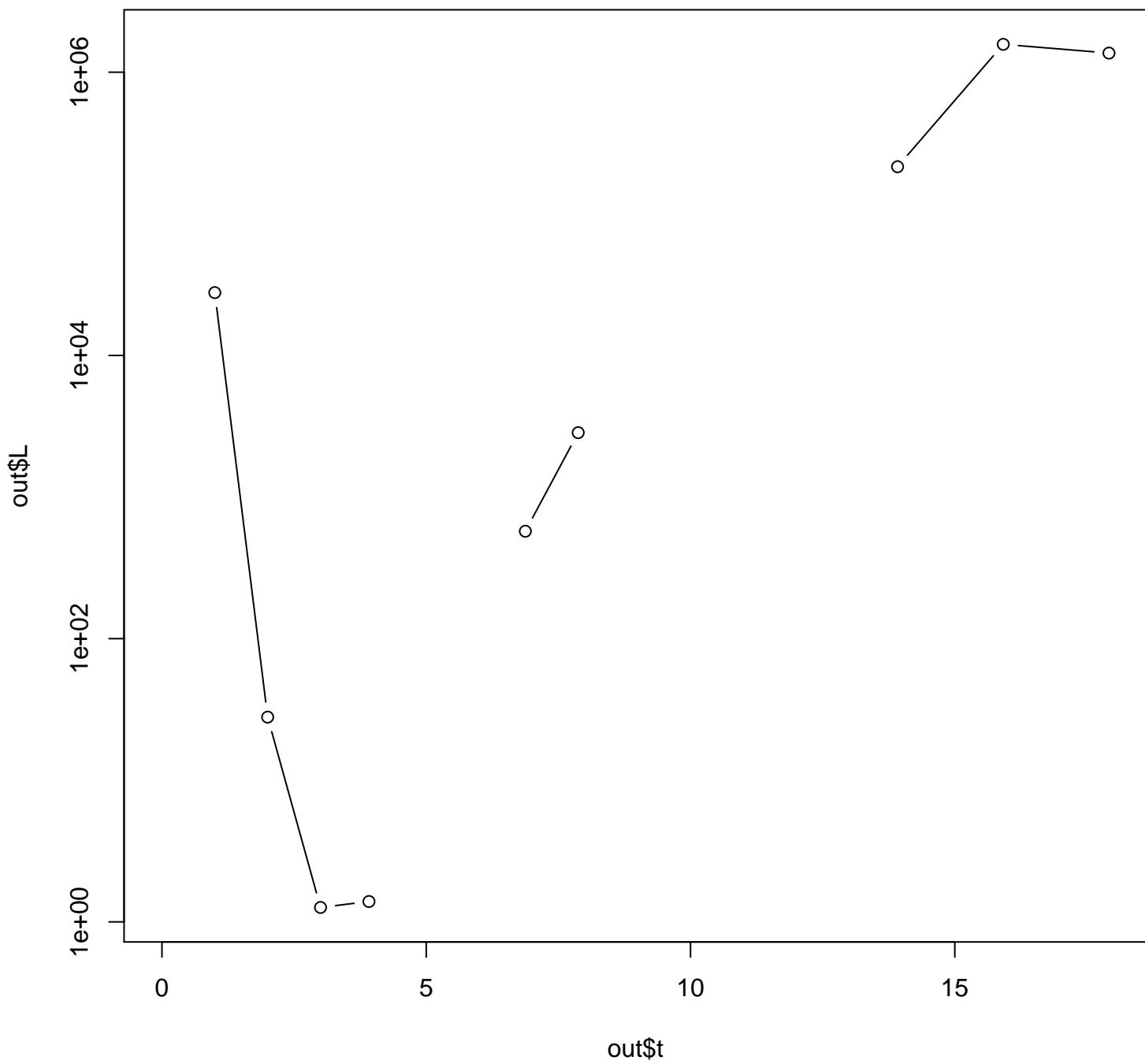
M2-8 050806



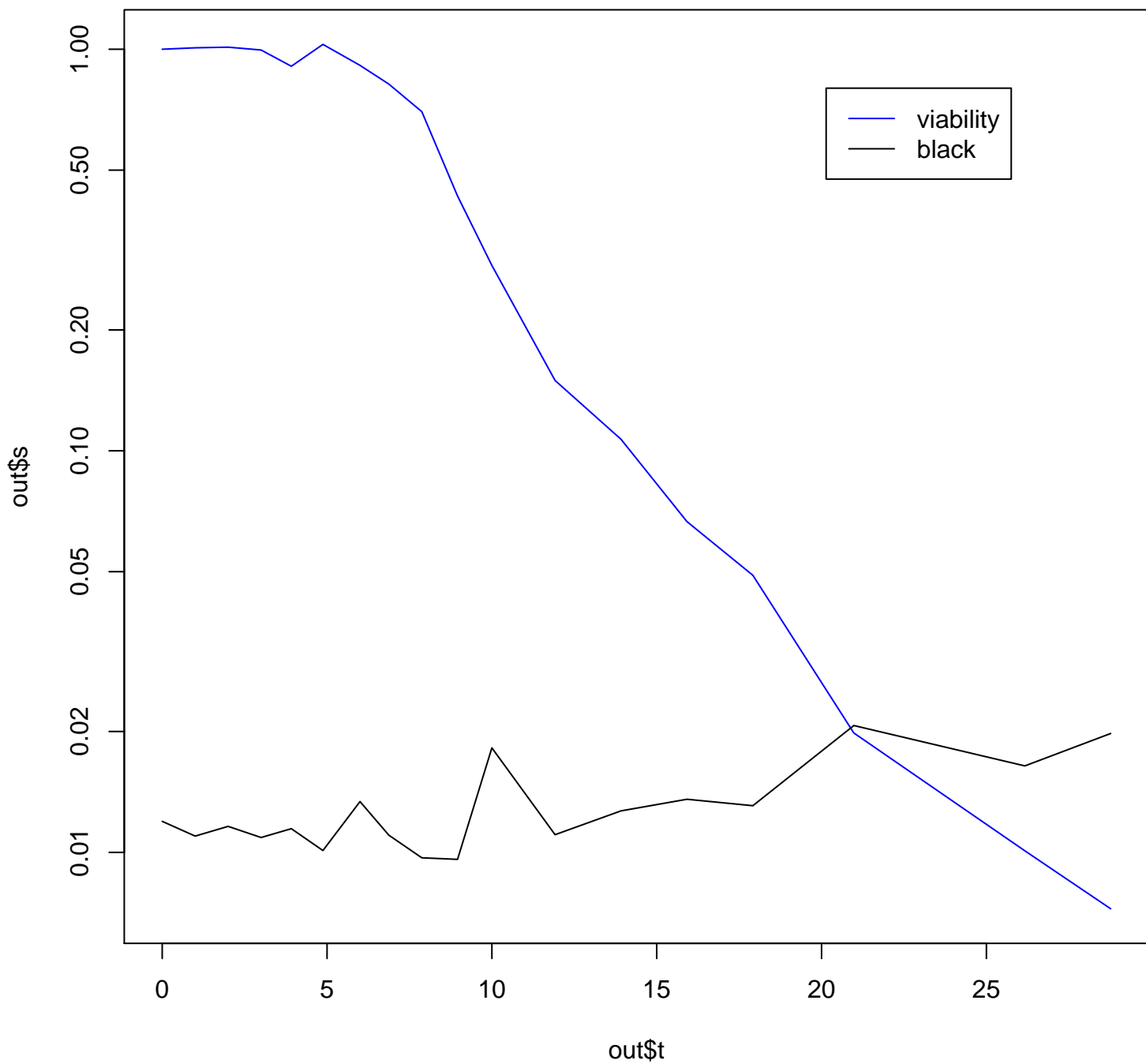
M2-8 050806



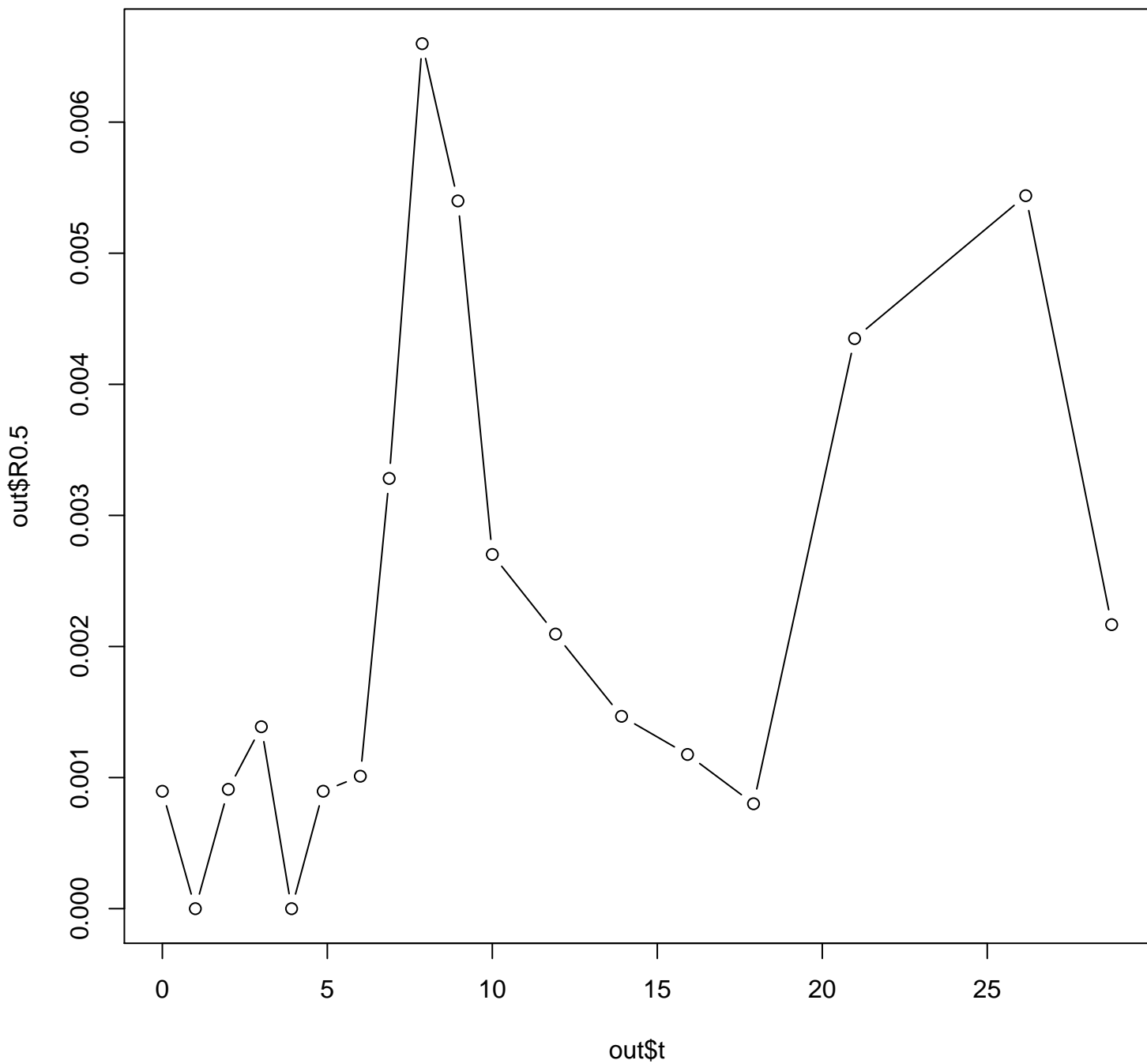
M2-8 050806



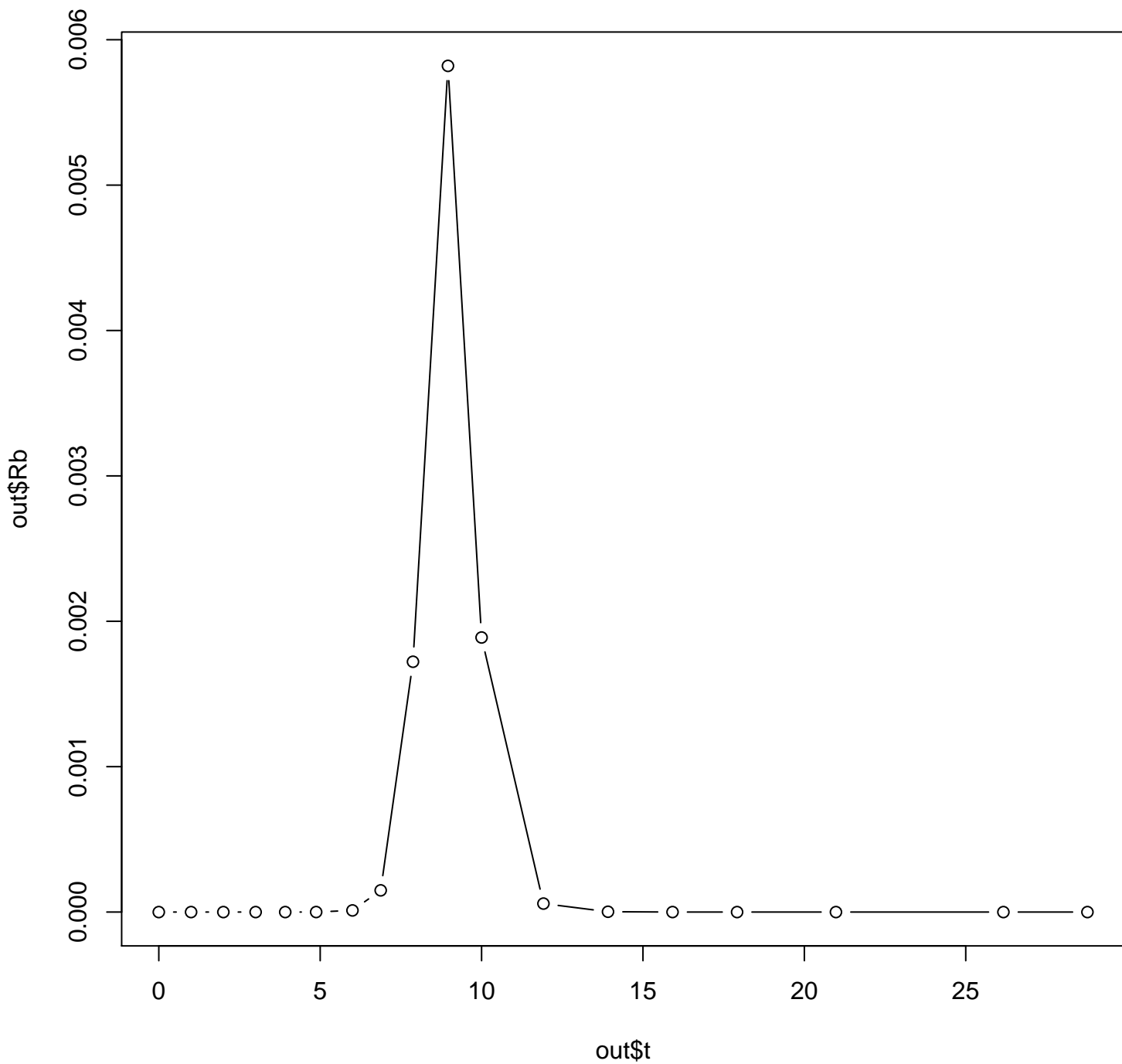
M32



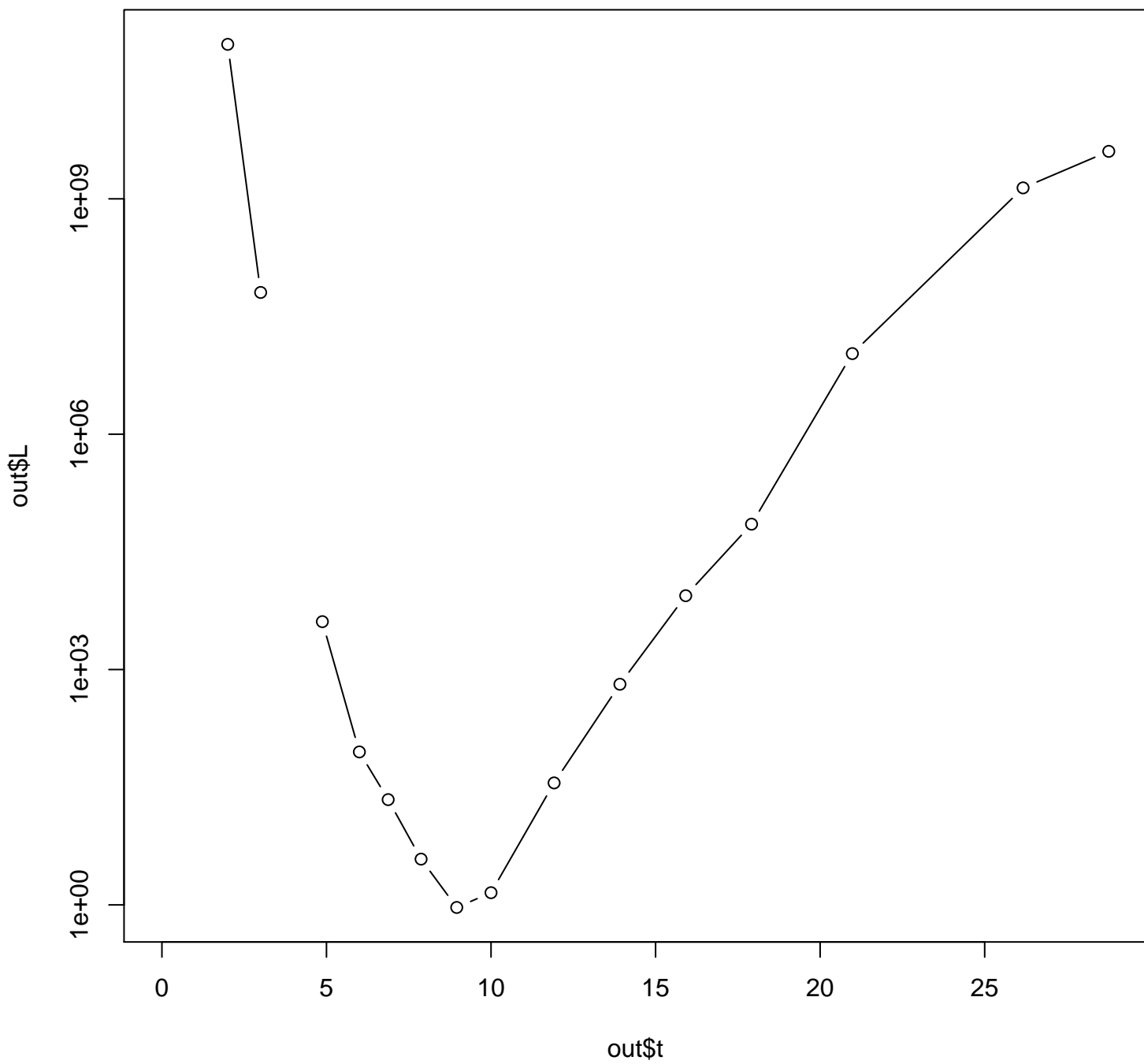
M32 050806



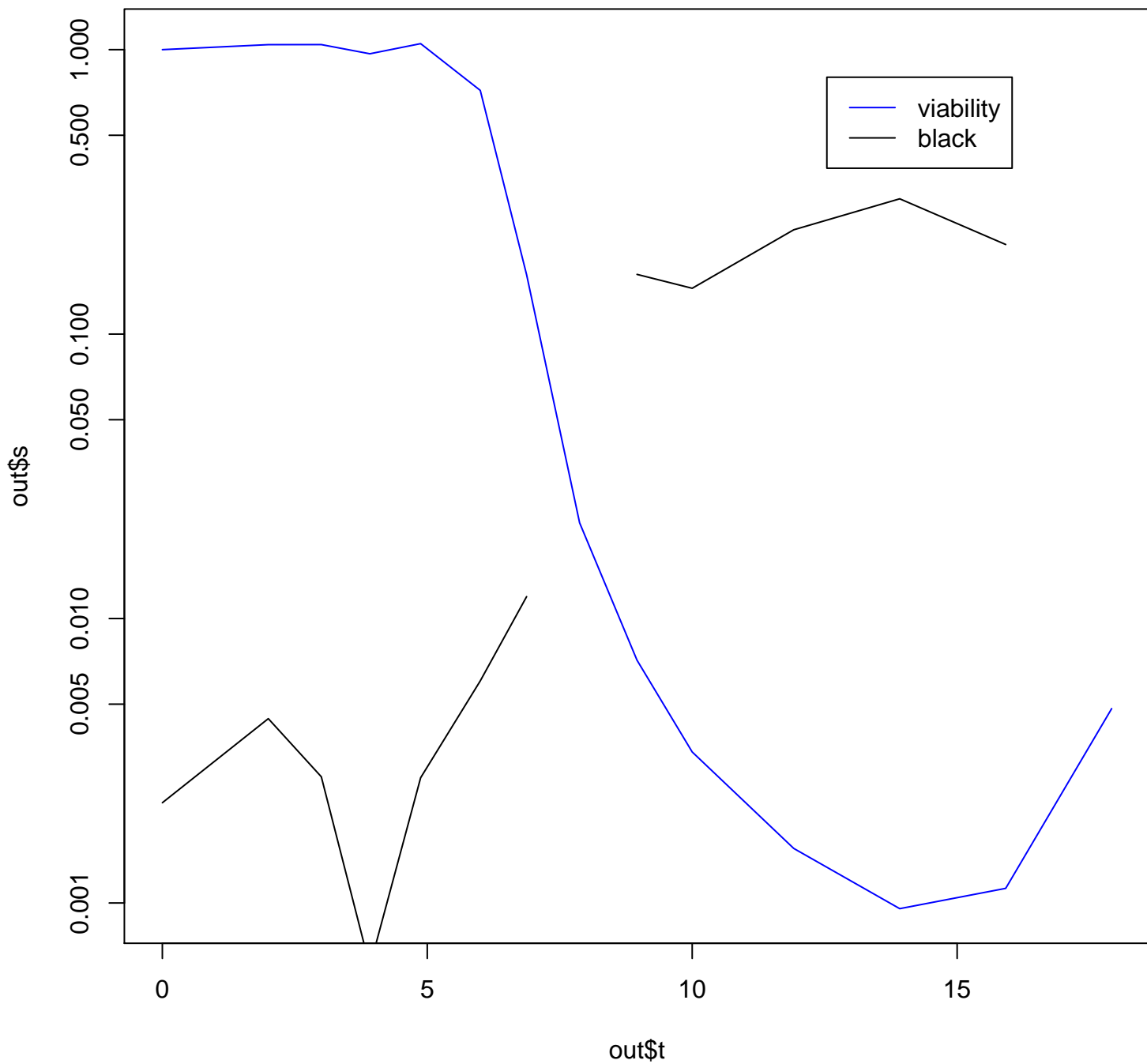
M32 050806



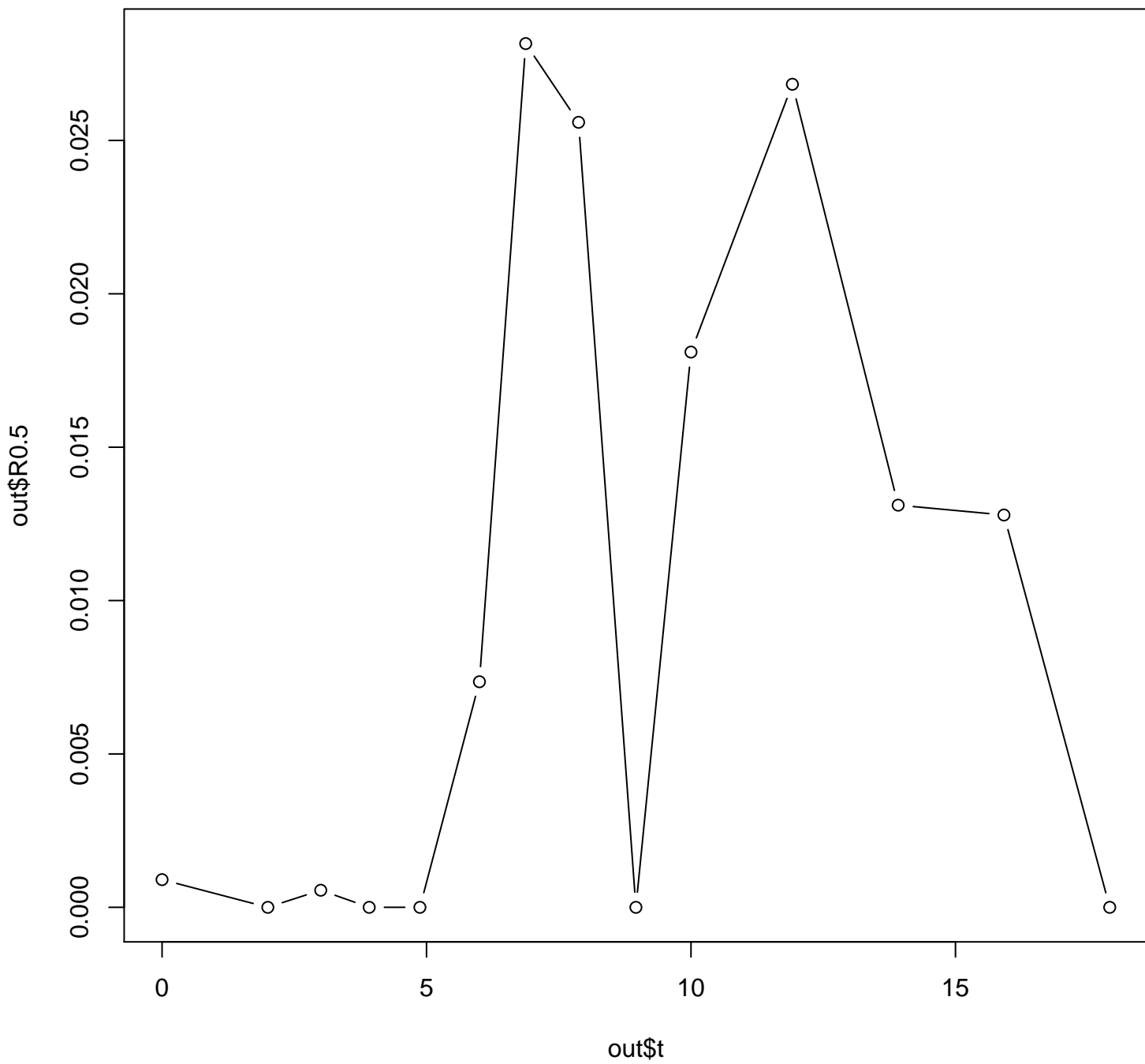
M32 050806



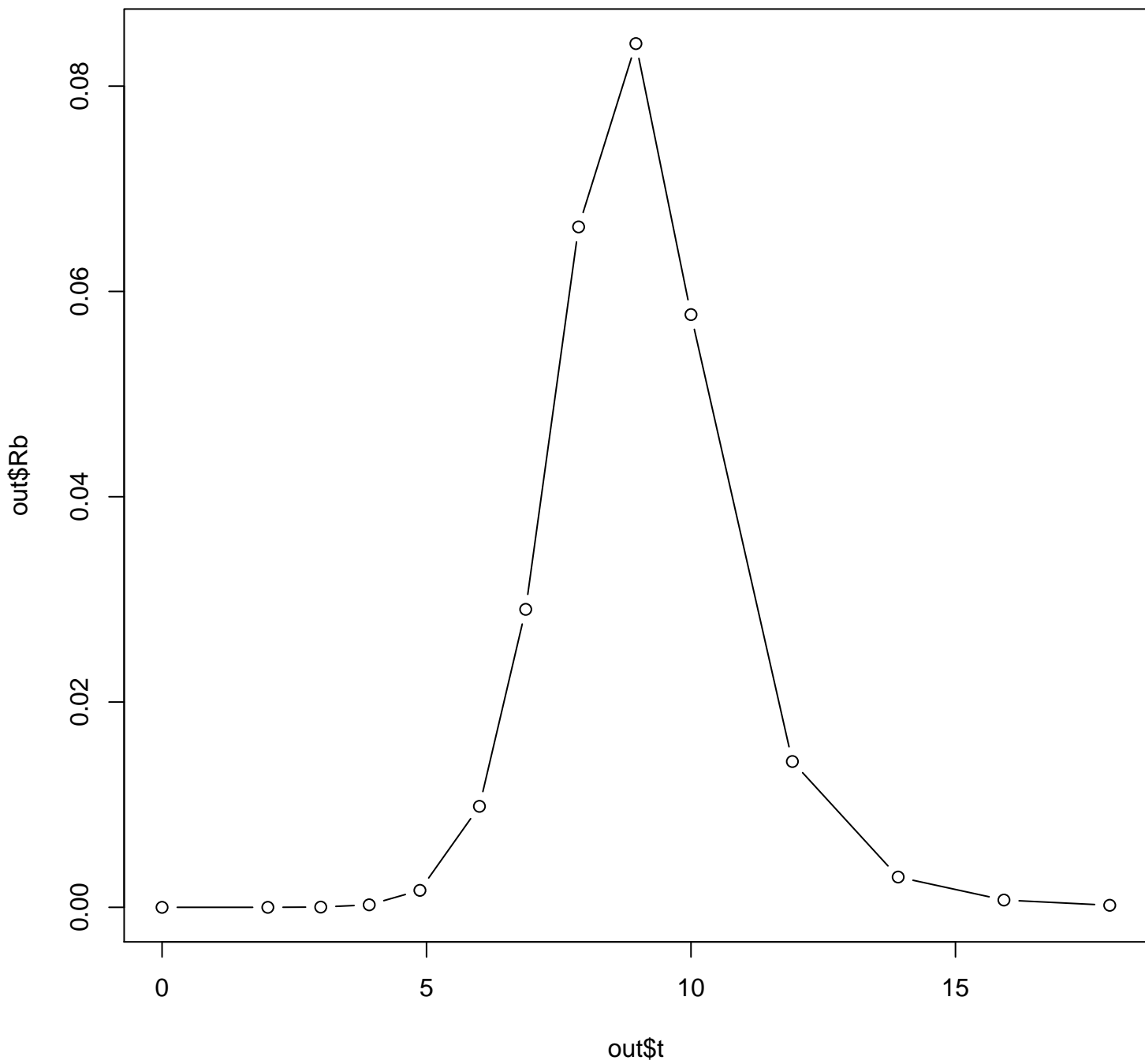
M5



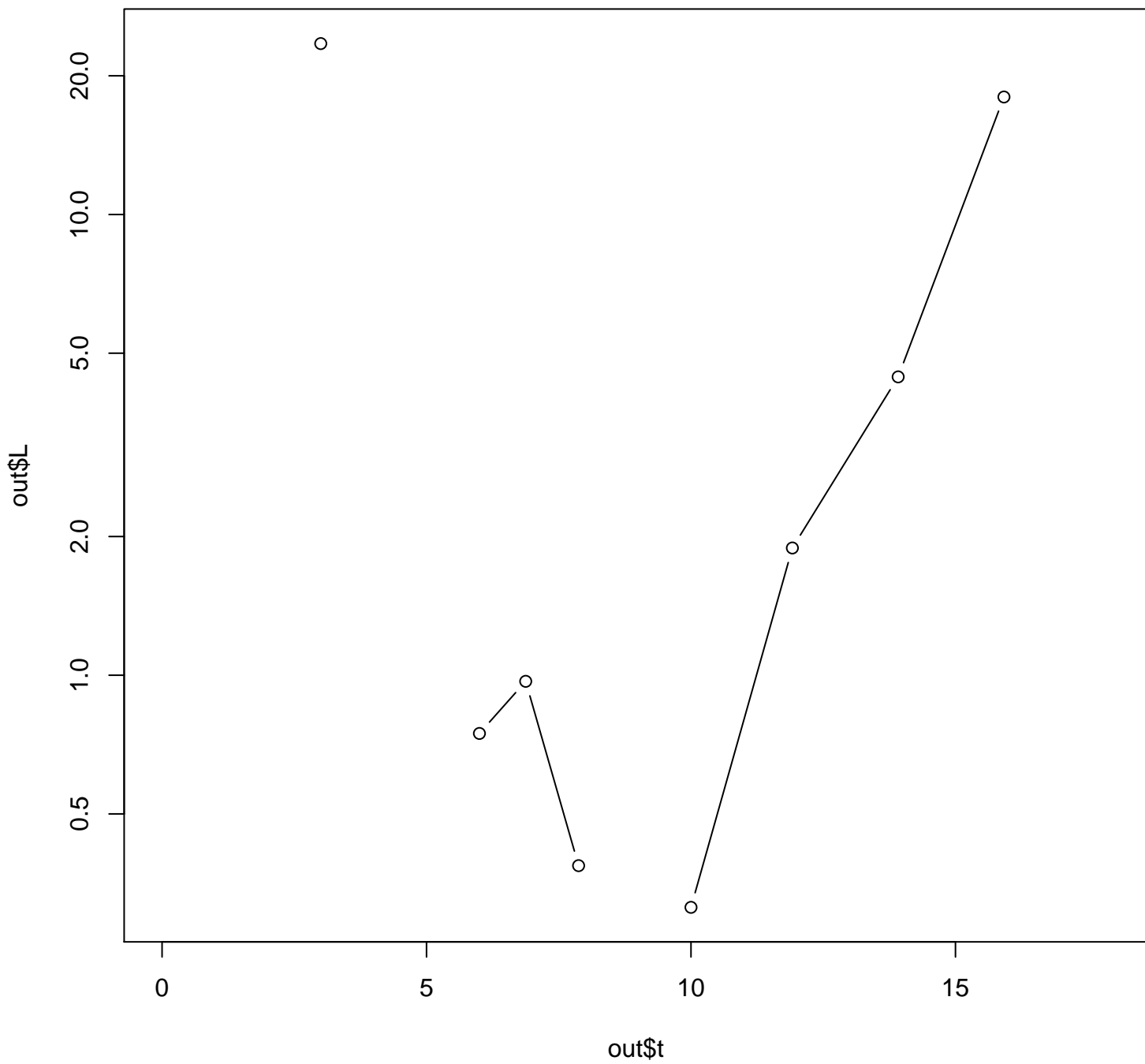
M5 050806



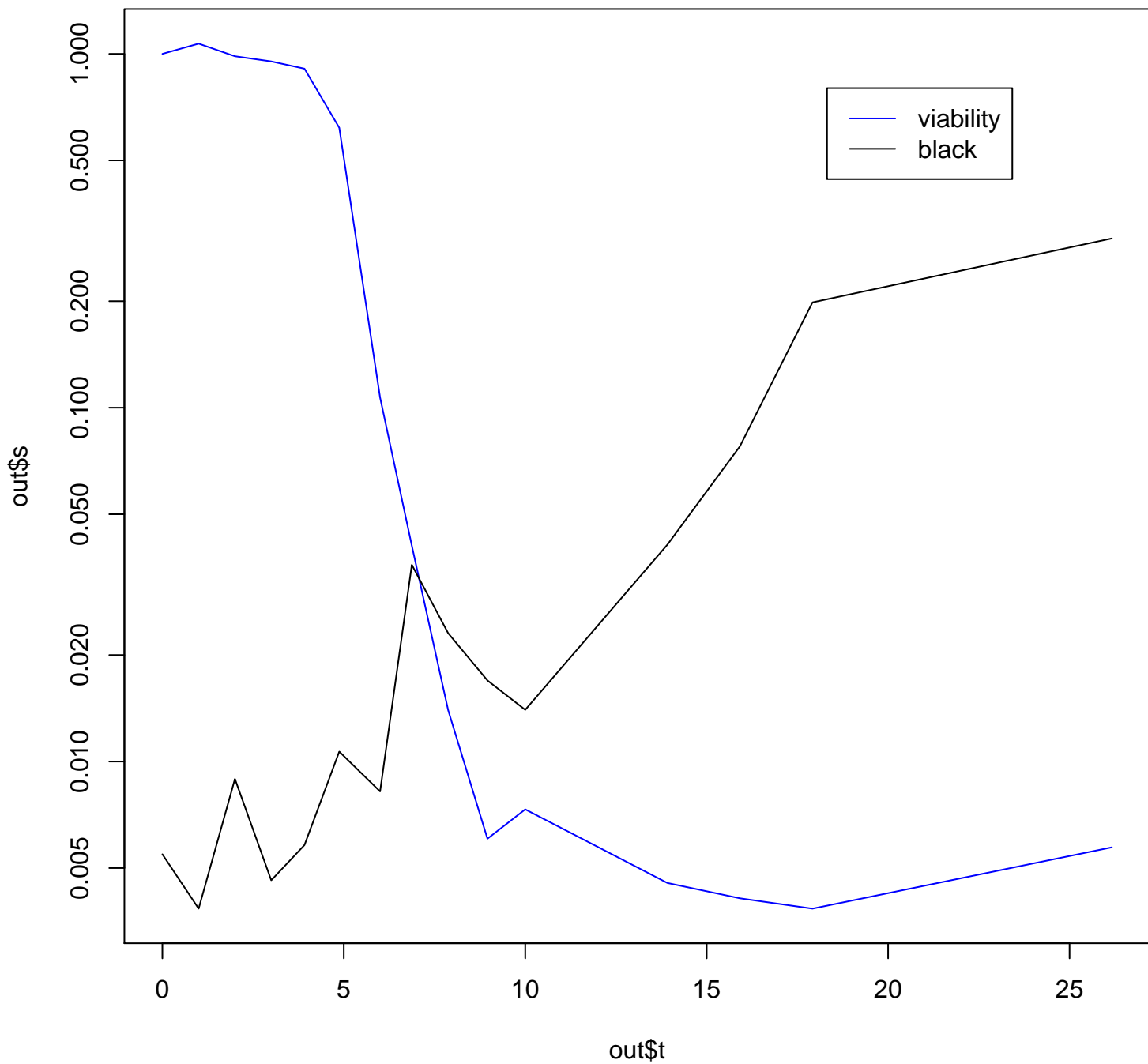
M5 050806



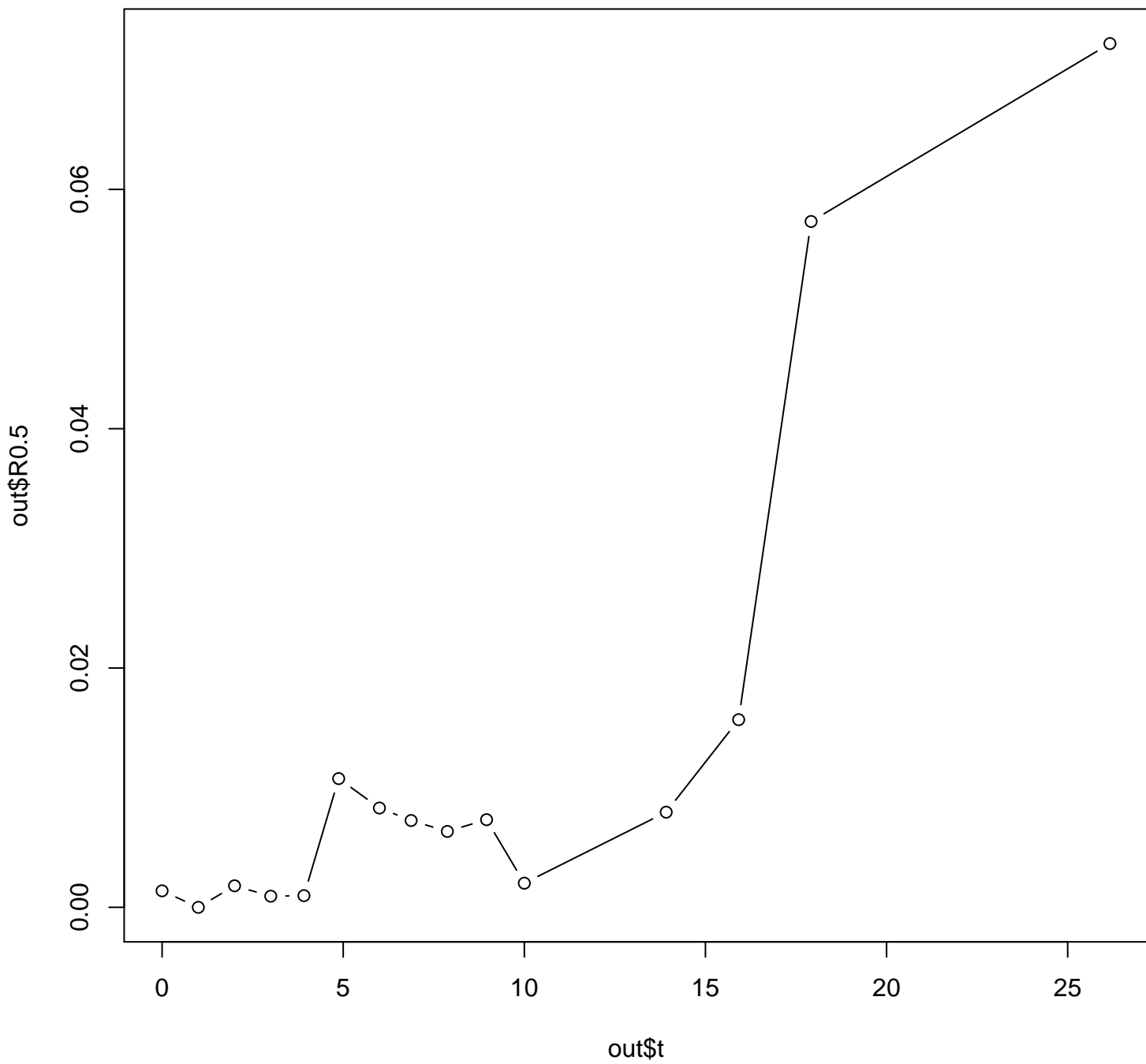
M5 050806



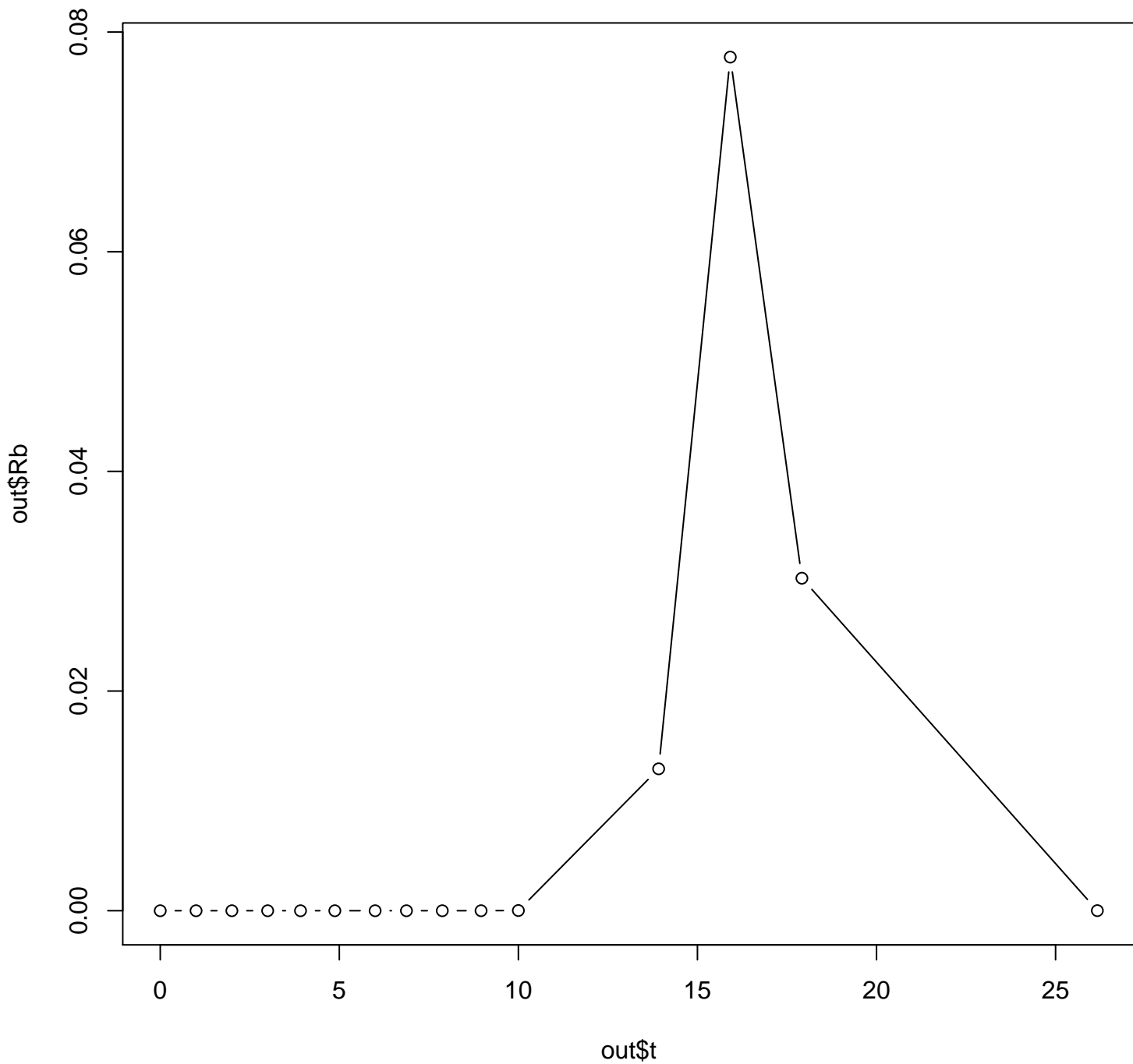
M8



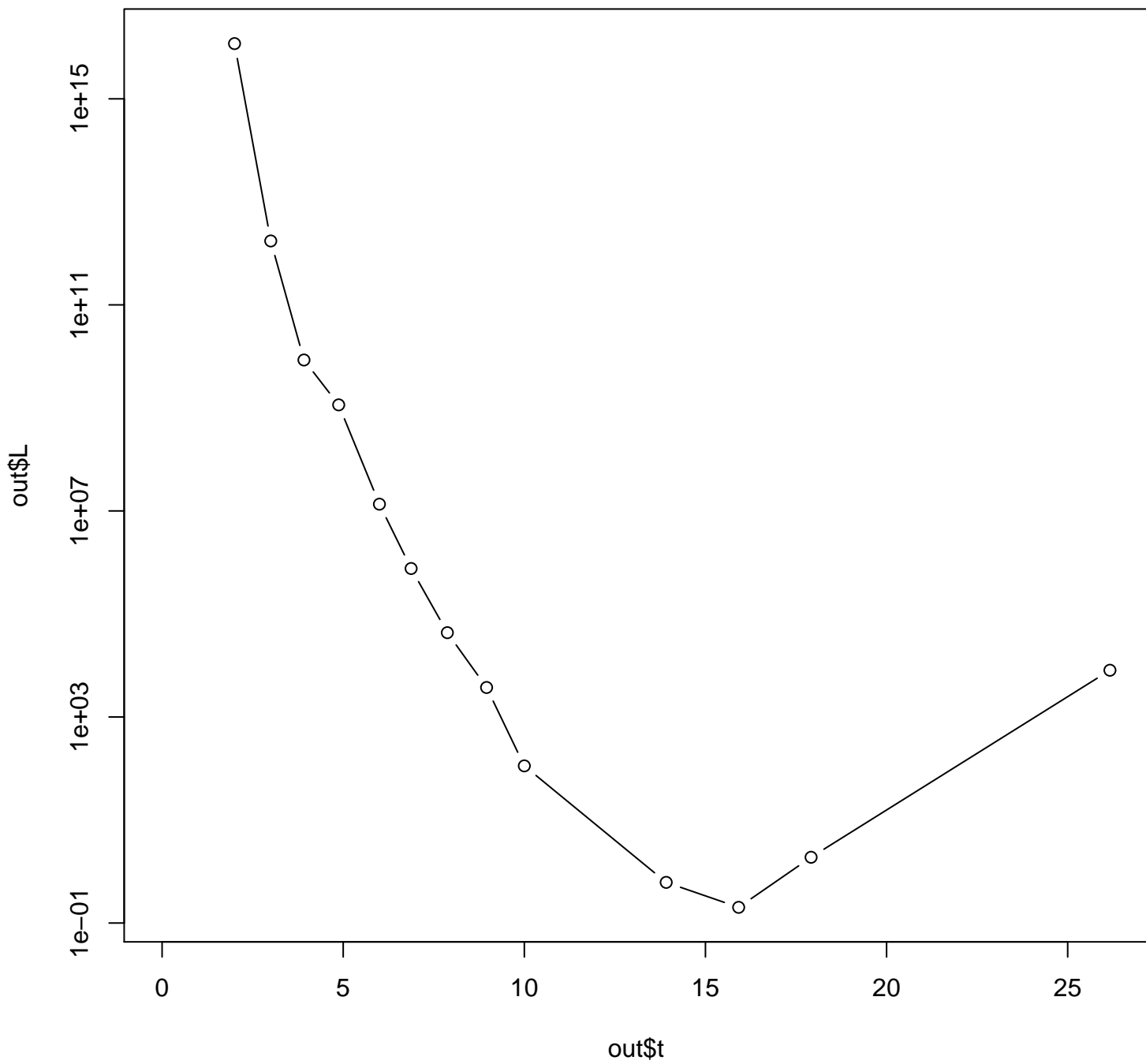
M8 050806



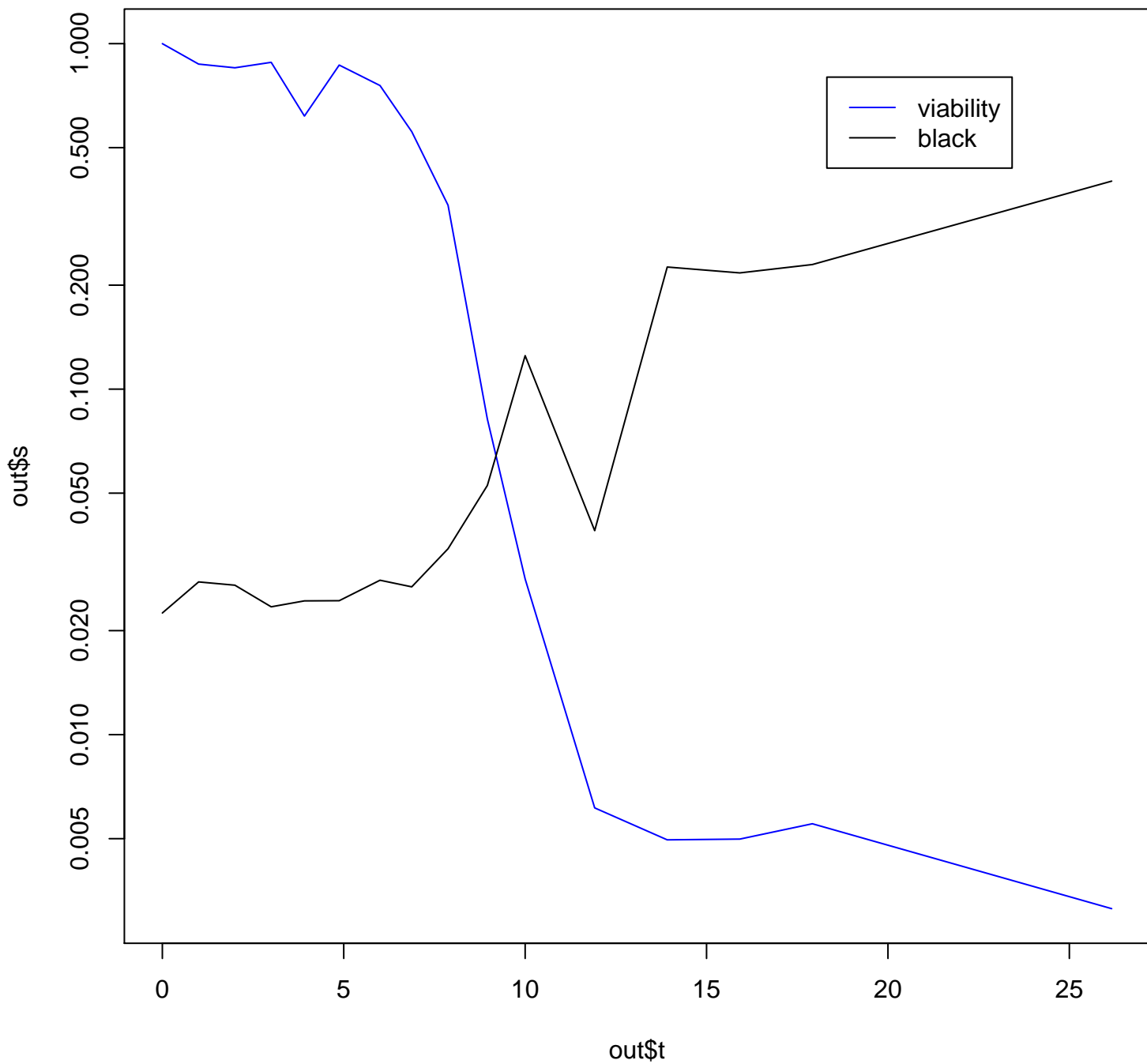
M8 050806



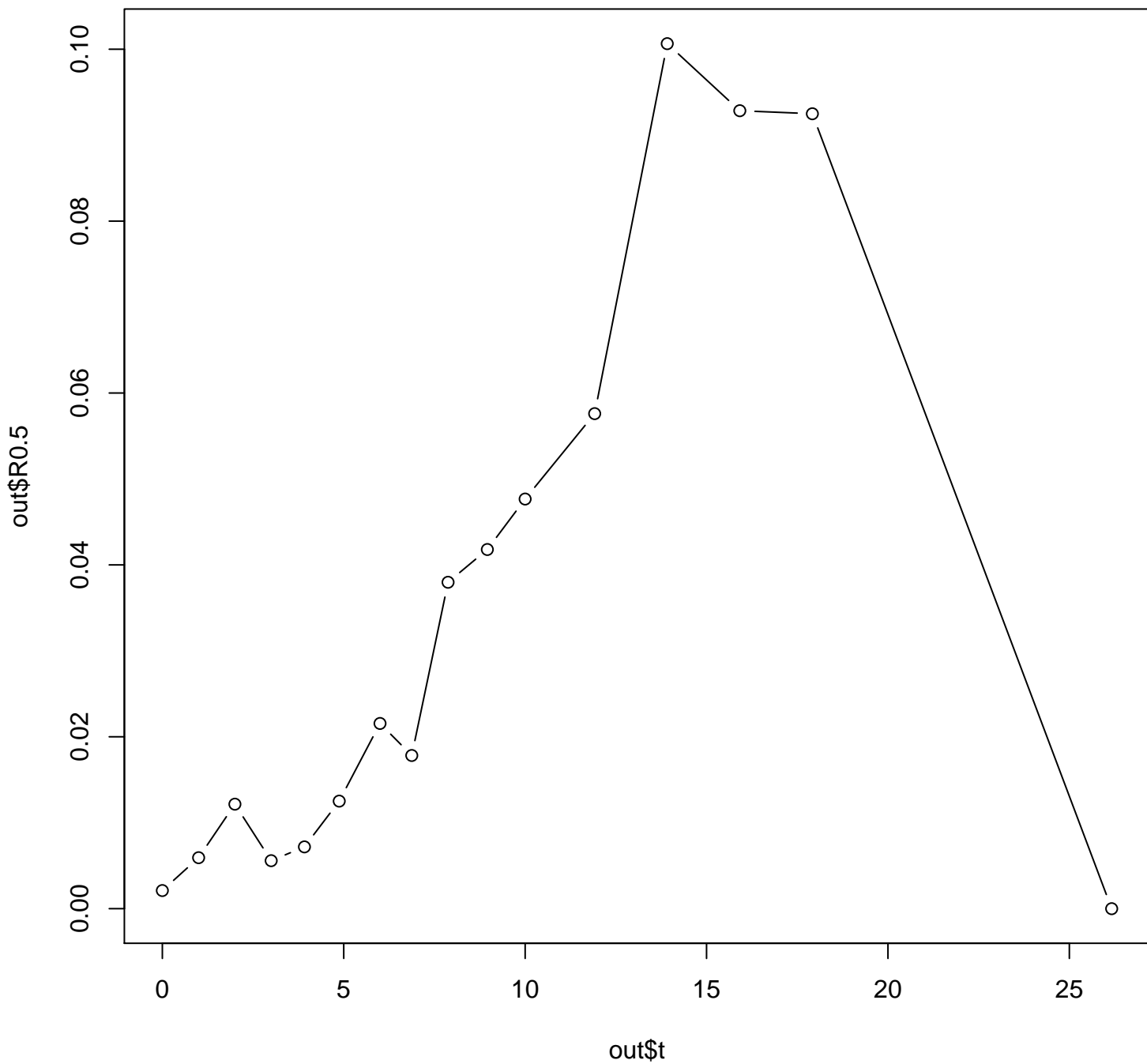
M8 050806



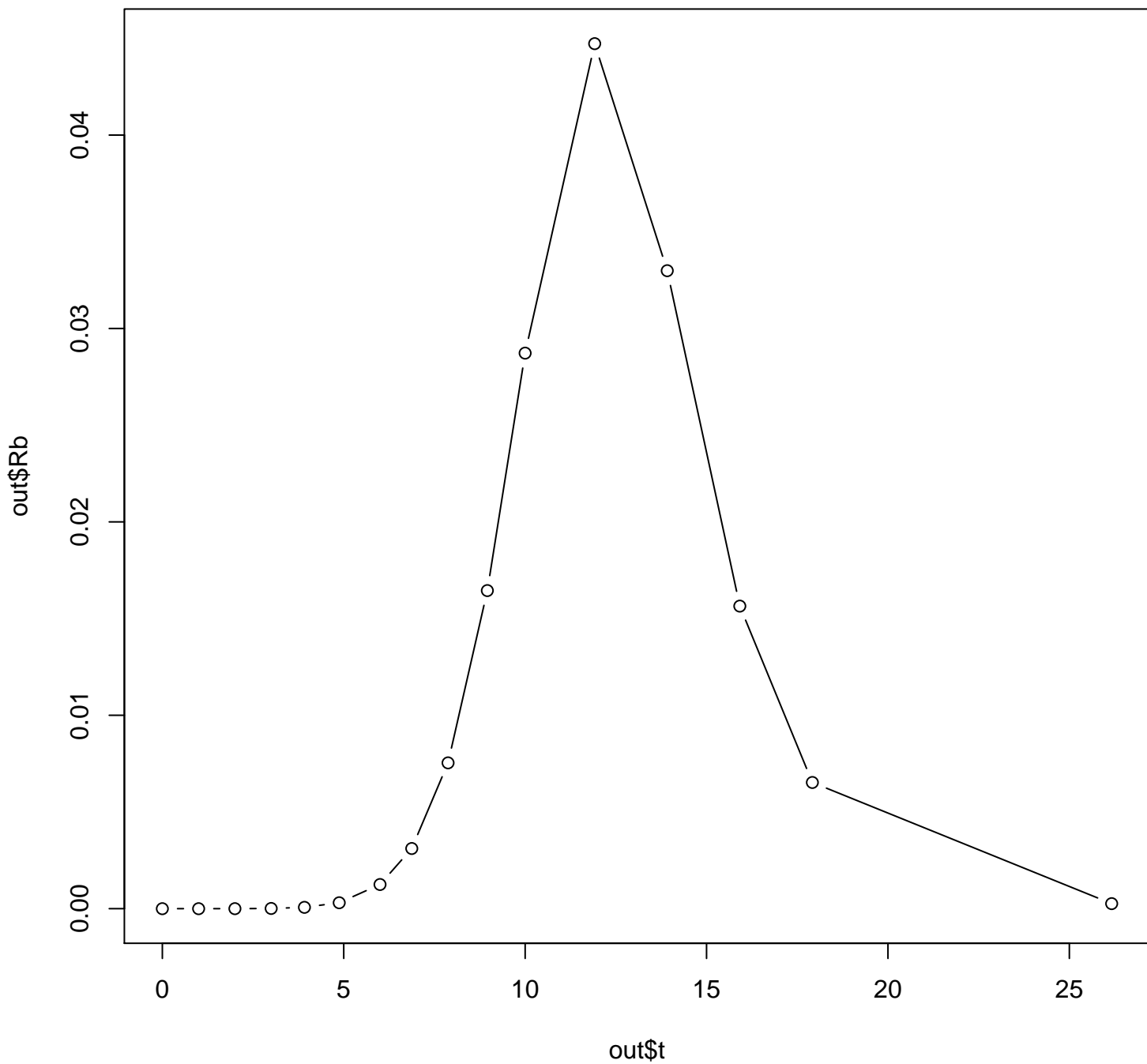
YPS128



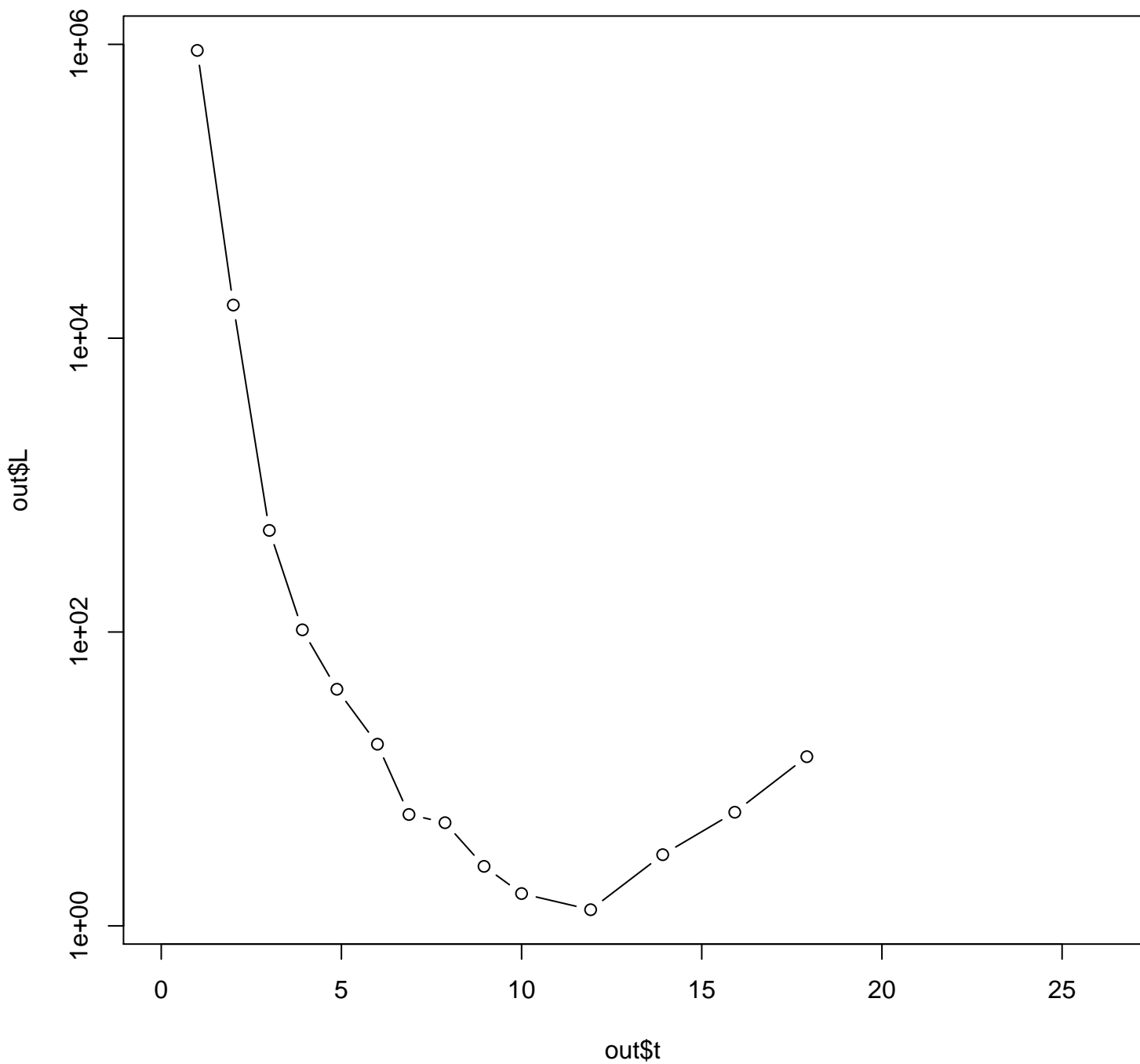
YPS128 050806



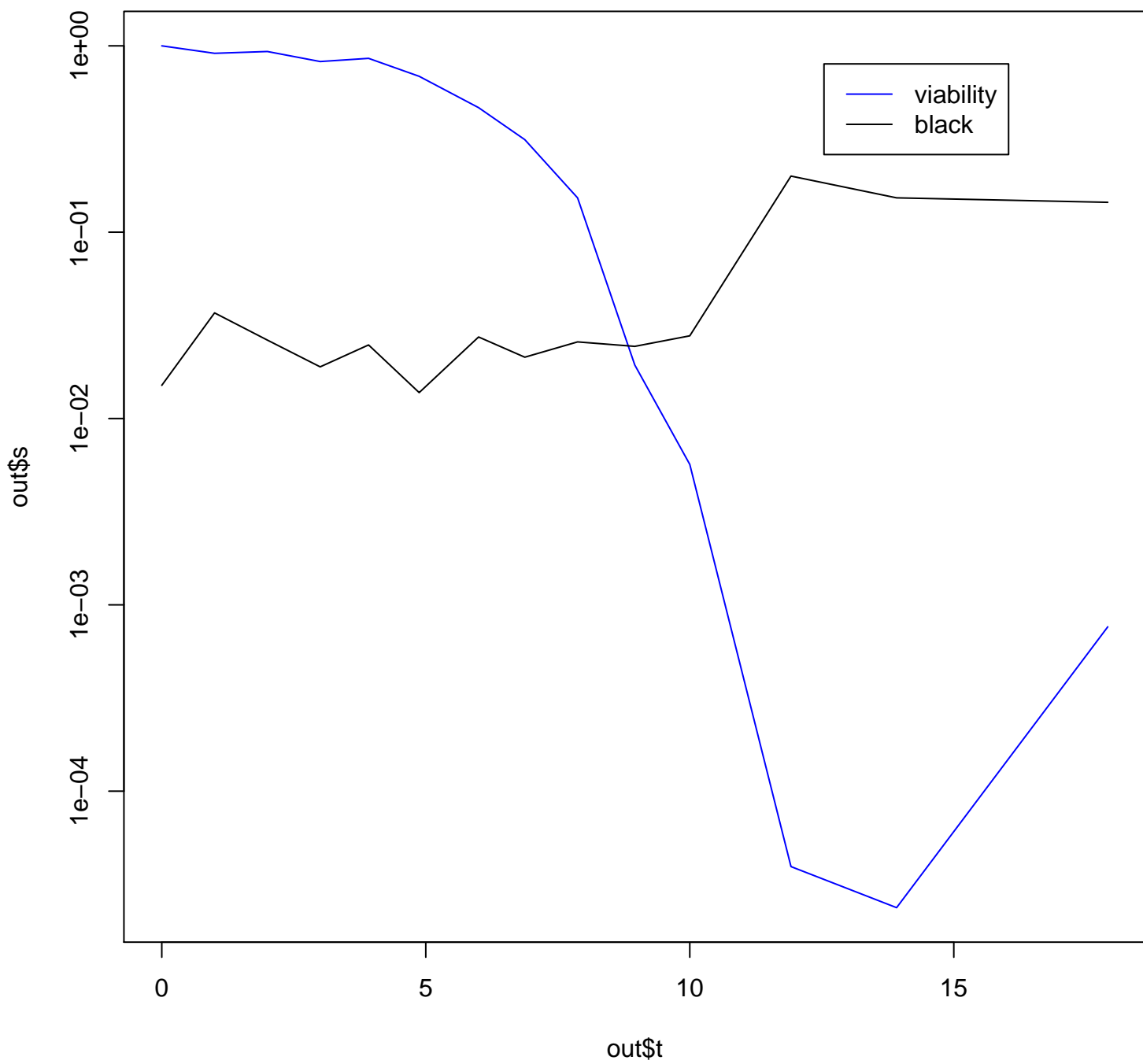
YPS128 050806



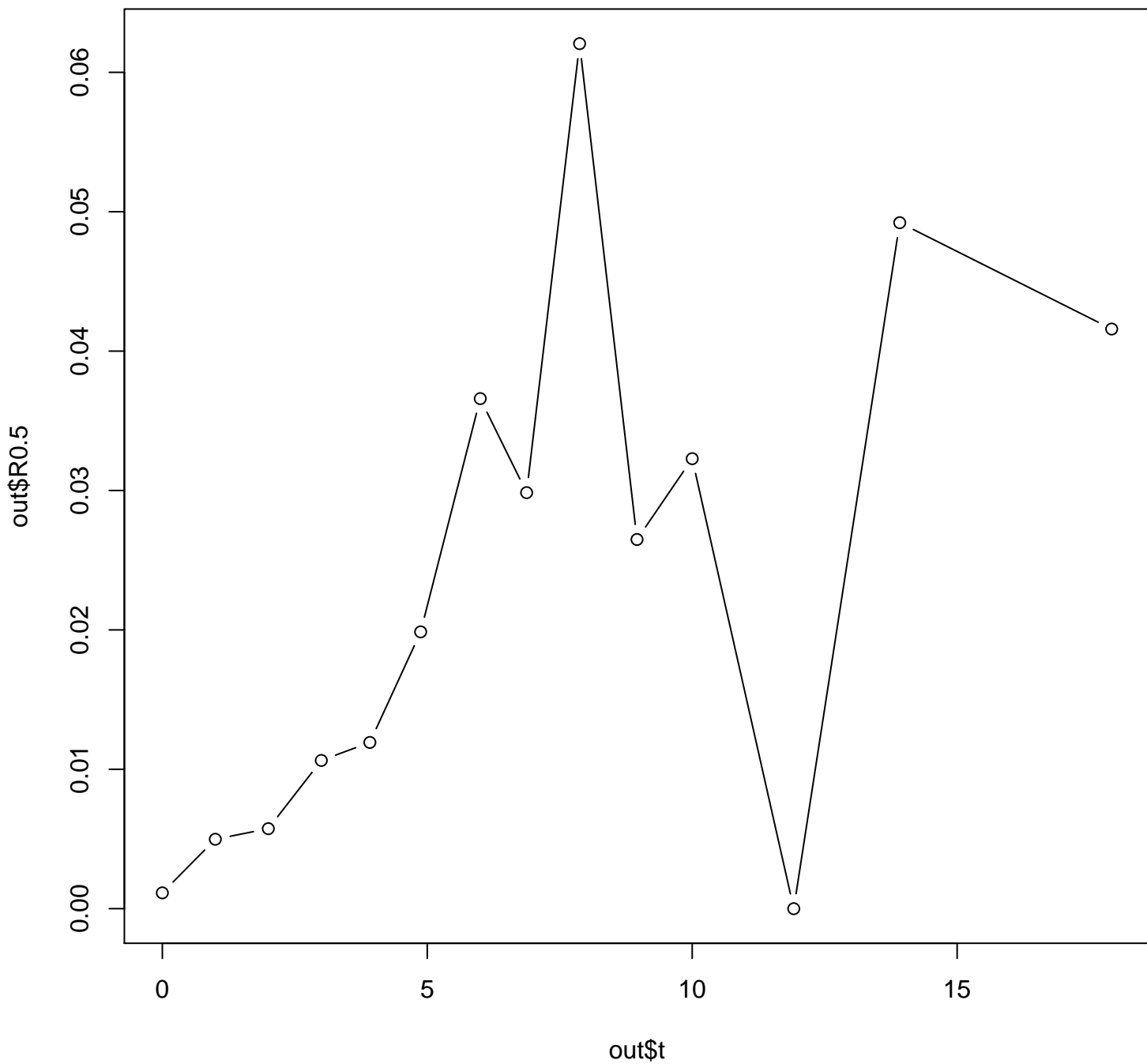
YPS128 050806



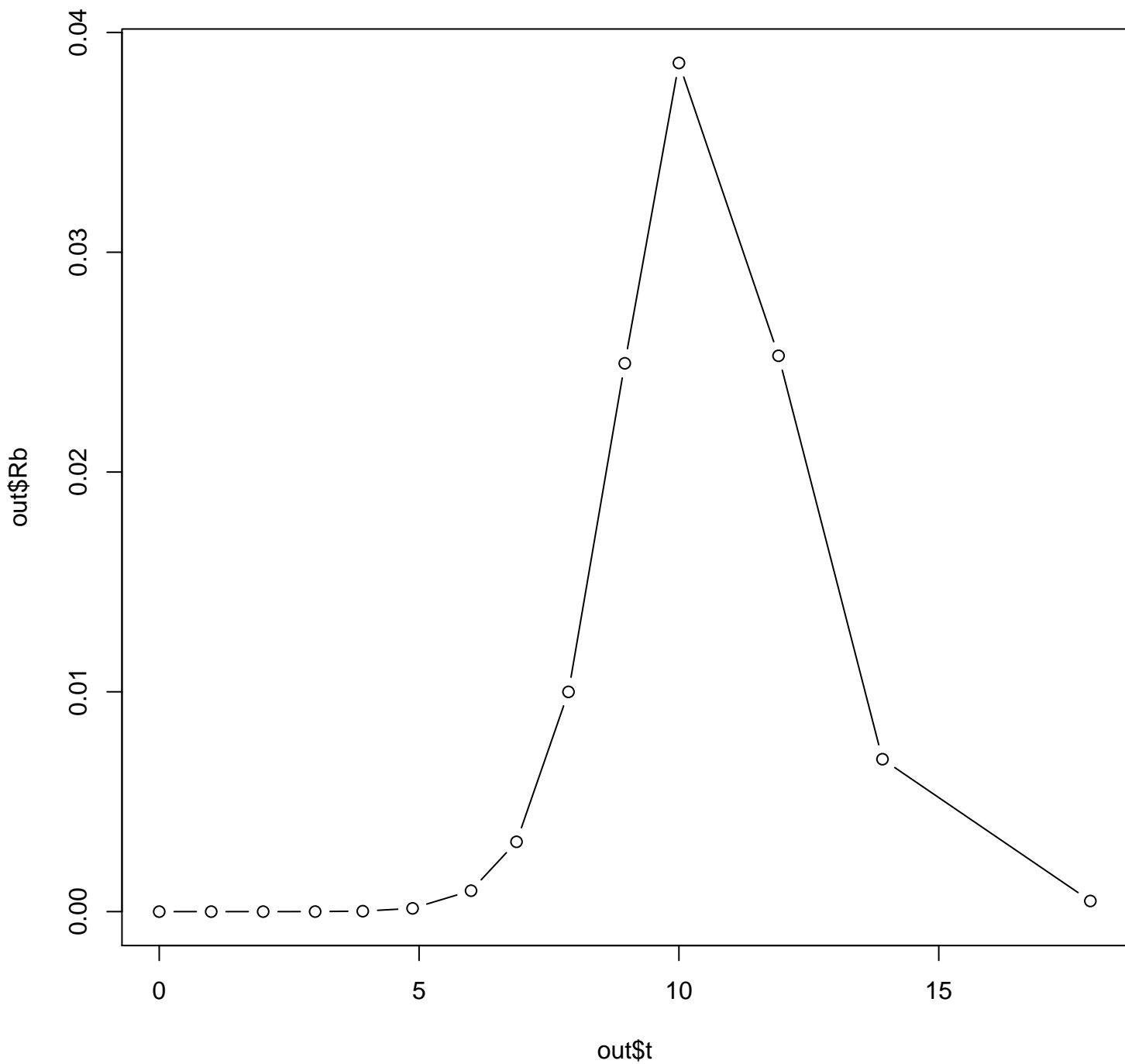
YPS163



YPS163 050806



YPS163 050806



YPS163 050806

