

# **MCUXpresso SDK Release Notes**

## **Supporting EVK-MIMXRT1020**



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# Chapter 1

## Overview

The MCUXpresso Software Development Kit (SDK) is a collection of software enablement for Microcontrollers that includes peripheral drivers, high-level stacks including FatFs, USB, lwIP, mbed TLS cryptography libraries, other middleware packages, and integrated RTOS support for FreeRTOS™ OS. In addition to the base enablement, the MCUXpresso SDK is augmented with demo applications and driver example projects, and API documentation to help the customers quickly leverage the support of the MCUXpresso SDK.

For more details about MCUXpresso SDK, see the MCUXpresso SDK homepage [MCUXpresso-SDK: Software Development Kit](#).

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### NOTE

See the attached Change Logs section at the end of this document to reference the device-specific driver logs, middleware logs, and RTOS log.

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# Chapter 2

## MCUXpresso SDK

As part of the MCUXpresso software and tools, MCUXpresso SDK is the evolution of Kinetis SDK v2.x.x, includes support for both LPC and i.MX System-on-Chips (SoC). The same drivers, APIs, and middleware are still available with support for Kinetis, LPC, and i.MX silicon. The MCUXpresso SDK adds support for the MCUXpresso IDE, an Eclipse-based toolchain that works with all MCUXpresso SDKs. Easily import your SDK into the new toolchain to have access to all of the available components, examples, and demos for your target silicon. In addition to the MCUXpresso IDE, support for the MCUXpresso Config Tools allows for easy cloning of existing SDK examples and demos, allowing users to easily leverage the existing software examples provided by the SDK for their own projects.

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### NOTE

In order to maintain compatibility with legacy Freescale code, the filenames and source code in MCUXpresso SDK containing the legacy Freescale prefix 'FSL' has been left as is. The 'FSL' prefix has been redefined as the NXP Foundation Software Library.

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# Chapter 3

## Development tools

The MCUXpresso SDK was compiled and tested with these development tools:

- IAR Embedded Workbench for Arm version 8.32.3
- MDK-Arm Microcontroller Development Kit (Keil)<sup>®</sup> 5.27
- Makefiles support with GCC revision 8-2018-q4-major GCC8 from Arm Embedded
- MCUXpresso IDE v11.0.0

# Chapter 4

## Supported development systems

This release supports boards and devices listed in this table. Boards and devices in boldface were tested in this release:

**Table 1. Supported MCU devices and development boards**

<b>Development boards</b>	<b>MCU devices</b>
<b>EVK-MIMXRT1020</b>	MIMXRT1021CAF4A, MIMXRT1021CAG4A, MIMXRT1021DAF5A, <b>MIMXRT1021DAG5A</b>

# Chapter 5

## Release contents

This table provides an overview of the MCUXpresso SDK release package contents and locations.

**Table 2. Release contents**

Deliverable	Location
Boards	<install_dir>/boards
Qualcomm WiFi	<install_dir>/middleware/wifi_qca
Demo applications	<install_dir>/boards/<board_name>/demo_apps
USB demo applications	<install_dir>/boards/<board_name>/usb_examples
lwIP demo applications	<install_dir>/boards/<board_name>/lwip_examples
Driver examples	<install_dir>/boards/<board_name>/driver_examples
Cortex Microcontroller Software Interface Standard (CMSIS) driver examples	<install_dir>/boards/<board_name>/cmsis_driver_examples
FatFS examples	<install_dir>/boards/<board_name>/fatfs_examples
AWS IoT SDK examples	<install_dir>/boards/<board_name>/aws_examples
RTOS examples	<install_dir>/boards/<board_name>/rtos_examples
Qualcomm WiFi stack examples	<install_dir>/boards/<board_name>/wifi_qca_examples
mbed TLS examples	<install_dir>/boards/<board_name>/mbedtls_examples
Documentation	<install_dir>/docs
USB Documentation	<install_dir>/docs/usb
lwIP Documentation	<install_dir>/docs/lwip
Middleware	<install_dir>/middleware
mbed TLS	<install_dir>/middleware/mbedtls
lwIP stack	<install_dir>/middleware/lwip
jsmn	<install_dir>/middleware/aws_iot/external_libs/jsmn
AWS IoT	<install_dir>/middleware/aws_iot
FatFS stack	<install_dir>/middleware/fatfs
USB stack	<install_dir>/middleware/usb
Driver, SoC header files, extension header files and feature header files, utilities	<install_dir>/devices/<device_name>
CMSIS Arm Cortex®-M header files, DSP library source	<install_dir>/CMSIS
Peripheral Drivers	<install_dir>/devices/<device_name>/drivers
CMSIS drivers	<install_dir>/devices/<device_name>/cmsis_drivers
Utilities such as debug console	<install_dir>/devices/<device_name>/utilities

*Table continues on the next page...*

**Table 2. Release contents (continued)**

RTOS Kernel Code	<install_dir>/rtos
Tools	<install_dir>/tools
segger_systemview	<install_dir>/boards/<board>/rtos_examples/visualization/ freertos_segger_sysview
percepio_snapshot	<install_dir>/boards/<board>/rtos_examples/visualization/ freertos_percepio_snapshot
gradle	<install_dir>/boards/<board>/aws_examples/ remote_control_android/gradle, boards/<board>/ aws_examples/led_wifi_android/gradle, boards/<board>/ aws_examples/device_configuration_android/gradle
FNET	<install_dir>/boards/<board>/aws_examples/ device_configuration_enet/fnet_mdns
AWS Remote Control	<install_dir>/boards/<board>/aws_examples/ remote_control_android/AwsRemoteControl.apk
AWS LED WiFi	<install_dir>/boards/<board>/aws_examples/ led_wifi_android/AwsLedWifi.apk
AWS Device Configuration	<install_dir>/boards/<board>/aws_examples/ device_configuration_android/AwsDeviceConfiguration.apk



# Chapter 6

## MCUXpresso SDK release package

The MCUXpresso SDK release package contents are aligned with the silicon subfamily it supports. This includes the boards, CMSIS, devices, documentation, middleware, and RTOS support.

### 6.1 Device support

The device folder contains all available software enablement for the specific System-on-Chip (SoC) subfamily. This folder includes clock-specific implementation, device register header file, device register feature header file, CMSIS derived device SVD, and the system configuration source files. Included with the standard SoC support are folders containing peripheral drivers, toolchain support, and a simple debug console.

The device-specific header files provide a direct access to the MCU peripheral registers. The device header file provides an overall SoC memory mapped register definition. In addition to the overall device memory mapped header file, the MCUXpresso SDK also includes the feature header file for each peripheral instantiated on the SoC.

The toolchain folder contains the startup code and linker files for each supported toolchain. The startup code is a CMSIScompliant startup that efficiently transfers the code execution to the main() function.

#### 6.1.1 Board support

The boards folder provides the board-specific demo applications, driver examples, RTOS, and middleware examples.

#### 6.1.2 Demo applications and other examples

The demo applications demonstrate the usage of the peripheral drivers to achieve a system level solution. Each demo application contains a readme file that describes the operation of the demo and required setup steps.

The driver examples demonstrate the capabilities of the peripheral drivers. Each example implements a common use case to help demonstrate the driver functionality.

### 6.2 Middleware

#### 6.2.1 USB stack

See the *MCUXpresso SDK USB Stack User's Guide* (document MCUXSDKUSBSUG) for more information.

##### 6.2.1.1 Peripheral devices tested with USB Host stack

This table provides a list of USB devices tested with the USB Host stack.

**Table 3. Peripheral devices**

Device type	Device
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*Table continues on the next page...*

**Table 3. Peripheral devices (continued)**

USB HUB	BELKIN F5U233 BELKIN F5U304 BELKIN F5U307 BELKIN F4U040 UNITEK Y-2151 Z-TEK ZK032A HYUNDAI HY-HB608
USB flash drive	ADATA C008 32 GB ADATA S102 8 G ADATA S102 16 G Verbatim STORE N GO USB Device 8 G Kingston DataTraveler DT101 G2 SanDisk Cruzer Blade 8 GB Unisplendour 1 G Imation 2 GB V-mux 2 GB Sanmina-SCI 128 M Corporate Express 1 G TOSHIBA THUHYBS-008G 8 G Transcend JF700 8 G Netac U903 16 G SSK SFD205 8 GB Rex 4 GB SAMSUNG USB3.0 16GB
USB card reader/adapter	SSK TF adapter Kawau Multi Card Reader Kawau TF adapter Kawau SDHC card

*Table continues on the next page...*

**Table 3. Peripheral devices (continued)**

USB Mouse	DELL MS111-P DELL M066U0A DELL MUAVDEL8 TARGUS AMU76AP DELL MD56U0 DELL MS111-T RAPOO M110
USB Keyboard	DELL SK8135 DELL SK8115

## 6.2.2 TCP/IP stack

The lwIP TCP/IP stack is pre-integrated with MCUXpresso SDK and runs on top of the MCUXpresso SDK Ethernet driver with Ethernet-capable devices/boards. For details, see the *lwIP TCP/IP Stack and MCUXpresso SDK Integration User's Guide* (document MCUXSDKLWIPUG).

## 6.2.3 File system

The FatFs file system is integrated with MCUXpresso SDK and can be used to access either the SD card or the USB memory stick when the SD card driver or the USB Mass Storage Device class implementation is used.

## 6.2.4 RTOS

The MCUXpresso SDK is integrated with FreeRTOS OS.

## 6.2.5 CMSIS

The MCUXpresso SDK is shipped with the standard CMSIS development pack, including the prebuilt libraries.

# Chapter 7

## MISRA compliance

All MCUXpresso SDK drivers and USB stack comply to MISRA 2012 rules with the following exceptions.

**Table 4. MISRA exceptions**

Exception Rules	Description
Rule 5.1	External identifiers shall be distinct.
Rule 5.4	Macro identifiers shall be distinct.
Rule 21.1	#define and #undef shall not be used on a reserved identifier or reserved macro name.
Rule 21.2	A reserved identifier or macro name shall not be declared.
Directive 4.4	Sections of code should not be "commented out".
Directive 4.5	Identifiers in the same name space with overlapping visibility should be typographically unambiguous.
Directive 4.6	Typedefs that indicate size and signedness should be used in place of the basic numerical types.
Directive 4.8	If a pointer to a structure or union is never dereferenced within a translation unit, then the implementation of the object should be hidden.
Directive 4.9	A function should be used in preference to a function-like macro where they are interchangeable.
Directive 4.13	Functions which are designed to provide operations on a resource should be called in an appropriate sequence.
Rule 1.2	Language extensions should not be used.
Rule 2.3	A project should not contain unused type declarations.
Rule 2.4	A project should not contain unused tag declarations.
Rule 2.5	A project should not contain unused macro declarations.
Rule 2.6	A function should not contain unused label declarations.
Rule 2.7	There should be no unused parameters in functions.
Rule 4.2	Trigraphs should not be used.
Rule 5.9	Identifiers that define objects or functions with internal linkage should be unique.
Rule 8.7	Functions and objects should not be defined with external linkage if they are referenced in only one translation unit.
Rule 8.9	An object should be defined at block scope if its identifier only appears in a single function.
Rule 8.11	When an array with external linkage is declared, its size should be explicitly specified.

*Table continues on the next page...*

**Table 4. MISRA exceptions (continued)**

Rule 8.13	A pointer should point to a const-qualified type whenever possible.
Rule 10.5	The value of an expression should not be cast to an inappropriate essential type.
Rule 11.4	A conversion should not be performed between a pointer to object and an integer type.
Rule 11.5	A conversion should not be performed from pointer to void into pointer to object.
Rule 12.1	The precedence of operators within expressions should be made explicit.
Rule 12.3	The comma operator should not be used.
Rule 12.4	Evaluation of constant expressions should not lead to unsigned integer wrap-around.
Rule 13.3	A full expression containing an increment (++) or decrement (--) operator should have no other potential side effects other than that caused by the increment or decrement operator.
Rule 15.4	There should be no more than one break or go to statement used to terminate any iteration statement.
Rule 17.5	The function argument corresponding to a parameter declared to have an array type shall have an appropriate number of elements.
Rule 17.8	A function parameter should not be modified.
Rule 19.2	The union keyword should not be used.
Rule 20.1	#include directives should only be preceded by preprocessor directives or comments.
Rule 20.10	The #and ## preprocessor operators should not be used.
Rule 21.12	The exception handling features of <fenv.h> should not be used. .

# Chapter 8

## Known issues

### 8.1 Maximum file path length in Windows 7<sup>®</sup> operating system

Windows 7 operating system imposes a 260 character maximum length for file paths. When installing the MCUXpresso SDK, place it in a directory close to the root to prevent file paths from exceeding the maximum character length specified by the Windows operating system. The recommended location is the C:\nxp folder.

### 8.2 USBFS controller issue

Because of the USBFS controller design issues, the USB host suspend/resume demos (usb\_suspend\_resume\_host\_hid\_mouse) of the full speed controller do not support the low speed device directly.

### 8.3 Create new project without board template

The following components should be selected at the same time when creating a new project without using a board template, including serial\_manager, serial\_manager\_uart, debug\_console, and one UART adapter (lpuart\_adapter for LPUART IP, uart\_adapter for UART IP, lpsci\_adapter for LPSCI IP, etc).

### 8.4 New Project Wizard compile failure

The following components request the user to manually select other components that they depend on to pass the compile. These components depend on several components, and the New Project Wizard (NPW) is not able to decide which one is needed by the user.

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**NOTE**

"xxx" means core variants like cm0plus, cm33, cm4, cm33\_nodsp.

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**Components:** Assert, assert\_cm0plus, assert\_xxx, assert\_lite, baremetal, button, codec\_i2c, codec\_i2c\_xxx, debug\_console, debug\_console\_xxx, debug\_console\_lite, dialog7212, led, misc\_utilities, panic, serial\_manager, serial\_manager\_xxx, serial\_manager\_swo, serial\_manager\_swo\_xxx, serial\_manager\_uart, serial\_manager\_uart\_xxx, serial\_manager\_usb\_cdc, serial\_manager\_usb\_cdc\_xxx, sgtl\_adapter, sgtl5000, shell, shell\_xxx, timer\_manager, wm8904, wm8904\_xxx, wm8904\_adapter, wm8904\_adapter\_xxx, wm8960, wm8960\_adapter, xip\_device.

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# **MCUXpresso SDK Release Notes Supporting EVK-MIMXRT1020**

**Change Logs**

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# 1 Driver Change Log

## CLOCK

The current CLOCK driver version is 2.2.0.

- 2.2.0
  - New feature
    - \* Adding new API `CLOCK_DelayAtLeastUs()` implemented by DWT to allow users set delay in unit of microsecond.
- 2.1.6
  - Bug Fix:
    - \* Fix build issue with GCC compiler when include header from C++ file.
- 2.1.5
  - Bug Fix:
    - \* Add initialization of the fractional mode and spread spectrum mode in `CLOCK_InitSysPll()`.
- 2.1.4
  - Optimization:
    - \* Add `PerClk` in `clock_name_t` and `CLOCK_GetFreq`.
    - \* Add APIs to get the frequency of AHB clock and SEMC, IPG clock and PER clock.
- 2.1.3
  - Use double instead of `uint64_t` to achieve better performance with double precision FPU.
- 2.1.2
  - some minor fixes.
- 2.0.0
  - initial version.

## IOMUXC

The current IOMUXC driver version is 2.0.0.

- 2.0.0
  - initial version.

## LPI2C\_CMSIS

Current LPI2C\_CMSIS driver version is 2.0

- 2.0
  - Initial version.

## **LPSPI\_CMSIS**

Current LPSPI\_CMSIS driver version is 2.1

- 2.1
  - Bug Fix:
    - \* Fix the incorrect clock polarity assignment in the driver. For ARM\_SPI\_CPOL0\_CPHA0 and other frame format parameters, CPOL = 0 means kSPI\_ClockPolarityActiveHigh not kSPI\_ClockPolarityActiveLow in driver.
  - New feature:
    - \* Allow user to set up the default Transmit value by using ARM\_SPI\_SET\_DEFAULT\_TX\_VALUE. Please note that set default value is not supported in slave interrupts, because the pin will stay tristated if TX buffer is NULL.
    - \* Enable slave select mode in the new driver, but this has no effect when users set any of them because the driver can only support the hardware control function.
    - \* Enable 3-Wire mode, users can use ARM\_SPI\_MODE\_MASTER\_SIMPLEX/ARM\_SPI\_MODE\_SLAVE\_SIMPLEX to enable this feature. For ARM\_SPI\_MODE\_MASTER\_SIMPLEX mode, select SOUT pin as the input/output pin, and for ARM\_SPI\_MODE\_SLAVE\_SIMPLEX, the SIN pin is selected as the input/output pin.
- 2.0
  - - Initial version.

## **LPUART\_CMSIS**

Current LPUART\_CMSIS driver version is 2.0

- 2.0
  - Initial version.

## **ADC**

The current ADC driver version is 2.0.2.

- 2.0.2
  - Used conversion control feature macro instead of that in IO map.
- 2.0.1
  - Added control macro to enable/disable the CLOCK code in current driver.
- 2.0.0
  - Initial version.

## **ADC\_ETC**

The current ADC\_ETC driver version is 2.0.1.

- 2.0.1

- Added control macro to enable/disable the CLOCK code in current driver.
- 2.0.0
  - Initial version.

## **AIPSTZ**

The current AIPSTZ driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **AOI**

The current AOI driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **BEE**

The current BEE driver version is 2.0.1.

- 2.0.1
  - Bug fix:
    - \* Fixed bug in key user key loading sequence. BEE must be enabled during loading of user key.
    - \* Fixed typos in comments.
  - Feature:
    - \* Setting of AES nonce was moved from BEE\_SetRegionKey() into separate BEE\_SetRegionNonce() function.
    - \* Changed handling of region settings. Both regions are configured simultaneously by BEE\_SetConfig() function. Configuration of FAC start and end address using IOMUXC\_GPRs was moved to application.
    - \* Added configuration setting for endian swap, access permission and region security level.
    - \* Default value for region address offset was changed to 0.
- 2.0.0
  - Initial version.

## **CACHE**

The current CACHE driver version is 2.0.1.

- 2.0.1

- Fixed cache size issue in L2CACHE\_GetDefaultConfig API.
- 2.0.0
  - Initial version.

## **CMP**

The current CMP driver version is 2.0.1.

- 2.0.1
  - Bug Fix:
    - \* Fix MISRA-2012 rules.
      - Rule 14.4, rule 10.3, rule 10.1, rule 10.4, rule 17.7.
- 2.0.0
  - Initial version.

## **COMMON**

The current COMMON driver version is 2.1.0.

- 2.1.0
  - New features:
    - \* Added IRQ operation for XCC toolchain.
    - \* Added group IDs for newly supported drivers.
- 2.0.2
  - MISRA C-2012 issue fixed.
    - \* Fixed rule contain: rule-10.4.
- 2.0.1
  - Removed the implementation of LPC8XX Enable/DisableDeepSleepIRQ() function.
  - Added new feature macro switch "FSL\_FEATURE\_HAS\_NO\_NONCACHEABLE\_SECTION" for specific SoCs which have no noncacheable sections, that helps avoid an unnecessary complex in link file and the startup file.
  - Updated the align(x) to **attribute**(aligned(x)) to support MDK v6 armclang compiler.
- 2.0.0
  - Initial version.

## **DCDC**

The current DCDC driver version is 2.0.0.

- 2.0.0
  - Initial version.

## DMAMUX

The current DMAMUX driver version is 2.0.3.

- 2.0.3
  - Fixed the issue for MISRA-2012 check.
    - \* Fixed rule 10.4, rule 10.3.
- 2.0.2
  - New feature:
    - \* Added an always-on enable feature to a DMA channel for ULP1 DMAMUX support.
- 2.0.1
  - Bug fix:
    - \* Fixed build warning while setting the DMA request source in DMAMUX\_SetSource-Change issue by changing the type of the parameter source from uint8\_t to uint32\_t.
- 2.0.0
  - Initial version.

## EDMA

The current eDMA driver version is 2.1.8.

- 2.1.8
  - Bug fix:
    - \* Fixed wrong channel preemption base address used in EDMA\_SetChannelPreemption-Config api that will cause channel preemption register cannot configure correctly.
- 2.1.7
  - Bug fix:
    - \* Fixed wrong transfer size setting
      - Add 8 bytes transfer configuration and feature for RT series
      - Add feature to support 16 bytes transfer for Kinetis
    - \* Fixed the issue that EDMA\_HandleIRQ will go to incorrect branch When TCD is not used and callback function is not registered.
- 2.1.6
  - Bug fix:
    - \* Fixed KW3X MISRA Issue.
      - Rule 14.4, 10.8, 10.4, 10.7, 10.1, 10.3, 13.5, 13.2.
  - Improvements:
    - \* Clear IRQ handler that not available for specific platform with macro FSL\_FEATURE\_EDMA\_MODULE\_CHANNEL\_IRQ\_ENTRY\_SHARED\_OFFSET.
- 2.1.5
  - Improvements:
    - \* Improve EDMA IRQ handler to support half interrupt feature.
- 2.1.4
  - Bug fix:
    - \* Clear enabled request, status during EDMA\_Init for the case that EDMA is halted before

- reinitialization.
- 2.1.3
  - Bug fix:
    - \* Add clear DONE bit in IRQ handler to avoid overwrite TCD issue.
    - \* Optimize above solution for the case that transfer request occurs in callback.
- 2.1.2
  - Improvements:
    - \* Added interface to get next TCD address.
    - \* Added interface to get the unused TCD number.
- 2.1.1
  - Improvements:
    - \* Added documentation for eDMA data flow when scatter/gather is implemented for the EDMA\_HandleIRQ API.
    - \* Updated and corrected some related comments in the EDMA\_HandleIRQ API and edma\_handle\_t struct.
- 2.1.0
  - Improvements:
    - \* Changed the EDMA\_GetRemainingBytes API into EDMA\_GetRemainingMajorLoopCount due to eDMA IP limitation (see API comments/note for further details).
- 2.0.5
  - Improvements:
    - \* Added pubweak DriverIRQHandler for K32H844P (16 channels shared).
- 2.0.4
  - Improvements:
    - \* Added support for SoCs with multiple eDMA instances.
    - \* Added pubweak DriverIRQHandler for KL28T DMA1 and MCIMX7U5\_M4.
- 2.0.3
  - Bug fix:
    - \* Fixed the incorrect pubweak IRQHandler name issue, which causes re-definition build errors when client sets his/her own IRQHandler, by changing the 32-channel IRQHandler name to DriverIRQHandler.
- 2.0.2
  - Bug fix:
    - \* Fixed incorrect minorLoopBytes type definition in \_edma\_transfer\_config struct, and defined minorLoopBytes as uint32\_t instead of uint16\_t.
- 2.0.1
  - Bug fix:
    - \* Fixed the eDMA callback issue (which did not check valid status) in EDMA\_HandleIRQ API.
- 2.0.0
  - Initial version.



## ENET

The current ENET driver version is 2.2.4.

- 2.2.4
  - Added call to Data Synchronization Barrier instruction before activating Tx/Rx buffer descriptor to ensure previous data update is completed.
  - Fixed the issue that ENET\_Ptp1588GetTimer does not handle the timer wrap situation.
  - Improved ENET\_TransmitIRQHandler to store timestamps for multiple transmit buffer descriptors.
- 2.2.3
  - Improved data buffer cache maintenance in the ENET driver.
- 2.2.2
  - Added the APIs for extended multi-ring support.
  - Added the AVB configure API for extended AVB feature support.
- 2.2.1
  - Changed the input data pointer attribute to const in ENET\_SendFrame().
- 2.1.1
  - Added the extended MDIO IEEE802.3 Clause 45 MDIO format SMI command APIs.
  - Added the extended interrupt coalescing feature.
  - Combined all storage operations in the ENET\_Init to ENET\_SetHandler API.
- 2.0.1
  - Bug fix:
    - \* Used direct transmit busy check when doing data transmit.
  - Miscellaneous changes:
    - \* Updated IRQ handler work flow.
    - \* Changed the TX/RX interrupt macro from kENET\_RxByteInterrupt to kENET\_RxBufferInterrupt, from kENET\_TxByteInterrupt to kENET\_TxBufferInterrupt.
    - \* Deleted unnecessary parameters in ENET handler.
- 2.0.0
  - Initial version.

## EWM

The current EWM driver version is 2.0.1.

- 2.0.1
  - Fixed EWM\_Deinit hard fault issue.
- 2.0.0
  - Initial version.

## FLEXCAN

The current FLEXCAN driver version is 2.4.0.

- 2.4.0
  - MISRA C-2012 issue check.
    - \* Fixed rule contain: rule-12.1, rule-17.7, rule-16.4, rule-11.9, rule-8.4, rule-14.4, rule-10.8, rule-10.4, rule-10.3, rule-10.7, rule-10.1, rule-11.6, rule-13.5, rule-11.3, rule-8.3, rule-12.2. rule-16.1.
  - Improvements:
    - \* fix issue for [FlexCAN] RemoteRequest UT Case run fail
    - \* Implementation all TX and RX transferring Timestamp used in FlexCAN demos
    - \* fix issue for UT Test Fail for CANFD payload size changed from 64BperMB to 8PerMB
    - \* Implementation for Update released CAN-FD capable products to same driver support, improve baud rate API
  - Bug fixes:
    - \* CANFD transfer data fail when use bus baudrate 30Khz
    - \* fix issue ERR009595 not follow the ERRATA document.
    - \* fix code error for ERR006032 work around solution
    - \* fix Coverity issue BAD\_SHIFT in FLEXCAN
    - \* fix Repo build warning issue for variable no initial.
- 2.3.2
  - Improvements:
    - \* Implementation for ERR005959.
    - \* Implementation for ERR005829.
    - \* Implementation for ERR006032.
- 2.3.1
  - Bug fixes:
    - \* Adding correct handle when kStatus\_FLEXCAN\_TxSwitchToRx coming.
- 2.3.0
  - Improvements:
    - \* Added self-wakeup support from STOP mode in the interrupt handling.
- 2.2.3
  - Bug fix:
    - \* Fixed CANFD data phase's bit rate not set as expected.
- 2.2.2
  - Improvements:
    - \* Added a time stamp feature and enabled it in the interrupt\_transfer example.
- 2.2.1
  - Improvements:
    - \* Separated CANFD initialization API.
    - \* In the interrupt handling, fixed the issue that the user cannot use the normal CAN API when FD is present.
- 2.2.0
  - Improvements:
    - \* Added FSL\_FEATURE\_FLEXCAN\_HAS\_SUPPORT\_ENGINE\_CLK\_SEL\_REMOVE feature to support SoCs without CAN Engine Clock selection in FlexCAN module.
    - \* Added FlexCAN Serial Clock Operation to support i.MX SoCs.
- 2.1.0

- Bug fixes:
  - \* Fixed incorrect function name spelling of FLEXCAN\_XXX()
  - \* Moved Freeze Enable/Disable setting from FLEXCAN\_Enter/ExitFreezeMode() to FLEXCAN\_Init();
  - \* Fixed incorrect helper macro values.
- Other changes:
  - \* Hid FLEXCAN\_Reset() from user.
  - \* Used NDEBUG macro to wrap FLEXCAN\_IsMbOccupied() function instead of DEBUG macro.
- 2.0.0
  - Initial version.

## **FLEXIO**

The current FLEXIO driver version is 2.0.2.

- 2.0.2:
  - Improvements:
    - \* Split FlexIO component, which combines all flexio/flexio\_uart/flexio\_i2c/flexio\_i2s drivers into several components. FlexIO component, flexio\_uart component, flexio\_i2c\_master component, and flexio\_i2s component.
- 2.0.1
  - Bug fix:
    - \* Fixed the doze mode configuration error in FLEXIO\_Init API. For enableInDoze = true, the configuration should be 0; for enableInDoze = false, the configuration should be 1.

## **FLEXIO\_UART**

The current FLEXIO\_UART driver version is 2.1.5.

- 2.1.5
  - Triggered user callback when all data in ringbuffer are received when calling FLEXIO\_UART\_TransferReceiveNonBlocking.
- 2.1.4
  - Unified component full name to FLEXIO UART (DMA/EDMA) driver
- 2.1.3
  - Bug fixes: The following modifications support FlexIO using multiple instances.
    - \* Removed FLEXIO\_Reset API in module Init APIs.
    - \* Updated module Deinit APIs to reset the shifter/timer configuration instead of disable module and disable clock.
    - \* Updated module Enable APIs to only support enable operation.
- 2.1.2
  - Bug fixes:
    - \* Fixed the transfer count calculation issue in FLEXIO\_UART\_TransferGetReceiveCount,

- FLEXIO\_UART\_TransferGetSendCount, FLEXIO\_UART\_TransferGetReceiveCountDMA, FLEXIO\_UART\_TransferGetSendCountDMA, FLEXIO\_UART\_TransferGetReceiveCountEDMA and FLEXIO\_UART\_TransferGetSendCountEDMA
  - \* Fixed the Dozen mode configuration error in FLEXIO\_UART\_Init API. For enableInDoze = true, the configuration should be 0; for enableInDoze = false, the configuration should be 1.
  - \* Reported error when set baudrate too low and FLEXIO cannot reach that baudrate.
  - \* Disabled FLEXIO\_UART receive interrupt instead of disable all NVIC when read data from ring buffer. Because ring buffer is used, receive nonblocking disables all NVIC interrupts to protect the ring buffer. This has negative effects on other IPS which are using interrupt.
- 2.1.1
  - Bug fixes:
    - \* Changed the API name FLEXIO\_UART\_StopRingBuffer to FLEXIO\_UART\_TransferStopRingBuffer to align with the definition in C file.
- 2.1.0
  - New features:
    - \* Added Transfer prefix in transactional APIs.
    - \* Added txSize/rxSize in handle structure to record the transfer size.
  - Bug fixes:
    - \* Added error handle to handle the data count is zero or data buffer is NULL situation.

## **FLEXIO\_I2C**

The current FLEXIO\_I2C driver version is 2.1.8.

- 2.1.8
  - Fixed coverity issue of useless call in FLEXIO\_I2C\_MasterTransferRunStateMachine.
- 2.1.7
  - Bug fix:
    - \* Fixed the issue that FLEXIO\_I2C\_MasterTransferBlocking does not wait for STOP bit sent.
- 2.1.6
  - Bug fix:
    - \* Fixed the issue that I2C Master transfer APIs(blocking/non-blocking) does not support the situation that master transfer with subaddress and transfer data size zero, which means no data follows by the subaddress.
- 2.1.5
  - Unified component full name to FLEXIO I2C driver.
- 2.1.4
  - Bug fixes: The following modifications support FlexIO using multiple instances.
    - \* Removed FLEXIO\_Reset API in module Init APIs.
    - \* Updated module Deinit APIs to reset the shifter/timer config instead of disable module and disable clock.

- \* Updated module Enable APIs to only support enable operation.
- 2.1.3
  - Changed the prototype of FLEXIO\_I2C\_MasterInit to return kStatus\_Success if initialization successfully and return kStatus\_InvalidArgument if "(srcClock\_Hz / masterConfig->baud-Rate\_Bps) / 2 - 1" exceeds 0xFFU.
- 2.1.2
  - Fixed the FLEXIO I2C issue where the master cannot receive data from I2C slave in high baudrate.
  - Fixed the FLEXIO I2C issue where the master cannot receive NAK when master sends non-existent addr.
  - Fixed the FLEXIO I2C issue where the master cannot get transfer count successfully.
  - Fixed the FLEXIO I2C issue where the master cannot receive data successfully when sending data first.
  - Fixed the Dozen mode configuration error in FLEXIO\_I2C\_MasterInit API. For enableInDoze = true, the configuration should be 0; for enableInDoze = false, the configuration should be 1.
  - Fixed the FLEXIO\_I2C\_MasterTransferBlocking API calls FLEXIO\_I2C\_MasterTransferCreateHandle issue. This leads the s\_flexioHandle/s\_flexioIsr/s\_flexioType variable written. Then, if calling FLEXIO\_I2C\_MasterTransferBlocking API multiple times, the s\_flexioHandle/s\_flexioIsr/s\_flexioType variable cannot be written any more due to it being out of range. This leads to the following: NonBlocking transfer APIs cannot work due to register IRQ failed.
- 2.1.1
  - Bug fixes:
    - \* Implemented the FLEXIO\_I2C\_MasterTransferBlocking API which defined in header file but has no implementation in the C file.
- 2.1.0
  - New features:
    - \* Added Transfer prefix in transactional APIs.
    - \* Added transferSize in handle structure to record the transfer size.

## **FLEXIO\_SPI**

The current FLEXIO\_SPI driver version is 2.1.3.

- 2.1.3
  - Unify component full name to FLEXIO SPI (DMA/EDMA) Driver
- 2.1.2
  - Bug fixes: The following modification support FlexIO using multiple instances.
    - \* Removed FLEXIO\_Reset API in module Init APIs.
    - \* Updated module Deinit APIs to reset the shifter/timer config instead of disable module and disable clock.
    - \* Updated module Enable APIs to only support enable operation.
- 2.1.1
  - Bug fixes:

- \* Fixed bug where FLEXIO SPI transfer data is in 16 bit per frame mode with eDMA.
- \* Fixed bug where FLEXIO SPI transfer data is in 16 bit per frame and direction is Lsbfirst mode with eDMA and interrupt.
- \* Fixed the Dozen mode configuration error in FLEXIO\_SPI\_MasterInit/FLEXIO\_SPI\_SlaveInit API. For enableInDoze = true, the configuration should be 0; for enableInDoze = false, the configuration should be 1.
- Optimization:
  - \* Added #ifndef/#endif to allow user to change the default TX value at compile time.
- 2.1.0
  - New features:
    - \* Added Transfer prefix in transactional APIs.
    - \* Added transferSize in handle structure to record the transfer size.
  - Bug fixes:
    - \* Fixed the error register address return for 16-bit data write in FLEXIO\_SPI\_GetTxData-RegisterAddress.
    - \* Provided independent IRQHandler/transfer APIs for Master and slave to fix the baudrate limit issue.

## **FLEXIO\_I2S**

The current FLEXIO\_I2S driver version is 2.1.6.

- 2.1.6
  - Bug fix:
    - \* Added reset FLEXIO before FLEXIO I2S init to make sure FLEXIO status is normal.
- 2.1.5
  - Bug fix:
    - \* Fixed I2S driver use hard code for bitwidth setting.
- 2.1.4
  - Unified component's full name to FLEXIO I2S (DMA/EDMA) driver.
- 2.1.3
  - Bug fixes: The following modifications support FLEXIO using multiple instances.
    - \* Removed FLEXIO\_Reset API in module Init APIs.
    - \* Updated module Deinit APIs to reset the shifter/timer config instead of disable module and disable clock.
    - \* Updated module Enable APIs to only support enable operation.
- 2.1.2
  - New features:
    - \* Added configure items for all pin polarity and data valid polarity.
    - \* Added default configure for pin polarity and data valid polarity.
- 2.1.1
  - Bug fixes:
    - \* Fixed FLEXIO I2S RX data read error and eDMA address error.
    - \* Fixed FLEXIO I2S slave timer compare setting error.

- 2.1.0
  - New features:
    - \* Added Transfer prefix in transactional APIs.
    - \* Added transferSize in handle structure to record the transfer size.

## **FLEXIO\_MCU\_LCD**

The current FLEXIO\_MCU\_LCD driver version is 2.0.2.

- 2.0.2
  - Unified component full name to FLEXIO\_MCU\_LCD (EDMA) driver.
- 2.0.1
  - Bug fixes: The following modification to support FlexIO using multiple instances.
    - \* Removed FLEXIO\_Reset API in module Init APIs.
    - \* Updated module Deinit APIs to reset the shifter/timer configuration instead of disable module and disable clock.
    - \* Updated module Enable APIs to only support enable operation.
- 2.0.0
  - Initial version.

## **FLEXIO\_CAMERA**

The current FLEXIO\_CAMERA driver version is 2.1.2.

- 2.1.2
  - Unified component full name to FLEXIO CAMERA (EDMA) driver.
- 2.1.1
  - Bug fixes: The following modifications support FlexIO using multiple instances.
    - \* Removed FLEXIO\_Reset API in module Init APIs.
    - \* Updated module Deinit APIs to reset the shifter/timer configuration instead of disable module and disable clock.
    - \* Updated module Enable APIs to only support enable operation.
- 2.1.0
  - New features:
    - \* Added Transfer prefix in transactional APIs.

## **FLEXRAM**

The current FLEXRAM driver version is 2.0.4.

- 2.0.4
  - Fixed FlexRAM driver missing extern C around functions in header file.
  - Removed magic address feature from driver.
- 2.0.3

- Fixed TCM size configuration is wrong when TCM bank number is not a value power of 2.
- 2.0.2
  - Updated driver due to Reference Manual update.
- 2.0.1
  - Fixed MISRA issue.
- 2.0.0
  - Initial version.

## **FLEXSPI**

The current FLEXSPI driver version is 2.1.1.

- 2.1.1
  - Improvements:
    - \* Default enable prefetch for AHB RX buffer configuration in FLEXSPI\_GetDefaultConfig, which is align with the reset value in AHB RX BUFxCR0.
    - \* Added software workaround for ERR011377 in FLEXSPI\_SetFlashConfig, add some delay after DLL lock status set to ensure correct data read/write.
- 2.1.0
  - New features:
    - \* Added new API FLEXSPI\_UpdateRxSampleClock for user to update read sample clock source after initialization.
    - \* Added reset peripheral operation in FLEXSPI\_Init if required.
- 2.0.5
  - Bug fixes:
    - \* Fixed FLEXSPI\_UpdateLUT cannot do partial update issue.
- 2.0.4
  - Bug fixes:
    - \* Reset flash size for all ports to zero in FLEXSPI\_Init, fixed the possible out of range flash access with no error reported.
- 2.0.3
  - Bug fixes:
    - \* Fixed AHB receive buffer size configuration issue. The FLEXSPI\_AHBRXBUFCR0\_B-UFSZ field should configure 64 bits size, and currently the the AHB receive buffer size is in bytes which means 8-bit, so the correct configuration should be config->ahbConfig->buffer[i].bufferSize / 8.
- 2.0.2
  - New features:
    - \* Supports DQS write mask enable/disable feature during set FLEXSPI configuration.
    - \* Provides new API FLEXSPI\_TransferUpdateSizeEDMA for user to update eDMA transfer size(SSIZE/DSIZE) per DMA transfer.
  - Bug fixes:
    - \* Fixed FLEXSPI\_Init invalid operation to enable AHB bus Read Access to IP RX FIFO issue.



- \* Fixed FLEXSPI\_Init incorrect operation to configure IP TX FIFO watermark issue.
- 2.0.1
  - Bug fixes:
    - \* Fixed the flag clear issue and AHB read Command index configuration issue in FLEXSPI\_SetFlashConfig.
    - \* Updated FLEXSPI\_UpdateLUT function to update LUT table from any index instead of previous command index.
    - \* Added bus idle wait in FLEXSPI\_SetFlashConfig and FLEXSPI\_UpdateLUT to ensure bus is idle before any change to FlexSPI controller.
    - \* Updated interrupt API FLEXSPI\_TransferNonBlocking and interrupt handle flow FLEXSPI\_TransferHandleIRQ.
    - \* Updated eDMA API FLEXSPI\_TransferEDMA.
- 2.0.0
  - Initial version.

## GPC

The current GPC driver version is 2.1.0.

- 2.1.0
  - Updated driver for IMX6RT.
- 2.0.0
  - Initial version.

## GPT

The current GPT driver version is 2.0.0.

- 2.0.0
  - Initial version.

## GPIO

The current GPIO driver version is 2.0.2.

- 2.0.2
  - Fixed bug enabling wrong GPIO clock gate in initial API. Since some GPIO instances may not have a clock gate enabled, it checks the clock gate number and makes sure the clock gate is valid.
- 2.0.1:
  - API interface changes:
    - \* Refined naming of API while keeping all original APIs, marking them as deprecated. Original API will be removed in next release. The main change is to update the API with prefix of \_PinXXX() and \_PortXXX().

- 2.0.0
  - Initial version.

## **KPP**

The current KPP driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **LPI2C**

The current LPI2C driver version is 2.1.9.

- 2.1.9
  - Fixed coverity issue of unchecked return value in I2C\_RTOS\_Transfer. Fixed coverity issue of operands does not affect the result in LPI2C\_SlaveReceive and LPI2C\_SlaveSend.
  - Removed STOP signal wait when NAK detected.
  - Cleared slave repeat start flag before transmission started in LPI2C\_SlaveSend/LPI2C\_SlaveReceive. The issue is that LPI2C\_SlaveSend/LPI2C\_SlaveReceive does not handle with the reserved repeat start flag. This causes the next slave to send a break, and the master is always in the receive data status, but cannot receive data.
- 2.1.8
  - Bug fix:
    - \* Fixed the transfer issue with LPI2C\_MasterTransferNonBlocking, kLPI2C\_TransferNoStopFlag, and the wait transfer done through callback in a way it is not doing a blocking transfer.
    - \* Fixed issue where STOP signal does not appear in the bus when NAK event happens.
- 2.1.7
  - Bug fix:
    - \* Cleared the stopflag before transmission started in LPI2C\_SlaveSend/LPI2C\_SlaveReceive. The issue is that LPI2C\_SlaveSend/LPI2C\_SlaveReceive does not handle with the reserved stop flag and causes the next slave to send a break, and the master always stays in the receive data status but cannot receive data.
- 2.1.6
  - Bug fix:
    - \* Fixed driver MISRA build error and C++ build error in LPI2C\_MasterSend and LPI2C\_SlaveSend.
    - \* Reset FIFO in LPI2C Master Transfer functions to avoid that there are still remaining bytes in FIFO during last transfer.
    - \* Fixed issue where LPI2C\_MasterStop does not return the correct NAK status in the bus for second transfer to the non-existing slave address.
- 2.1.5
  - Bug fix:

- \* Extended the Driver IRQ handler to support LPI2C4 and change to use ARRAY\_SIZE(k-Lpi2cBases) instead of FEATURE COUNT to decide the array size for handle pointer array.
- 2.1.4
  - Bug fix:
    - \* Fixed the LPI2C\_MasterTransferEDMA receive issue when LPI2C share same request source for TX/RX DMA request. Previously, the API uses scatter-gather method, which handles the command transfer first, then handles the linked TCD which is preset with the receive data transfer. The issue is that the TX DMA request and the RX DMA request are both enabled, so when the DMA finished the first command TCD transfer and handled the receive data TCD, the TX DMA request still happens due to TX FIFO empty. The result is the RX DMA transfer starts, without waiting on the expected RX DMA request.
    - \* Fixed the issue by enabling IntMajor interrupt for the command TCD and checking if there is a linked TCD to disable the TX DMA request in LPI2C\_MasterEDMACallback API.
- 2.1.3
  - Improvement:
    - \* Added LPI2C\_WATI\_TIMEOUT macro to allow the user to specify the timeout times for waiting flags in functional API and blocking transfer API.
    - \* Added LPI2C\_MasterTransferBlocking API.
- 2.1.2
  - Bug fix:
    - \* In LPI2C\_SlaveTransferHandleIRQ, reset the slave status to idle when stop flag is detected.
- 2.1.1
  - Bug fix:
    - \* Disabled auto-stop feature in eDMA driver. Previously, the auto-stop feature was enabled at transfer when transferring with stop flag. If the previous transfer was without stop flag, because the auto-stop feature is enabled, then when starting a new transfer with stop flag, the stop flag sends before starting the new transfer, and the start flag cannot successfully send, so the transfer cannot start.
    - \* Changed default slave configuration with address stall false.
- 2.1.0
  - API name change:
    - \* LPI2C\_MasterTransferCreateHandle -> LPI2C\_MasterCreateHandle.
    - \* LPI2C\_MasterTransferGetCount -> LPI2C\_MasterGetTransferCount.
    - \* LPI2C\_MasterTransferAbort -> LPI2C\_MasterAbortTransfer.
    - \* LPI2C\_MasterTransferHandleIRQ -> LPI2C\_MasterHandleInterrupt.
    - \* LPI2C\_SlaveTransferCreateHandle -> LPI2C\_SlaveCreateHandle.
    - \* LPI2C\_SlaveTransferGetCount -> LPI2C\_SlaveGetTransferCount.
    - \* LPI2C\_SlaveTransferAbort -> LPI2C\_SlaveAbortTransfer.
    - \* LPI2C\_SlaveTransferHandleIRQ -> LPI2C\_SlaveHandleInterrupt.
- 2.0.0
  - Initial version.

## LPSPi

The current LPSPi driver version is 2.0.3.

- 2.0.3
  - Bug fix:
    - \* Removed LPSPi\_Reset() from LPSPi\_MasterInit() and LPSPi\_SlaveInit(), because this API may glitch the slave select line. If needed, call this function manually.
- 2.0.2
  - New feature:
    - \* Added dummy data set up API to allow users to configure the dummy data to be transferred.
    - \* Enabled the 3-wire mode, SIN and SOUT pins can be configured as input/output pin.
- 2.0.1
  - Bug fix:
    - \* The clock source should be divided by the PRESCALE setting in LPSPi\_MasterSetDelayTimes function.
    - \* Fixed the bug that LPSPi\_MasterTransferBlocking function would hang in some corner cases.
  - Optimization:
    - \* Added #ifndef/#endif to allow user to change the default TX value at compile time.
- 2.0.0
  - Initial version.

## LPUART

The current LPUART driver version is 2.2.7.

- 2.2.7
  - Fixed issue for MISRA-2012 check.
    - \* Fixed rule-12.1, rule-17.7, rule-14.4, rule-13.3, rule-14.4, rule-10.4, rule-10.8, rule-10.3, rule-10.7, rule-10.1, rule-11.6, rule-13.5, rule-11.3, rule-13.2, rule-8.3.
- 2.2.6
  - Fixed repeated reading status register issue while dealing with the IRQ routine.
- 2.2.5
  - Do not set or clear the TIE/RIE bits when using LPUART\_EnableTxDMA() and LPUART\_EnableRxDMA().
- 2.2.4
  - Added hardware flow control function support.
  - Added idle line detected feature in LPUART\_TransferNonBlocking function. If an idle line was detected, a callback is triggered with status kStatus\_LPUART\_IdleLineDetected returned. This feature may be useful when the received Bytes is less than the expected receive data size. Before triggering the callback, data in the FIFO (if has FIFO) is read out, and all interrupts will not be disabled, except if the receive data size reaches 0.
  - Enabled the RX FIFO watermark function. With the idle line detected feature enabled, you can

set the watermark value to whatever you want (should be less than the RX FIFO size). Data is received and a callback is triggered when data receive is end.

- 2.2.3
  - Changed parameter type in LPUART\_RTOS\_Init() struct rtos\_lpuart\_config -> lpuart\_rtos\_config\_t.
  - Bug fix:
    - \* Disabled LPUART receive interrupt instead of disabling all NVIC when read data from ring buffer. Because the ring buffer is used, receive nonblocking disables all NVIC interrupts to protect the ring buffer. This has a negative effect to other IPS which are using the interrupt.
- 2.2.2
  - Added software reset feature support.
  - Added software reset API to LPUART\_Init().
- 2.2.1
  - Added separate RX/TX IRQ number support.
- 2.2.0
  - Added 7 data bits and MSB support.
- 2.1.1
  - Removed needless check of event flags and assert in LPUART\_RTOS\_Receive.
  - Always wait for RX event flag in LPUART\_RTOS\_Receive.
- 2.1.0
  - Update transactional APIs.

## **PIT**

The current PIT driver version is 2.0.2.

- 2.0.2
  - Bug fix:
    - \* Fixed MISRA-2012 issues.
      - Rule 10.1.
- 2.0.1
  - Bug fix:
    - \* Cleared timer enable bit for all channels in function PIT\_Init() to make sure all channels stay in disable status before setting other configurations.
    - \* Fixed MISRA-2012 rules.
      - Rule 14.4, rule 10.4.
- 2.0.0
  - Initial version.

## **PMU**

The current PMU driver version is 2.1.0.

- 2.1.0
  - Added feature macros for low power control APIs to support to conditional compile.
  - Renamed "PMU\_2P1EnablePullDown" to "PMU\_2P5EnablePullDown"
- 2.0.0
  - Initial version.

## **PWM**

The current PWM driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **QTMR**

The current QTMR driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **RTWDOG**

The current RTWDOG driver version is 2.1.0.

- 2.1.0
  - Added API to enable or disable the window mode.
  - Added API to convert a raw count value to millisecond.
  - Used AT\_QUICKACCESS\_SECTION\_CODE macro to decorate RTWDOG\_Init, copy this function from flash to QUICKACCESS section.
- 2.0.1
  - Fixed bug in the RTWDOG\_Init, added check of register's unlock status when configuring the RTWDOG in RTWDOG\_init.
- 2.0.0
  - Initial version.

## **SAI**

The current SAI driver version is 2.2.1.

- 2.2.1
  - Improvements:
    - \* Added mclk post divider support in function SAI\_SetMasterClockDivider.
    - \* Removed useless configuration code in SAI\_RxSetSerialDataConfig.

- Bug fix:
  - \* Fixed the SAI SDMA driver build issue caused by the wrong structure member name used in the function SAI\_TransferRxSetConfigSDMA/SAI\_TransferTxSetConfigSDMA.
  - \* Fixed BAD BIT SHIFT OPERATION issue caused by the FSL\_FEATURE\_SAI\_CHANNEL\_COUNTn.
  - \* Apply ERR05144: not set FCONT = 1 when TMR > 0, otherwise the TX may not work.
- 2.2.0
  - Improvements:
    - \* Add new APIs for parameters collection and user interfaces simplify: SAI\_Init SAI\_SetMasterClockConfig SAI\_TxSetBitClockRate SAI\_TxSetSerialDataConfig SAI\_TxSetFrameSyncConfig SAI\_TxSetFifoConfig SAI\_TxSetBitclockConfig SAI\_TxSetConfig SAI\_TxSetTransferConfig SAI\_RxSetBitClockRate SAI\_RxSetSerialDataConfig SAI\_RxSetFrameSyncConfig SAI\_RxSetFifoConfig SAI\_RxSetBitclockConfig SAI\_RXSetConfig SAI\_RxSetTransferConfig SAI\_GetClassicI2SConfig SAI\_GetLeftJustifiedConfig SAI\_GetRightJustifiedConfig SAI\_GetTDMConfig
- 2.1.9
  - Improvements:
    - \* Improved SAI driver comment for clock polarity.
    - \* Added enumeration for SAI for sample inputs on different edge.
    - \* Changed FSL\_FEATURE\_SAI\_CHANNEL\_COUNT to FSL\_FEATURE\_SAI\_CHANNEL\_COUNTn(base) for the difference between the different SAI instance.
    - \* Added new API: SAI\_TxSetBitClockDirection SAI\_RxSetBitClockDirection SAI\_RxSetFrameSyncDirection SAI\_TxSetFrameSyncDirection
- 2.1.8
  - Improvements:
    - \* Added feature macro test for the sync mode2 and mode 3.
    - \* Added feature macro test for masterClockHz in sai\_transfer\_format\_t.
- 2.1.7
  - Improvements:
    - \* Added feature macro test for the mclkSource member in sai\_config\_t.
    - \* Changed "FSL\_FEATURE\_SAI5\_SAI6\_SHARE\_IRQ" to "FSL\_FEATURE\_SAI\_SAI5\_SAI6\_SHARE\_IRQ".
    - \* Add #ifndef #endif check for SAI\_XFER\_QUEUE\_SIZE to allow redefinition.
  - Bug fix:
    - \* Fixed build error caused by feature macro test for mclkSource.
- 2.1.6
  - Improvement:
    - Added feature macro test for mclkSourceClockHz check.
    - Added bit clock source name for general devices.
  - Bug fix:
    - Fixed incorrect channel numbers setting while call RX/TX set format together.

**-2.1.5**

- Bug fix:
  - Corrected SAI3 driver IRQ handler name.
  - Added I2S4/5/6 IRQ handler.
  - Added base in handler structure to support different instances share one IRQ number.
- New features:
  - Updated SAI driver for MCR bit MICS.
  - Added 192 KHZ/384 KHZ in the sample rate enumeration.
  - Added multi FIFO interrupt/SDMA transfer support for TX/RX.
  - Added API to read/write multi FIFO data in a blocking method.
  - Added bclk bypass support when bclk is same with mclk.

**2.1.4**

- New feature:
  - Added API to enable/disable auto FIFO error recovery in platforms that support this feature.
  - Added API to set data packing feature in platform which support this feature.

**2.1.3**

- New feature:
  - Added feature to make I2S frame sync length configurable according to bitWidth.

**2.1.2**

- Bug fix:
  - Added 24-bit support for SAI eDMA transfer. All data shall be 32 bits for send/receive, as eDMA cannot directly handle 3 Byte transfer.

**2.1.1**

- Optimization:
  - Reduced code size while not using transactional API.

**2.1.0**

- API name change:
  - SAI\_GetSendRemainingBytes -> SAI\_GetSentCount.
  - SAI\_GetReceiveRemainingBytes -> SAI\_GetReceivedCount.
  - All transactional API name add "Transfer" prefix.
  - All transactional API use base and handle as input parameter.
  - Unify the parameter names.
- Bug fix:
  - Fixed WLC bug while reading TCSR/RCSR registers.
  - Fixed MOE enable flow issue, move MOE enable after MICS settings in SAI\_TxInit/SAI\_Rx-Init.

**2.0.0**

- Initial version.



## **SEMC**

The current SEMC driver version is 2.0.4.

- 2.0.4
  - Fixed the SEMC queueA and queueB weight configuration issue.
  - Fixed the wrong configuration of DBICR1 register in SEMC\_ConfigureDBI.
- 2.0.3
  - Added feature macro to control WDS&WDH bit setting for NOR synchronous transfer.
- 2.0.2
  - Changed SEMC NAND configuration structure and verify SEMC NAND related APIs.
  - Added extended SEMC clock enable.
- 2.0.1
  - Fixed data size mask configure in SEMC\_ConfigureIPCommand API.
  - Updated the command mode in IP command type.
- 2.0.0
  - Initial version.

## **SPDIF**

The current SPDIF driver version is 2.0.2.

- 2.0.2 Bug fix:
  - Corrected operator used for size value assertion in SPDIF\_ReadBlocking/SPDIF\_WriteBlocking.
- 2.0.1
  - Corrected the feature macro name used to define s\_edmaPrivateHandle.
- 2.0.0
  - Initial version.

## **SRC**

The current SRC driver version is 2.0.1.

- 2.0.1
  - Improvements: Updated SRC driver for adding SRC\_SRSR\_JTAG\_SW\_RST enumeration.
- 2.0.0
  - Initial version.

## **TEMPMON**

The current TEMPMON driver version is 2.0.0.

- 2.0.0
  - Initial version.

## USDHC

The current USDHC driver version is 2.2.8.

- 2.2.8
  - Fixed out-of bounds write in function USDHC\_ReceiveCommandResponse.
- 2.2.7
  - Added API USDHC\_GetEnabledInterruptStatusFlags and used in USDHC\_TransferHandleIRQ.
  - Removed useless member interruptFlags in usdhc\_handle\_t.
- 2.2.6
  - Added address align check for ADMA descriptor table address.
  - Changed USDHC\_ADMA1\_DESCRIPTOR\_MAX\_LENGTH\_PER\_ENTRY to (65536-4096) to make sure the data address is 4KB align for a transfer need more than one ADMA1 descriptor.
- 2.2.5
  - Fixed MDK 66-D warning.
- 2.2.4
  - Fixed issue where real clock frequency is mismatched with target clock frequency, which is caused by an incorrect prescaler calculation.
  - Added control macro to enable/disable the CLOCK code in current driver.
- 2.2.3
  - Fixed issue where AMDA did not disable with DMAEN clear.
  - Improved set clock function to check the output frequency range.
  - Dynamic set SDCLKFS during DDR enable or disable.
- 2.2.2
  - Improved read transfer cache maintain operation, combined clean and invalidated into one function.
- 2.2.1
  - Disabled the invalidate cache operation for tuning.
- 2.2.0
  - Improved USDHC to support MMC boot feature.
- 2.1.3
  - Fixed MISRA issue.
- 2.1.2
  - Fixed coverity issue.
  - Added base address and userData parameter for all callback functions.
- 2.1.1
  - Added cache maintain operation.
  - Added timeout status check for the DATA transfer which ignore error.
  - Added feature macro for SDR50/SDR104 mode.
  - Removed useless IRQ handler for different platform.
- 2.1.0
  - Integrated tuning into transfer function.
  - Added strobe DLL feature.
  - Added enableAutoCommand23 in data structure.

- Removed enable card clock function because the controller will handle the clock on/off.
- 2.0.0
  - Initial version.

## WDOG

The current WDOG driver version is 2.1.0.

- 2.1.0
  - New Feature:
    - \* Added new API "WDOG\_TriggerSystemSoftwareReset()" to allow user reset the system by software.
    - \* Added new API "WDOG\_TriggerSoftwareSignal()" to allow user trigger a WDOG\_B signal by software.
    - \* Removed the parameter "softwareAssertion" and "softwareResetSignal" out of the wdog\_config\_t structure.
    - \* Added new parameter "enableTimeOutAssert" to the wdog\_config\_t structure, with this parameter enabled, while the WDOG timeout occurred, a WDOG\_B signal will be asserted, this signal can be routed to external pin of the chip. Note that WDOG\_B signal remains asserted until a power-on reset (POR) occurs.
- 2.0.1
  - Added control macro to enable/disable the CLOCK code in current driver.
- 2.0.0
  - Initial version.

## XBARA

The current XBARA driver version is 2.0.4.

- 2.0.4
  - Improvement:
    - \* Optimized XBARA\_SetOutputSignalConfig.
- 2.0.3
  - Bug fixes:
    - \* Corrected configuration for function XBAR\_SetOutputSignalConfig.
- 2.0.2
  - Other changes:
    - \* Changed array clock name.
- 2.0.1
  - Bug fixes:
    - \* Fixed w1c bits for XBARA\_SetOutputSignalConfig function.
- 2.0.0
  - Initial version.

## **XBARB**

The current XBARB driver version is 2.0.1.

- 2.0.1
  - Bug fixes:
    - \* Corrected XBARB\_SetSignalsConnection function.
  - Other changes:
    - \* Changed array clock name.
- 2.0.0
  - Initial version.

## 2 Middleware Change Log

### emWin library

The current version of emWin is 5.48r.

### FatFs for MCUXpresso SDK

The current version is FatFs R0.13b\_rev0.

- R0.13b\_rev0
  - Upgraded to version 0.13b
- R0.13a\_rev0
  - Upgraded to version 0.13a. Added patch ff\_13a\_p1.diff.
- R0.12c\_rev1
  - Added NAND disk support.
- R0.12c\_rev0
  - Upgraded to version 0.12c and applied patches ff\_12c\_p1.diff and ff\_12c\_p2.diff.
- R0.12b\_rev0
  - Upgraded to version 0.12b.
- R0.11a
  - Added glue functions for low-level drivers (SDHC, SDSPI, RAM, MMC). Modified diskio.c.
  - Added RTOS wrappers to make FatFs thread safe. Modified syscall.c.
  - Renamed ffconf.h to ffconf\_template.h. Each application should contain its own ffconf.h.
  - Included ffconf.h into diskio.c to enable the selection of physical disk from ffconf.h by macro definition.
  - Conditional compilation of physical disk interfaces in diskio.c.

### lwIP for MCUXpresso SDK

The current version of lwIP is based on lwIP 2.1.2 and lwIP-contrib 2.1.0.

- 2.1.2\_rev2
  - Bug fixes:
    - \* Fixed lwiperf\_abort() in lwiperf.c to correctly close connections and free resources
- 2.1.2\_rev1
  - New features:
    - \* Ported lwIP 2.1.2 (2018-11-22, SHA-1: 159e31b689577dbf69cf0683bbaffbd71fa5ee10) to KSDK 2.0.0.
    - \* Ported lwIP-contrib 2.1.0 (2018-09-24, SHA-1: 35b011d4cf4c4b480f8859c456587a884ec9d287) to KSDK 2.0.0.
- 2.0.3\_rev1
  - New features:

- \* Ported lwIP 2.0.3 (2017-09-15, SHA-1: 92f23d6ca0971a32f2085b9480e738d34174417b) to KSDK 2.0.0.
- 2.0.2\_rev1
  - New features:
    - \* Ported lwIP 2.0.2 (2017-03-13, SHA-1: c0862d60746e2d1ceae69af4c6f24e469570ecef) to KSDK 2.0.0.
- 2.0.0\_rev3
  - New features:
    - \* Ported lwIP 2.0.0 (2016-11-10, SHA-1: 216bf89491815029aa15463a18744afa04df58fe) to KSDK 2.0.0.
- 2.0.0\_rev2
  - New features:
    - \* Ported lwIP 2.0.0 RC2 (2016-08-08, SHA-1: b1dfd00f9233d124514a36a8c8606990016f2ad4) to KSDK 2.0.0.
- 2.0.0\_rev1
  - New features:
    - \* Ported lwIP 2.0.0 RC0 (2016-05-26) to KSDK 2.0.0.
    - \* Changed lwIP bare-metal examples to use poll-driven approach instead of interrupt-driven one.
- 1.4.1\_rev2
  - New features:
    - \* Enabled critical sections in lwIP.
  - Bug fixes:
    - \* Fixed default lwIP packet-buffer size to be able to accept a maximum size frame from the ENET driver.
    - \* Fixed possible drop of multi-frame packets during transmission.
- 1.4.1\_rev1
  - New features:
    - \* Ported lwIP 1.4.1 to KSDK 2.0.0.

## **mbedTLS for MCUXpresso SDK**

The current version of mbedTLS is based on mbedTLS 2.13.1 released 2018-09-06.

- 2.13.1\_rev3
  - Bug fixes:
    - \* Forced to align AES\_CCM and AES\_GCM self-test keys to fix unaligned key issue when using hardware acceleration.
- 2.13.1\_rev2
  - Bug fixes:
    - \* Disabled default hardware acceleration of SHA in parallel with AES.
- 2.13.1\_rev1
  - Bug fixes:
    - \* Fixed incorrect macro check when skipping AES-192 or AES-256.

- 2.13.1
  - New features:
    - \* Ported mbedTLS 2.13.1 to SDK.
- 2.12.0\_rev1
  - New features:
    - \* Added support for NIST P-256 elliptic curve with CASPER driver.
- 2.12.0
  - New features:
    - \* Ported mbedTLS 2.12.0 to SDK.
- 2.9.0\_rev2
  - New features:
    - \* Added support for Hashcrypt driver.
- 2.9.0\_rev1
  - New features:
    - \* Added support for CASPER driver.
- 2.9.0
  - New features:
    - \* Ported mbedTLS 2.9.0 to SDK.
- 2.6.0\_rev2
  - Bug fixes:
    - \* ssl\_cookie.c now uses SHA256 for COOKIE\_MD (instead of original SHA224). Some hardware crypto acceleration (such as CAU3) does not support SHA224 but all support SHA256.
- 2.6.0\_rev1
  - Bug fixes:
    - \* ksdk\_mbedtls.c bignum functions now read sign of input mbedtls\_mpi at beginning of functions to properly support in place computations (when output bignum is the same as one of input bignums). Affected functions: mbedtls\_mpi\_mul\_mpi(), mbedtls\_mpi\_mod\_mpi(), ecp\_mul\_comb().
- 2.6.0
  - New features:
    - \* Ported mbedTLS 2.6.0 to KSDK.
    - \* Added MBEDTLS\_FREESCALE\_FREERTOS\_CALLOC\_ALT to allow alternate implementation of pvPortCalloc() when using .c.
- 2.5.1\_rev1
  - New features:
    - \* Added support for DCP driver.
- 2.5.1
  - New features:
    - \* Ported mbedTLS 2.5.1 to SDK.
- 2.4.2\_rev2
  - New features:
    - \* Added Curve25519 support for CAU3.
    - \* Added MBEDTLS\_ECP\_MUL\_MXZ\_ALT configuration parameter enabling overloading of ecp\_mul\_mxz().

- 2.4.2\_rev1
  - New features:
    - \* Added support for CAU3 driver.
    - \* Added new files:
      - \* .c - contains regular software implementation of DES algorithm with added MBEDTLS\_DES3\_SETKEY\_DEC\_ALT and MBEDTLS\_DES3\_SETKEY\_ENC\_ALT config parameters.
      - \* .h - contains modified mbedtls\_des\_context and mbedtls\_des3\_context structures.
    - \* Added MBEDTLS\_DES3\_SETKEY\_DEC\_ALT configuration parameter enabling reloading of mbedtls\_des3\_set2key\_dec() and mbedtls\_des3\_set3key\_dec().
    - \* Added MBEDTLS\_DES3\_SETKEY\_ENC\_ALT configuration parameter enabling reloading of mbedtls\_des3\_set2key\_enc() and mbedtls\_des3\_set3key\_enc().
- 2.4.2
  - New features:
    - \* Ported mbedTLS 2.4.2 to KSDK 2.0.0.
    - \* Added CRYPTO\_InitHardware() function.
    - \* Added new file:
      - .h - contains declaration of CRYPTO\_InitHardware() function and should be included in applications.
- 2.3.0\_rev1
  - New features:
    - \* Added support for CAAM driver.
    - \* In LTC-specific wrapper, allocate temporary integers from heap in one large block.
- 2.3.0
  - New features:
    - \* Ported mbedTLS 2.3.0 to KSDK 2.0.0.

## 2.2.1

- New features:
  - Ported mbedTLS 2.2.1 to KSDK 2.0.0.
  - Added support of MMCAU cryptographic acceleration module. Accelerated MD5, SHA, AES, and DES.
  - Added support of LTC cryptographic acceleration module. Accelerated AES, DES, and PKHA.
  - Added new files:
    - .c - alternative implementation of cryptographic algorithm functions using LTC and MMCAU module drivers.
    - .h - configuration settings used by mbedTLS KSDK bare metal examples.
  - Added mbedTLS KSDK bare-metal examples:
    - \* <board name> - KSDK mbedTLS benchmark application.
    - \* <board name> - KSDK mbedTLS self-test application.
  - Added MBEDTLS\_GCM\_CRYPT\_ALT configuration parameter enabling reloading of mbedtls\_gcm\_crypt\_and\_tag().
  - Added MBEDTLS\_ECP\_MUL\_COMB\_ALT to enable alternate implementation of ecp\_mul\_comb().



- Added MBEDTLS\_ECP\_ADD\_ALT configuration parameter enabling reloading of ecp\_add().
- Added MBEDTLS\_DES\_SETKEY\_DEC\_ALT configuration parameter enabling reloading of mbedtls\_des\_setkey\_dec(), mbedtls\_des3\_set2key\_dec() and mbedtls\_des3\_set3key\_dec().
- Added MBEDTLS\_DES\_SETKEY\_ENC\_ALT configuration parameter enabling reloading of mbedtls\_des\_setkey\_enc(), mbedtls\_des3\_set2key\_enc() and mbedtls\_des3\_set3key\_enc().
- Added MBEDTLS\_DES\_CRYPT\_CBC\_ALT configuration parameter enabling reloading of mbedtls\_des\_crypt\_cbc().
- Added MBEDTLS\_DES3\_CRYPT\_CBC\_ALT configuration parameter enabling reloading of mbedtls\_des3\_crypt\_cbc().
- Added MBEDTLS\_AES\_CRYPT\_CBC\_ALT configuration parameter enabling reloading of mbedtls\_aes\_crypt\_cbc().
- Added MBEDTLS\_AES\_CRYPT\_CTR\_ALT configuration parameter enabling reloading of mbedtls\_aes\_crypt\_ctr().
- Added MBEDTLS\_CCM\_CRYPT\_ALT configuration parameter enabling reloading of mbedtls\_ccm\_encrypt\_and\_tag() and mbedtls\_ccm\_auth\_decrypt().
- Added MBEDTLS\_MPI\_ADD\_ABS\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_add\_abs().
- Added MBEDTLS\_MPI\_SUB\_ABS\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_sub\_abs().
- Added MBEDTLS\_MPI\_EXP\_MOD\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_exp\_mod().
- Added MBEDTLS\_MPI\_MUL\_MPI\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_mul\_mpi().
- Added MBEDTLS\_MPI\_MOD\_MPI\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_mod\_mpi().
- Added MBEDTLS\_MPI\_GCD\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_gcd().
- Added MBEDTLS\_MPI\_INV\_MOD\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_inv\_mod().
- Added MBEDTLS\_MPI\_IS\_PRIME\_ALT configuration parameter enabling reloading of mbedtls\_mpi\_is\_prime().
- Added encrypt/decrypt mode to mbedtls\_des\_context and mbedtls\_des3\_context structure.
- Added carriage return "\n" for mbedtls\_printf() in self test functions.

## **SDMMC**

The current driver version is 2.2.11.

- 2.2.11
  - Bug fixes:
    - \* Fixed NULL pointer dereference issue when calling function SDMMCHOST\_Card-DetectInit in host adaptor layer.
    - \* Fixed logical dead code issue in SDMMC\_SwitchToVoltage function.

- 2.2.10
  - Bug fixes:
    - \* Added NULL pointer check for USDHC FreeRTOS adaptor transfer complete. callback.
    - \* Added event value check for all the FreeRTOS event to fix program hang when a card event occurs before created.
- 2.2.9
  - Improvements:
    - \* Added NULL pointer check for sdmmhostcard\_usr\_param\_t member CD in card detect callback to avoid memory corruption.
    - \* Added card voltage switch function in sdmmhostcard\_usr\_param\_t to allow application register card signal line voltage switch function.
  - Bug fixes:
    - \* Fixed host FreeRTOS adaptor and polling adaptor, where they cannot detect card insert bug for USDHC.
    - \* Fixed SDHC host layer build issue and typo issue.
- 2.2.8
  - Improvement:
    - \* Updated SDMMC to support SDIO interrupt.
- 2.2.7
  - Bug fixes:
    - \* Fixed MDK 66-D warning.
- 2.2.6
  - Improvements:
    - \* Removed some SoC-specific header files from porting layer.
    - \* Saved MMC OCR registers while sending CMD1 with argument 0.
  - Bug fixes:
    - \* Added MMC\_PowerOn function in which there is delay function after powerup SD Card. Otherwise, the card may init fail.
- 2.2.5
  - New features:
    - \* Added SD\_ReadStatus API to get 512-bit SD status.
    - \* Added error log support in SD Card functions.
    - \* Added SDMMC\_ENABLE\_SOFTWARE\_TUNING to enable/disable software tuning. It is disabled by default.
    - \* Added error procedure in the transfer function to improve stability.
    - \* Removed deprecated GPIO API in host layer.
- 2.2.4
  - Bug fix:
    - \* Fixed DDR mode data sequence miss issue, which is caused by NIBBLE\_POS.
  - New features:
    - \* Increased g\_sdmmc 512 Bytes to improve performance when application uses a non-word align data buffer address.
    - \* Used OCR access mode bits to determine the MMC Card high capacity flag.
    - \* Enabled auto CMD12 for SD read/write.
    - \* Disabled DDR mode frequency multiply by 2.

- 2.2.3
  - Bug fix:
    - \* Added response check for send operation condition command. If not checked, the card may occasionally init fail.
- 2.2.2
  - Moved set card detect priority operation before enable IRQ.
- 2.2.1
  - New features:
    - \* Improved MMC Boot feature.
    - \* Kept SD\_Init/SDIO\_Init function for forward compatibility.
- 2.2.0
  - New features:
    - \* Separated the SD/MMC/SDIO init API to xxx\_CardInit/xxx\_HostInit.
    - \* Allowed user register card detect callback, select card detect type, and determine the card detect timeout value.
    - \* Allowed user register the power on/off function, and determine the power on/off delay time.
    - \* SD\_Init/SDIO\_Init is deprecated in the next version.
    - \* Added write complete wait operation for MMC\_Write to fix command timeout issue.
- 2.1.6
  - Enhanced SD IO default driver strength.
- 2.1.5
  - Fixed coverity issue.
  - Fixed SD v1.x card write fail issue. It was caused by the block length set error.
- 2.1.4
  - Miscellaneous:
    - \* Added Host reset function for card re-initialization.
    - \* Added Host\_ErrorRecovery function for host error recovery procedure.
    - \* Added cache maintain operation
    - \* Added HOST\_CARD\_INSERT\_CD\_LEVEL to improve compatibility.
  - Bug fix:
    - \* Fixed card cannot detect dynamically.
- 2.1.3
  - Bug fix:
    - \* Non high-speed sdcard init fail at switch to high speed.
  - Miscellaneous:
    - \* Optimized tuning/mmc switch voltage/mmc select power class/mmc select timing function.
    - \* Added strobe dll for mmc HS400 mode.
    - \* Added Delay for SDCard power up.
- 2.1.2
  - New features:
    - \* Added fsl\_host.h to provide prototype to adapt different controller IPs(SDHC/SDIF).
    - \* Added adaptor code in SDMMC/Port folder to adapt different host controller IPs with different. transfer modes(interrupt/polling/freertos). Application includes a different adaptor

- code to make application more simple.
- \* Adaptor code provides HOST\_Init/HOST\_Deinit/CardInsertDetect. APIs to do host controller initialize and transfer function configuration. SDMMC card stack uses adaptor code inside stack to wait card insert and configure host when calling card init APIs (SD\_Init/MMC\_Init/SDIO\_Init).
- \* This change requires the user to include host adaptor code into the application. If not changed, link errors saying it cannot find the definition of HOST\_Init/HOST\_Deinit/CardInsertDetect appear.
- New features: Improved SDMMC to support SD v3.0 and eMMC v5.0.
- Bug fix:
  - \* Fixed incorrect comparison between count and length in MMC\_ReadBlocks/MMC\_WriteBlocks.
- 2.1.1
  - Bug fix:
    - \* Fixed the block range boundary error when transferring data to MMC card.
    - \* Fixed the bit mask error in the SD card switch to high speed function.
  - Other changes:
    - \* Added error code to indicate that SDHC ADMA1 transfer type is not supported yet.
    - \* Optimized the SD card initialization function.
- 2.1.0
  - Bug fix:
    - \* Changed callback mechanism when sending a command.
    - \* Fixed low performance issue when transferring data.
  - Other changes:
    - \* Changed the name of some error codes returned by internal function.
    - \* Merged all host related attributes to one structure.
    - \* Optimize the function of setting maximum data bus width for MMC card.

## SDIO

The current driver version is 2.2.11.

- 2.2.11
  - Bug fix:
    - \* Added check card async interrupt capability in function SDIO\_GetCardCapability.
    - \* Fixed OUT OF BOUNDS access in function SDIO\_IO\_Transfer.
- 2.2.10
  - Bug fix:
    - \* Fixed SDIO card driver get wrong IO number when the card IO number is bigger than 2.
  - New feature:
    - \* Added SDIO 3.0 support.
    - \* Added API SDIO\_IO\_RW\_Direct for direct read/write card register access.
- 2.2.9
  - Improvement:

- \* Added API SDIO\_SetIOIRQHandler/SDIO\_HandlePendingIOInterrupt to handle multi IO pending IRQ.
- 2.2.8
  - Improvement:
    - \* Updates SDMMC to support SDIO interrupt.
    - \* Added API SDIO\_GetPendingInterrupt to get the pending IO interrupt.
- 2.2.7
  - Bug fix:
    - \* Fixed MDK 66-D warning.
- 2.2.6
  - New features:
    - \* Added a unified transfer interface for SDIO.
  - Bug fix:
    - \* Wrong pointer address used by SDMMCHOST\_Init.
- 2.1.5
  - Bug fix:
    - \* Improved SDIO card init sequence and added retry option for SDIO\_SwitchToHighSpeed function.
- 2.1.4
  - Miscellaneous:
    - \* Added Go\_Idle function for SDIO card.
- 2.0.0
  - Initial version.

## **SDSPI**

The current SDSPI driver version is 2.1.4.

- 2.1.4
  - Bug fix:
    - \* Fixed MDK 66-D warning.
- 2.1.3
  - Improved SDSPI code size and performance.
- 2.0.0
  - Initial version.

## **USB stack for MCUXpresso SDK**

The current version of USB stack is 2.2.0.

- 2.2.0
  - New features:
    - \* Added device DFU support.
    - \* Supports OM13790DOCK on LPCXpresso54018.

- \* Added multiple logical unit support in MSC class driver, updated `usb_device_lba_information_struct_t` to support this.
  - \* Supports multiple transfers for host ISO on IP3516HS.
- Bug fixes:
  - \* Fixed device ip3511 prime data length than maxpacket size issue.
  - \* Initialized interval attribute in `usb_device_endpoint_struct_t/usb_device_endpoint_init_struct_t`.
  - \* Removed unnecessary header file in device CDC class driver, removed unnecessary `usb_echo`, and added `DEBUG` macro for necessary `usb_echo` in device CDC class driver.
  - \* Fixed device IP3511HS unfinished interrupt transfer missing issue.
- 2.1.0
  - New features:
    - \* Added host RNDIS support. example: `lwip_dhcp_usb`
    - \* Enabled USB 3.0 support on device stack.
    - \* Power Delivery feature: Added OM13790HOST support. Added auto policy feature. Printed e-marked cable information.
- 2.0.1
  - Bug fixes:
    - \* Fixed some USB issues: Fixed MSC CV test failed in MSC examples.
    - \* Changed audio codec interfaces.
- 2.0.0
  - New features:
    - \* PTN5110N support.
  - Bug fix:
    - \* Added some comments, fixed some minor USB issues.
- 1.9.0
  - New features:
    - \* Examples:
      - `usb_pd_alt_mode_dp_host`
- 1.8.2
  - Updated license.
- 1.8.1
  - Bug fix:
    - \* Verified some hardware issues, support `aruba_flashless`.
- 1.8.0
  - New features:
    - \* Examples:
      - `usb_device_composite_cdc_vcom_cdc_vcom`
      - `usb_device_composite_hid_audio_unified`
      - `usb_pd_sink_battery`
      - Changed `usb_pd_battery` to `usb_pd_charger_battery`.
- Bug fix:
  - Code clean up, removed some irrelevant code.
- 1.7.0

- New features:
    - \* USB PD stack support.
  - Examples:
    - \* usb\_pd
    - \* usb\_pd\_battery
    - \* usb\_pd\_source\_charger
- 1.6.3
  - Bug fix: -IP3511\_HS driver control transfer sequence issue, enabled 3511 ip cv test.
- 1.6.2
  - New features:
    - \* Multi instance support.
- 1.6.1
  - New features:
  - Changed the struct variable address method for device\_video\_virtual\_camera and host\_phdc\_manager.
- 1.6.0
  - New features:
    - \* Supported Device Charger Detect feature on usb\_device\_hid\_mouse.
- 1.5.0
  - New features:
    - \* Supported controllers
      - OHCI (Full Speed, Host mode)
      - IP3516 (High Speed, Host mode)
      - IP3511 (High Speed, Device mode)
    - \* Examples:
      - usb\_lpm\_device\_hid\_mouse
      - usb\_lpm\_device\_hid\_mouse\_lite
      - usb\_lpm\_host\_hid\_mouse
- 1.4.0
  - New features:
    - \* Examples:
      - usb\_device\_hid\_mouse/freertos\_static
      - usb\_suspend\_resume\_device\_hid\_mouse\_lite
- 1.3.0
  - New features:
    - \* Supported roles
      - OTG
    - \* Supported classes
      - CDC RNDIS
    - \* Examples
      - usb\_otg\_hid\_mouse
      - usb\_device\_cdc\_vnic
      - usb\_suspend\_resume\_device\_hid\_mouse
      - usb\_suspend\_resume\_host\_hid\_mouse
- 1.2.0

- New features:
  - \* Supported controllers
    - LPC IP3511 (Full Speed, Device mode)
- 1.1.0
  - Bug fix:
    - \* Fixed some issues in USB certification.
    - \* Changed VID and Manufacturer string to NXP.
  - New features:
    - \* Supported classes
      - Pinter
    - \* Examples:
      - usb\_device\_composite\_cdc\_msc\_sdcard
      - usb\_device\_printer\_virtual\_plain\_text
      - usb\_host\_printer\_plain\_text
- 1.0.1
  - Bug fix:
    - \* Improved the efficiency of device audio speaker by changing the transfer mode from interrupt to DMA, thus providing the ability to eliminate the periodic noise.
- 1.0.0
  - New features:
    - \* Supported roles
      - Device
      - Host
    - \* Supported controllers:
      - KHCI (Full Speed)
      - EHCI (High Speed)
    - \* Supported classes:
      - AUDIO
      - CCID
      - CDC
      - HID
      - MSC
      - PHDC
      - VIDEO
    - \* Examples:
      - usb\_device\_audio\_generator
      - usb\_device\_audio\_speaker
      - usb\_device\_ccid\_smart\_card
      - usb\_device\_cdc\_vcom
      - usb\_device\_cdc\_vnic
      - usb\_device\_composite\_cdc\_msc
      - usb\_device\_composite\_hid\_audio
      - usb\_device\_composite\_hid\_mouse\_hid\_keyboard
      - usb\_device\_hid\_generic
      - usb\_device\_hid\_mouse



- usb\_device\_msc\_ramdisk
- usb\_device\_msc\_sdcard
- usb\_device\_phdc\_weighscale
- usb\_device\_video\_flexio\_ov7670
- usb\_device\_video\_virtual\_camera
- usb\_host\_audio\_speaker
- usb\_host\_cdc
- usb\_host\_hid\_generic
- usb\_host\_hid\_mouse
- usb\_host\_hid\_mouse\_keyboard
- usb\_host\_msdc\_command
- usb\_host\_msdc\_fatfs
- usb\_host\_phdc\_manager
- usb\_keyboard2mouse
- usb\_pin\_detect\_hid\_mouse

## **QCA WiFi**

The current version is 2.0.0.

- 2.0.0
  - Initial version.
    - \* Added QCA WiFi, ported from SDK 1.3, synchronized with latest MQX Qualcomm v3.-3.5.
  - Known issues:
    - \* Low power mode may not work, requires further investigation.
    - \* DHCP request requires a timeout to retrieve valid data.

### 3 RTOS Change Log

#### FreeRTOS for MCUXpresso SDK.

The current version is Amazon-FreeRTOS 1.4.0 Original package is available at [github.com/aws/amazon-freertos](https://github.com/aws/amazon-freertos).

- 1.4.7\_rev0
  - New features:
    - \* Add optional allocation scheme heap\_useNewlib.c by D. Nadler.
    - \* Enable task aware debugging for cm33 platforms
    - \* Move tickless implementation to application layer
  - Other changes:
    - \* Fix other build warnings, errors
- 1.4.6\_rev0
  - New features:
    - \* Update support of CM33 port with Trustzone, MPU, FPU support
    - \* Add support for AWS test for Cypress WiFi
    - \* Use lwip netif api to avoid lwIP raw API calls outside of tcpip thread in aws\_wifi.c
  - Other changes:
    - \* Fix issues with mflash driver
    - \* Fix other build warnings, errors
- 1.4.0\_rev1
  - New features:
    - \* Add implementation of vTaskEndScheduler for CM0 GCC port.
    - \* Support for CM33, CM33F architectures based on CM3, CM4F ports
- 1.4.0\_rev0
  - New features:
    - \* Support for pkcs11 for several platforms, secure element host library under pkcs11/portable/nxp folder
    - \* Lwip, wifi\_qca support for secure\_sockets in secure\_sockets/portable/nxp folder
    - \* Flash driver support for several platforms in third\_party/mcu\_vendor/nxp folder
    - \* Generic support for aws\_wifi under wifi/portable/nxp/common folder
  - Other changes:
    - \* Fix several build warnings, errors

Updates applied to FreeRTOS kernel up to version 10.0.0 (up to Amazon - FreeRTOS merge). New kernel related changes will be described in section above as part of AWS package.

- 9.0.0\_rev3
  - New features:
    - \* Tickless idle mode support for Cortex-A7. Add fsl\_tickless\_epit.c and fsl\_tickless\_generic.h in portable/IAR/ARM\_CA9 folder.
    - \* Enabled float context saving in IAR for Cortex-A7. Added configUSE\_TASK\_FPU\_SUPPORT macros. Modified port.c and portmacro.h in portable/IAR/ARM\_CA9 folder.

- Other changes:
  - \* Transformed ARM\_CM core specific tickless low power support into generic form under freertos/Source/portable/low\_power\_tickless/.
- 9.0.0\_rev2
  - New features:
    - \* Enabled MCUXpresso thread aware debugging. Add freertos\_tasks\_c\_additions.h and configINCLUDE\_FREERTOS\_TASK\_C\_ADDITIONS\_H and configFRTOS\_MEMORY\_SCHEME macros.
- 9.0.0\_rev1
  - New features:
    - \* Enabled -fno-plt optimization in GCC by adding **attribute((used))** for vTaskSwitchContext.
    - \* Enabled KDS Task Aware Debugger. Apply FreeRTOS patch to enable configRECORD\_STACK\_HIGH\_ADDRESS macro. Modified files are task.c and FreeRTOS.h.
- 9.0.0\_rev0
  - New features:
    - \* Example freertos\_sem\_static.
    - \* Static allocation support RTOS driver wrappers.
  - Other changes:
    - \* Tickless idle rework. Support for different timers is in separated files (fsl\_tickless\_systick.c, fsl\_tickless\_lptmr.c).
    - \* Removed configuration option configSYSTICK\_USE\_LOW\_POWER\_TIMER. Low power timer is now selected by linking of appropriate file fsl\_tickless\_lptmr.c.
    - \* Removed configOVERRIDE\_DEFAULT\_TICK\_CONFIGURATION in RVDS port. Use of **attribute((weak))** is the preferred solution. Not same as \_weak!
- 8.2.3
  - New features:
    - \* Tickless idle mode support.
    - \* Added template application for Kinetis Expert (KEx) tool (template\_application).
  - Other changes:
    - \* Folder structure reduction. Keep only Kinetis related parts.

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