

# **MCUXpresso SDK Release Notes**

## **Supporting TWR-KV46F150M and HVP-KV46F150M**



# Contents

**Chapter 1 Overview..... 3**

**Chapter 2 MCUXpresso SDK..... 4**

**Chapter 3 Development tools..... 5**

**Chapter 4 Supported development systems.....6**

**Chapter 5 Release contents.....7**

**Chapter 6 MCUXpresso SDK release package.....8**

**Chapter 7 MISRA compliance..... 9**

**Chapter 8 Known issues..... 12**

# Chapter 1

## Overview

The MCUXpresso Software Development Kit (SDK) is a collection of software enablement for Microcontrollers that includes peripheral drivers, high-level stacks including USB and lwIP, integration with WolfSSL and mbed TLS cryptography libraries, other middleware packages, such as multicore support and FatFs, and integrated RTOS support for FreeRTOS™ OS. In addition to the base enablement, the MCUXpresso SDK is augmented with demo applications and driver example projects, and API documentation to help the customers quickly leverage the support of the MCUXpresso SDK.

For the latest version of this and other MCUXpresso SDK documents, see the MCUXpresso SDK homepage [MCUXpresso-SDK: Software Development Kit](#).

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### NOTE

See the attached Change Logs section at the end of this document to reference the device-specific driver logs, middleware logs, and RTOS log.

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## Chapter 2

# MCUXpresso SDK

As part of the MCUXpresso software and tools, MCUXpressoSDK is the evolution of Kinetis SDK v2.3.0, includes support for both LPC and i.MX System-on-Chips (SoC). The same drivers, APIs, and middleware are still available with support for Kinetis, LPC, and i.MX silicon. The MCUXpresso SDK adds support for the MCUXpresso IDE, an Eclipse-based toolchain that works with all MCUXpresso SDKs. Easily import your SDK into the new toolchain to have access to all of the available components, examples, and demos for your target silicon. In addition to the MCUXpresso IDE, support for the MCUXpresso Config Tools allows for easy cloning of existing SDK examples and demos, allowing users to easily leverage the existing software examples provided by the SDK for their own projects.

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### NOTE

In order to maintain compatibility with legacy FSL code, the filenames and source code in MCUXpresso SDK containing the legacy Freescale prefix 'FSL' has been left as is. The 'FSL' prefix has been redefined as the NXP Foundation Software Library.

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# Chapter 3

## Development tools

The MCUXpresso SDK was compiled and tested with these development tools:

- IAR Embedded Workbench for Arm version 8.32.1
- MDK-Arm Microcontroller Development Kit (Keil)<sup>®</sup> 5.26
- Makefiles support with GCC revision 7-2018-q2-update from Arm Embedded
- MCUXpresso IDE v10.3.0

## Chapter 4

# Supported development systems

This release supports boards and devices listed in this table. Boards and devices in boldface were tested in this release:

**Table 1. Supported MCU devices and development boards**

Development boards	MCU devices
<b>TWR-KV46F150M, HVP-KV46F150M</b>	<b>MKV46F256VLL16</b> , MKV46F256VLH16, MKV46F128VLH16, MKV46F128VLL16, MKV44F256VLH16, MKV44F128VLF16, MKV44F256VLL16, MKV44F128VLH16, MKV44F128VLL16, MKV44F64VLH16, MKV44F64VLF16, MKV42F256VLH16, MKV42F256VLL16, MKV42F128VLF16, MKV42F128VLH16, MKV42F128VLL16, MKV42F64VLH16, MKV42F64VLF16

# Chapter 5

## Release contents

This table provides an overview of the MCUXpresso SDK release package contents and locations.

**Table 2. Release contents**

Deliverable	Location
Boards	<install_dir>/boards
TinyCBOR	<install_dir>/rtos/amazon-freertos/lib/third_party/tinycbor
Demo applications	<install_dir>/boards/<board_name>/demo_apps
Driver examples	<install_dir>/boards/<board_name>/driver_examples
CMSIS driver examples	<install_dir>/boards/<board_name>/cmsis_driver_examples
<install_dir>/boards/<board_name>/aws_examples	
RTOS examples	<install_dir>/boards/<board_name>/rtos_examples
Documentation	<install_dir>/docs
Middleware	<install_dir>/middleware
Driver, SoC header files, extension header files and feature header files, utilities	<install_dir>/devices/<device_name>
Cortex Microcontroller Software Interface Standard (CMSIS) ARM Cortex®-M header files, DSP library source	<install_dir>/CMSIS
Peripheral Drivers	<install_dir>/devices/<device_name>/drivers
CMSIS drivers	<install_dir>/devices/<device_name>/cmsis_drivers
Utilities such as debug console	<install_dir>/devices/<device_name>/utilities
RTOS Kernel Code	<install_dir>/rtos
Tools	<install_dir>/tools
segger_systemview	<install_dir>/boards/<board>/rtos_examples/visualization/freertos_segger_sysview
percepio_snapshot	<install_dir>/boards/<board>/rtos_examples/visualization/freertos_percepio_snapshot

# Chapter 6

## MCUXpresso SDK release package

The MCUXpresso SDK release package contents are aligned with the silicon subfamily it supports. This includes the boards, CMSIS, devices, documentation, middleware, and RTOS support.

### 6.1 Device support

The device folder contains all available software enablement for the specific System-on-Chip (SoC) subfamily. This folder includes clock-specific implementation, device register header file, device register feature header file, CMSIS derived device SVD, and the system configuration source files. Included with the standard SoC support are folders containing peripheral drivers, toolchain support, and a simple debug console.

The device-specific header files provide a direct access to the MCU peripheral registers. The device header file provides an overall SoC memory mapped register definition. In addition to the overall device memory mapped header file, the MCUXpresso SDK also includes the feature header file for each peripheral instantiated on the SoC.

The toolchain folder contains the startup code and linker files for each supported toolchain. The startup code is a CMSIScompliant startup that efficiently transfers the code execution to the main() function.

#### 6.1.1 Board support

The boards folder provides the board-specific demo applications, driver examples, RTOS, and middleware examples.

#### 6.1.2 Demo applications and other examples

The demo applications demonstrate the usage of the peripheral drivers to achieve a system level solution. Each demo application contains a readme file that describes the operation of the demo and required setup steps.

The driver examples demonstrate the capabilities of the peripheral drivers. Each example implements a common use case to help demonstrate the driver functionality.

### 6.2 Middleware

#### 6.2.1 RTOS

The MCUXpresso SDK is integrated with FreeRTOS OS.

#### 6.2.2 CMSIS

The MCUXpresso SDK is shipped with the standard CMSIS development pack, including the prebuilt libraries.



# Chapter 7

## MISRA compliance

All MCUXpresso SDK drivers and USB stack comply to MISRA 2012 rules with the following exceptions.

**Table 3. MISRA exceptions**

Exception Rules	Description
Directive 4.4	Sections of code should not be commented out.
Directive 4.5	Identifiers in the same name space with overlapping visibility should be typographically unambiguous.
Directive 4.6	Typedef that indicate size and signedness should be used in place of the basic numerical type.
Directive 4.8	If a pointer to a structure or union is never dereferenced within a transaction unit then the implementation of the object should be hidden.
Directive 4.9	A function should be used in preference to a function like macro where they are interchangeable.
Directive 4.10	Precautions shall be taken in order to prevent the contents of a header file being included more than once.
Directive 4.11	The validity of values passed to library functions shall be checked.
Rule 2.3	A project should not contain unused type declarations.
Rule 2.4	A project should not contain unused tag declarations.
Rule 2.5	A project should not contain unused macro declarations.
Rule 2.7	There should be no unused parameters in functions.
Rule 3.1	The character sequences <code>/*</code> and <code>//</code> shall not be used within a comment.
Rule 5.1	External identifiers shall be distinct.
Rule 5.3	An identifier declared in an inner scope shall not hide an identifier declared in an outer scope.
Rule 5.7	A tag name shall be a unique identifier.
Rule 5.9	Identifiers that define objects or functions with external linkage shall be unique.
Rule 8.13	A pointer should point to a const-qualified type whenever possible.
Rule 8.3	All declarations of an object or function shall use the same names and type qualifiers.
Rule 8.6	An identifier with external linkage shall have exactly one external definition.
Rule 8.7	Octal constants shall not be used.

*Table continues on the next page...*

**Table 3. MISRA exceptions (continued)**

Rule 8.9	A object should be defined at block scope if its identified only appears in a single function.
Rule 10.1	Operands shall not be of an inappropriate essential type.
Rule 10.3	The value of an expression shall not be assigned to an object with a narrower essential type of a different essential type category.
Rule 10.4	Both operands of an operator in which the usual arithmetic conversions are performed shall have the same essential type category.
Rule 10.5	The value of an expression should not be cast to an inappropriate essential type.
Rule 10.6	The value of a composite expression shall not be assigned to an object with wider essential type.
Rule 10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essential type.
Rule 10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type.
Rule 11.1	Conversions shall not be performed between a pointer to a function and any other type.
Rule 11.3	A case shall not be performed between a pointer to object type and a pointer to a different object type.
Rule 11.4	A conversion should not be performed between a pointer to object and an integer type.
Rule 11.5	A conversion should not be performed from pointer to void into pointer to object.
Rule 11.6	A cast shall not be performed between pointer to void and an arithmetic type.
Rule 12.1	The precedence of operators within expressions should be made explicit.
Rule 12.2	The right hand operator of a shift operator shall lie in the range zero to one less than the width in bits of the essential type of the left hand operand.
Rule 13.3	A full expression containing an increment(++) or decrement(--) operator should have no other potential side effects other than that caused by the increment or decrement operator.
Rule 13.5	The right hand operand of a logical && or    operator shall not contain persistent side effects.
Rule 14.2	A for loop shall be well formed.

*Table continues on the next page...*

**Table 3. MISRA exceptions (continued)**

Rule 14.4	The controlling expressions of an statement and the controlling expression of an iteration-statement shall have essentially Boolean type.
Rule 15.5	A function should have a single point of exit at the end.
Rule 16.1	All switch statements shall be well-formed.
Rule 17.1	The feature of <stdarg.h> shall not be used.
Rule 18.4	The +, -, += and -= operators should not be applied to an expression of pointer type.
Rule 19.2	The union keyword should not be used.
Rule 20.1	#include directives should only be preceded by preprocessor directives or comments.
Rule 20.10	The # and ## preprocessor operators should not be used.
Rule 21.1	#define and #undef shall not be used on a reserved identifier or reserved macro name.

## Chapter 8

# Known issues

### 8.1 Maximum file path length in Windows 7<sup>®</sup> Operating System

Windows 7 operating system imposes a 260 character maximum length for file paths. When installing the MCUXpresso SDK, place it in a directory close to the root to prevent file paths from exceeding the maximum character length specified by the Windows operating system. The recommended location is the C:\nxp folder.

### 8.2 USBFS controller issue

Because of the USBFS controller design issues, the USB host suspend/resume demos (usb\_suspend\_resume\_host\_hid\_mouse) of the full speed controller do not support the low speed device directly.

### 8.3 USB PID issue

Because the PID of all USB device examples is updated, uninstall the device drivers and then reinstall when the device (with new PID) is plugged in the first time.

### 8.4 Create new project without board template

The following components should be selected at the same time when creating a new project without using a board template, including serial\_manager, serial\_manager\_uart, debug\_console, and one UART adapter (lpuart\_adapter for LPUART IP, uart\_adapter for UART IP, lpsci\_adapter for LPSCI IP, etc).

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# **MCUXpresso SDK Release Notes Supporting TWR-KV46F150M and HVP-KV46F150M**

**Change Logs**

# Contents

<b>Driver Change Log</b>	<b>1</b>
<b>AOI</b> . . . . .	<b>1</b>
<b>CADC</b> . . . . .	<b>1</b>
<b>CMP</b> . . . . .	<b>1</b>
<b>CRC</b> . . . . .	<b>1</b>
<b>DAC</b> . . . . .	<b>1</b>
<b>DMAMUX</b> . . . . .	<b>2</b>
<b>DSPI</b> . . . . .	<b>2</b>
<b>EDMA</b> . . . . .	<b>3</b>
<b>EWM</b> . . . . .	<b>4</b>
<b>FLASH</b> . . . . .	<b>4</b>
<b>FLEXCAN</b> . . . . .	<b>5</b>
<b>FTM</b> . . . . .	<b>6</b>
<b>GPIO</b> . . . . .	<b>7</b>
<b>I2C</b> . . . . .	<b>7</b>
<b>LLWU</b> . . . . .	<b>8</b>
<b>LPTMR</b> . . . . .	<b>9</b>
<b>PDB</b> . . . . .	<b>9</b>
<b>PIT</b> . . . . .	<b>9</b>
<b>PMC</b> . . . . .	<b>9</b>
<b>PORT</b> . . . . .	<b>10</b>

# Contents

Title	Page Number
<b>PWM</b> . . . . .	<b>10</b>
<b>RCM</b> . . . . .	<b>10</b>
<b>SIM</b> . . . . .	<b>10</b>
<b>SMC</b> . . . . .	<b>10</b>
<b>UART</b> . . . . .	<b>11</b>
<b>WDOG</b> . . . . .	<b>12</b>
<b>XBARA</b> . . . . .	<b>12</b>
<b>XBARB</b> . . . . .	<b>12</b>
<b>CLOCK</b> . . . . .	<b>13</b>
<b>Middleware Change Log</b>	<b>14</b>
<b>DMA_MANAGER</b> . . . . .	<b>14</b>
<b>FatFs for MCUXpresso SDK</b> . . . . .	<b>14</b>
<b>SDMMC</b> . . . . .	<b>14</b>
<b>SDIO</b> . . . . .	<b>17</b>
<b>SDSPI</b> . . . . .	<b>17</b>
<b>RTOS Change Log</b>	<b>18</b>
<b>FreeRTOS for MCUXpresso SDK</b> . . . . .	<b>18</b>

# 1 Driver Change Log

## AOI

The current AOI driver version is 2.0.0.

- 2.0.0
  - Initial version.

## CADC

The current CADC driver version is 2.0.0.

- 2.0.0
  - Initial version.

## CMP

The current CMP driver version is 2.0.0.

- 2.0.0
  - Initial version.

## CRC

The current CRC driver version is 2.0.1.

- 2.0.1
  - Bug fix:
    - \* DATA and DATALL macro definition moved from header file to source file.
- 2.0.0
  - Initial version.

## DAC

The current DAC driver version is 2.0.1.

- 2.0.1
  - Bug fix:
    - \* Moved the default DAC\_Enable(..., true) from DAC\_Init() to the application code so users can enable the DAC's output.
- 2.0.0
  - Initial version.



## DMAMUX

The current DMAMUX driver version is 2.0.2.

- 2.0.2
  - New feature:
    - \* Added an always-on enable feature to a DMA channel for ULP1 DMAMUX support.
- 2.0.1
  - Bug fix:
    - \* Fixed build warning while setting the DMA request source in DMAMUX\_SetSource-Change issue by changing the type of the parameter source from uint8\_t to uint32\_t.
- 2.0.0
  - Initial version.

## DSPI

The current dspi driver version is 2.2.0.

- 2.2.0
  - New features:
    - \* Added gasket feature for SPI EDMA driver, which reduces one channel used in the EDMA master transfer. With this feature support, only two channels are needed. For example, if the gasket feature is supported, we could use the DSPI\_MasterTransferCreateHandleEDMA(EXAMPLE\_DSPI\_MASTER\_BASEADDR, &g\_dspi\_edma\_m\_handle, DSPI\_MasterUserCallback, &userData, &dspiEdmaMasterRxRegToRxDataHandle, NULL, &dspiEdmaMasterIntermediaryToTxRegHandle);
    - \* Added dummy data setup API to allow users to configure the dummy data to be transferred.
    - \* Added new APIs for half-duplex transfer function. Users can send and receive data by one API in the polling/interrupt/EDMA way, and users can choose to either transmit first or receive first. Additionally, the PCS pin can be configured as assert status in transmission (between transmit and receive) by setting the isPcsAssertInTransfer to true.
- 2.1.4
  - Bug fix:
    - \* DSPI EDMA driver: The DSPI instance that has separated so the DMA request source can now transfer up to 32767 Bytes data in one DSPI\_MasterTransferEDMA() transfer.
- 2.1.3
  - Bug fix:
    - \* DSPI EDMA driver can no longer support the case that the transfer data size is odd, but the bitsPerFrame is greater than 8.
  - Optimization:
    - \* Added #ifndef/#endif to allow users to change the default TX value at compile time.
- 2.1.2
  - Bug fix:

- \* DSPI\_MasterTransferBlocking function would hang in some corner cases (for example, some cases with bitsPerFrame is 4,6 and kDSPI\_MasterPcsContinuous transfer mode).
- 2.1.1
  - Bug fix:
    - \* Set the EOQ (End Of Queue) bit to TRUE for the last transfer in transactional APIs.
- 2.1.0
  - New features:
    - \* Added Transfer prefix in transactional APIs.

## EDMA

The current eDMA driver version is 2.1.4.

- 2.1.4
  - Bug fix:
    - \* Clear enabled request, status during EDMA\_Init for the case that EDMA is halted before reinitialization.
- 2.1.3
  - Bug fix:
    - \* Add clear DONE bit in IRQ handler to avoid overwrite TCD issue.
    - \* Optimize above solution for the case that transfer request occurs in callback.
- 2.1.2
  - Improvements:
    - \* Added interface to get next TCD address.
    - \* Added interface to get the unused TCD number.
- 2.1.1
  - Improvements:
    - \* Added documentation for eDMA data flow when scatter/gather is implemented for the EDMA\_HandleIRQ API.
    - \* Updated and corrected some related comments in the EDMA\_HandleIRQ API and edma\_handle\_t struct.
- 2.1.0
  - Improvements:
    - \* Changed the EDMA\_GetRemainingBytes API into EDMA\_GetRemainingMajorLoopCount due to eDMA IP limitation (see API comments/note for further details).
- 2.0.5
  - Improvements:
    - \* Added pubweak DriverIRQHandler for K32H844P (16 channels shared).
- 2.0.4
  - Improvements:
    - \* Added support for SoCs with multiple eDMA instances.
    - \* Added pubweak DriverIRQHandler for KL28T DMA1 and MCIMX7U5\_M4.
- 2.0.3
  - Bug fix:

- \* Fixed the incorrect pubweak IRQHandler name issue, which causes re-definition build errors when client sets his/her own IRQHandler, by changing the 32-channel IRQHandler name to DriverIRQHandler.
- 2.0.2
  - Bug fix:
    - \* Fixed incorrect minorLoopBytes type definition in \_edma\_transfer\_config struct, and defined minorLoopBytes as uint32\_t instead of uint16\_t.
- 2.0.1
  - Bug fix:
    - \* Fixed the eDMA callback issue (which did not check valid status) in EDMA\_HandleIRQ API.
- 2.0.0
  - Initial version.

## **EWM**

The current EWM driver version is 2.0.1.

- 2.0.1
  - Fixed EWM\_Deinit hardfault issue.
- 2.0.0
  - Initial version.

## **FLASH**

The current FLASH driver version is 3.0.0.

- 3.0.0
  - Improvements:
    - \* Reorganized FTFx Flash driver source file.
    - \* Extracted Flash cache driver from FTFx driver.
    - \* Extracted FLEXNVM flash driver from FTFx driver.
- 2.3.1
  - Bug fixes:
    - \* Unified Flash IFR design from K3.
    - \* New encoding rule for K3 flash size.
- 2.3.0
  - New features:
    - \* Added support for device with LP flash (K3S/G).
    - \* Added Flash prefetch speculation APIs.
  - Improvements:
    - \* Refined flash\_cache\_clear function.
    - \* Reorganized the member of flash\_config\_t struct.
- 2.2.0

- New features:
  - \* Supported FTFL device in FLASH\_Swap API.
  - \* Supported various pflash start addresses.
  - \* Added support for KV58 in cache clear function.
  - \* Added support for devices with secondary flash (KW40).
- Bug fixes:
  - \* Compiled execute-in-ram functions as PIC binary code for driver use.
  - \* Added missed FLEXRAM properties.
  - \* Fixed unaligned variable issue for execute-in-ram function code array.
- 2.1.0
  - Improvements:
    - \* Updated coding style to align with KSDK 2.0.
    - \* Different alignment size support for PFLASH and FLEXNVM.
    - \* Improved the implementation of execute-in-ram functions.
- 2.0.0
  - Initial version.

## **FLEXCAN**

The current FLEXCAN driver version is 2.3.2.

- 2.3.2
  - Improvements:
    - \* Implementation for ERR005959.
    - \* Implementation for ERR005829.
    - \* Implementation for ERR006032.
- 2.3.1
  - Bug fixes:
    - \* Adding correct handle when kStatus\_FLEXCAN\_TxSwitchToRx coming.
- 2.3.0
  - Improvements:
    - \* Added self wake support from STOP mode in the interrupt handling.
- 2.2.3
  - Bug fix:
    - \* Fixed CANFD data phase's bit rate not set as expected.
- 2.2.2
  - Improvements:
    - \* Added time stamp feature and enabled in interrupt\_transfer example.
- 2.2.1
  - Improvements:
    - \* Separated CANFD initialization API.
    - \* In the interrupt handling, fixed issue that user cannot use normal CAN API when FD is present.
- 2.2.0

- Improvements:
  - \* Added FSL\_FEATURE\_FLEXCAN\_HAS\_SUPPORT\_ENGINE\_CLK\_SEL\_REMOVE feature to support SoCs without CAN Engine Clock selection in FlexCAN module.
  - \* Added FlexCAN Serial Clock Operation to support i.MX SoCs.
- 2.1.0
  - Bug fixes:
    - \* Fixed wrong function name spelling: FLEXCAN\_XXX() -> FLEXCAN\_XXX();
    - \* Moved Freeze Enable/Disable setting from FLEXCAN\_Enter/ExitFreezeMode() to FLEXCAN\_Init();
    - \* Fixed wrong helper macro values.
  - Other changes:
    - \* Hided FLEXCAN\_Reset() to user.
    - \* Used NDEBUG macro to wrap FLEXCAN\_IsMbOccupied() function instead of DEBUG macro.
- 2.0.0
  - Initial version.

## FTM

The current FTM driver version is 2.1.0.

- 2.1.0
  - New feature:
    - \* Add a new API FTM\_SetupPwmMode() to allow user set the channel match value in unit of timer ticks. New configure structure called ftm\_chnl\_pwm\_config\_param\_t was offered to configure the channel's PWM parameters. This API is similar with FTM\_SetupPwm() API, but the new API will not set the timer period(MOD value), it will be useful for users to set the PWM parameters without changing the timer period.
  - Bug fixes:
    - \* Add feature macro to enable/disable the external trigger source configuration.
- 2.0.4
  - Features:
    - \* Added to enable DMA transfer with new API:
      - FTM\_EnableDmaTransfer()
- 2.0.3
  - Bug fixes:
    - \* Updated the FTM driver to enable fault input after configuring polarity.
- 2.0.2
  - Features:
    - \* Added support to Quad Decoder feature with new APIs:
      - FTM\_GetQuadDecoderFlags()
      - FTM\_SetQuadDecoderModuloValue()
      - FTM\_GetQuadDecoderCounterValue()
      - FTM\_ClearQuadDecoderCounterValue()

- 2.0.1
  - Bug fixes:
    - \* Updated the FTM driver to fix write to ELSA and ELSB bits.
    - \* FTM combine mode: set the COMBINE bit before writing to CnV register.
- 2.0.0
  - Initial version.

## **GPIO**

The current driver version is 2.3.0.

- 2.3.1:
  - Remove deprecated APIs.
- 2.3.0:
  - New feature:
    - \* Update the driver code to adapt the case of interrupt configurations in GPIO module. New APIs were added to configure the GPIO interrupt settings if the module has this feature on it.
- 2.2.1:
  - API interface changes:
    - \* Refined naming of API while keep all original APIs by marking them as deprecated. Original API will be removed in next release. The main change is update API with prefix of `_PinXXX()` and `_PortXXX`.
- 2.1.1:
  - API interface changes:
    - \* Added API for the check attribute bytes.
- 2.1.0:
  - API interface changes:
    - \* Added "pins" or "pin" to some APIs' names.
    - \* Renamed "`_PinConfigure`" to "`GPIO_PinInit`".

## **I2C**

The current I2C driver version is 2.0.6.

- 2.0.6
  - Bug fix:
    - \* Fixed the issue that I2C Master transfer APIs(blocking/non-blocking) does not support the situation that master transfer with subaddress and transfer data size zero, which means no data follows by the subaddress.
- 2.0.5
  - Improvements:
    - \* Added `I2C_WATI_TIMEOUT` macro to allow the user to specify the timeout times for waiting flags in functional API and blocking transfer API.

- 2.0.4
  - Bug fixes:
    - \* Added proper handle for transfer config flag `kI2C_TransferNoStartFlag` to support transmit with `kI2C_TransferNoStartFlag` flag. Only supports write only or write+read with no start flag, does not support read only with no start flag.
- 2.0.3
  - Bug fixes:
    - \* Removed `enableHighDrive` member in the master/slave configuration structure because the operation to `HDRS` bit is useless, user needs to use `DSE` bit in port register to configure the high drive capability.
    - \* Added reset registers operation in `I2C_MasterInit` and `I2C_SlaveInit` APIs. Fixed issue where I2C could not switch between master and slave mode.
    - \* Improved slave IRQ handler to handle the corner case that stop flag and address match flag come synchronously.
- 2.0.2
  - Bug fixes:
    - \* Fixed issue in master receive and slave transmit mode with no stop flag. The master could not succeed to start next transfer because the master could not send out re-start signal.
    - \* Fixed data transfer out of order issue due to memory barrier
    - \* Added hold time configuration for slave. By leaving the `SCL` divider and `MULT` reset values when configure to slave mode, the setup and hold time of the slave is then reduced outside of spec for lower baudrates. This can cause intermittent arbitration loss on the master side.
  - New features:
    - \* Added address nak event for master.
    - \* Added general call event for slave.
- 2.0.1
  - New features:
    - \* Added double buffer enable configuration for Socs which have the `DFEN` bit in `S2` register.
    - \* Added flexible transmit/receive buffer size support in `I2C_SlaveHandleIRQ`.
    - \* Added start flag clear, address match, and release bus operation in `I2C_SlaveWrite/Read-Blocking` API.
  - Bug fix:
    - \* Changed the `kI2C_SlaveRepeatedStartEvent` to `kI2C_SlaveStartEvent`.

## LLWU

The current LLWU driver version is 2.0.2.

- 2.0.2
  - Optimization:
    - \* Correct driver function `LLWU_SetResetPinMode` parameter name.
- 2.0.1
  - Miscellaneous changes:

- \* Updates for KL8x.
- 2.0.0
  - Initial version.

## **LPTMR**

The current LPTMR driver version is 2.0.1.

- 2.0.1
  - Driver update:
    - \* Updated the LPTMR driver to support 32-bit CNR and CMR registers in some devices.
- 2.0.0
  - Initial version.

## **PDB**

The current PDB driver version is 2.0.1.

- 2.0.1
  - Changed PDB register base array to const.
- 2.0.0
  - Initial version.

## **PIT**

The current PIT driver version is 2.0.1.

- 2.0.1
  - Bug fix:
    - \* Cleared timer enable bit for all channels in function PIT\_Init() to make sure all channels stay in disable status before setting other configurations.
- 2.0.0
  - Initial version.

## **PMC**

The current PMC driver version is 2.0.0.

- 2.0.0
  - Initial version.



## **PORT**

The current PORT driver version is 2.1.0.

- 2.1.0
  - New features:
    - \* Updated the driver code to adapt the case of the interrupt configurations in GPIO module.
    - Will move the pin configuration APIs to the GPIO module.
- 2.0.2
  - Miscellaneous changes:
    - \* Added feature guard macros in the driver.
- 2.0.1
  - Miscellaneous changes:
    - \* Added "const" in function parameter.
    - \* Updated some enumeration variables' names.

## **PWM**

The current PWM driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **RCM**

The current RCM driver version is 2.0.1.

- 2.0.1
  - [KPSDK-10249] Fixed kRCM\_SourceSw bit shift issue.
- 2.0.0
  - Initial version.

## **SIM**

The current SIM driver version is 2.1.0.

- 2.1.0
  - Added new APIs of SIM\_GetRfAddr() and SIM\_EnableSysTickClock().
- 2.0.0
  - Initial version.

## **SMC**

The current SMC driver version is 2.0.4.

- 2.0.4
  - When entering stop modes, use the RAM function for the flash synchronize issue. The application should make sure that the RW data of fsl\_smc.c is located in the memory region which is not powered off in stop modes.
- 2.0.3
  - Added APIs SMC\_PreEnterStopModes, SMC\_PreEnterWaitModes, SMC\_PostExitWaitModes, and SMC\_PostExitStopModes.
- 2.0.2
  - Bug fix:
    - \* Added DSB before WFI, add ISB after WFI.
  - Miscellaneous changes:
    - \* Updated SMC\_SetPowerModeVlpx implementation.
- 2.0.1
  - Miscellaneous changes:
    - \* Updated for KL8x.
- 2.0.0
  - Initial version.

## UART

The current UART driver version is 2.1.6.

- 2.1.6
  - Fixed the repeated reading status register issue while dealing the IRQ routine.
- 2.1.5
  - Added hardware flow control function support.
  - Added idle line detected feature in UART\_TransferNonBlocking function. If an idle line is detected, a callback is triggered with status kStatus\_UART\_IdleLineDetected returned. This feature may be useful when the number of received bytes is less than the expected receive data size. Before triggering the callback, data in the FIFO is read out (if it has FIFO), and all interrupts are not disabled except if the receive data size reaches 0.
  - Enabled the RX FIFO watermark function. With the idle line detected feature enabled, you can set the watermark value to whatever you want (should not be bigger than the RX FIFO size). Data is then received and a callback is triggered when data receive ends.
- 2.1.4
  - Changed parameter type in UART\_RTOS\_Init() struct rtos\_uart\_config -> uart\_rtos\_config\_t.
  - Bug fixes:
    - \* Disabled UART receive interrupt instead of disable all NVIC when read data from ring buffer. Because with ring buffer is used, receive nonblocking disables all NVIC interrupts to protect the ring buffer. This has a negative effect to other IPS which are using interrupt.
- 2.1.3
  - Added RX framing error and parity error status check when use interrupt transfer.
- 2.1.2

- Fixed baud rate fine adjust bug to make the computed baud rate more accurately.
- 2.1.1
  - Removed needless check of event flags and assert in UART\_RTOS\_Receive.
  - Waited always for RX event flag in UART\_RTOS\_Receive.
- 2.1.0
  - Added transactional API.
- 2.0.0
  - Initial version.

## **WDOG**

The current WDOG driver version is 2.0.0.

- 2.0.0
  - Initial version.

## **XBARA**

The current XBARA driver version is 2.0.3.

- 2.0.3
  - Bug fixes:
    - \* Corrected configuration for function XBAR\_SetOutputSignalConfig.
- 2.0.2
  - Other changes:
    - \* Changed array clock name.
- 2.0.1
  - Bug fixes:
    - \* Fixed w1c bits for XBARA\_SetOutputSignalConfig function.
- 2.0.0
  - Initial version.

## **XBARB**

The current XBARB driver version is 2.0.1.

- 2.0.1
  - Bug fixes:
    - \* Corrected XBARB\_SetSignalsConnection function.
  - Other changes:
    - \* Changed array clock name.
- 2.0.0
  - Initial version.

## **CLOCK**

Current CLOCK driver version is 2.2.1

- 2.2.1
  - Bug Fix:
    - \* Fix the issue that MCG could not switch to FEE/FBE/PBE modes when OSCERCLK clock not enabled.
- 2.2.0
  - New Features:
    - \* [KPSDK-9157] Update CLOCK\_SetFeiMode/CLOCK\_SetFbiMode/CLOCK\_BootToFeiMode() to support set MCG\_C4[DMX32]=1 in FEI/FBI modes.
  - Bug Fix:
    - \* Update IP\_CLOCKS array, remove unused gates and add missing gates.
- 2.1.0
  - Other Changes:
    - \* Merge fsl\_mcg and fsl\_osc into fsl\_clock.
- 2.0.0
  - Initial version.

## 2 Middleware Change Log

### DMA\_MANAGER

The current DMA\_MANAGER driver version is 2.1.0.

- 2.1.0
  - Updated DMA manager interface to support dynamic configuration of the managed area. This is used for a platform with multiple cores.
- 2.0.0
  - Initial version.

### FatFs for MCUXpresso SDK

The current version is FatFs R0.13a\_rev0.

- R0.13a\_rev0
  - Upgraded to version 0.13a. Added patch ff\_13a\_p1.diff.
- R0.12c\_rev1
  - Add NAND disk support.
- R0.12c\_rev0
  - Upgraded to version 0.12c and applied patches ff\_12c\_p1.diff and ff\_12c\_p2.diff.
- R0.12b\_rev0
  - Upgraded to version 0.12b.
- R0.11a
  - Added glue functions for low-level drivers (SDHC, SDSPI, RAM, MMC). Modified diskio.c.
  - Added RTOS wrappers to make FatFs thread safe. Modified syscall.c.
  - Renamed ffconf.h to ffconf\_template.h. Each application should contain its own ffconf.h.
  - Included ffconf.h into diskio.c to enable the selection of physical disk from ffconf.h by macro definition.
  - Conditional compilation of physical disk interfaces in diskio.c.

### SDMMC

The current driver version is 2.2.7.

- 2.2.7
  - Bug fix:
    - \* Fixed MDK 66-D warning.
- 2.2.6
  - Improvements:
    - \* Removed some SoC-specific header files from porting layer.
    - \* Saved MMC OCR registers while sending CMD1 with argument 0.

- Bug fix:
  - \* Added MMC\_PowerOn function, where there is a delay function after the power up SD-CARD. Otherwise, the card may init fail.
- 2.2.5
  - New features:
    - \* Added SD\_ReadStatus API to get 512bit SD status.
    - \* Added error log support in SDCARD functions.
    - \* Added SDMMC\_ENABLE\_SOFTWARE\_TUNING to enable/disable software tuning and is disabled by default.
    - \* Added an error procedure in the transfer function to improve stability.
    - \* Removed deprecated GPIO SPI in host layer.
- 2.2.4
  - Bug fix:
    - \* Fixed DDR mode data sequence miss issue, which is caused by NIBBLE\_POS.
  - New features:
    - \* Increased g\_sdmmc 512byte to improve the performance when application use a non-word align data buffer address.
    - \* Used OCR access mode bits to determine the mmccard high capacity flag.
    - \* Enabled auto cmd12 for SD read/write.
    - \* Disabled DDR mode frequency multiply by 2.
- 2.2.3
  - Bug fix:
    - \* Added response check for send operation condition command. If not checked, the card may occasionally init fail.
- 2.2.2
  - Moved set card detect priority operation before enable IRQ.
- 2.2.1
  - New features:
    - \* Improved MMC Boot feature.
    - \* Keep SD\_Init/SDIO\_Init function for forward compatibility.
- 2.2.0
  - New features:
    - \* Separated the SD/MMC/SDIO init API to xxx\_CardInit/xxx\_HostInit.
    - \* Allowed user register card detect callback, select card detect type, and determine the card detect timeout value.
    - \* Allowed user register the power on/off function, and determine the power on/off delay time.
    - \* SD\_Init/SDIO\_Init will be deprecated in the next version.
    - \* Added write complete wait operation for MMC\_Write to fix command timeout issue.
- 2.1.6
  - Enhanced SD IO default driver strength.
- 2.1.5
  - Fixed coverity issue.
  - Fixed SD v1.x card write fail issue. It was caused by the block length set error.
- 2.1.4

- Miscellaneous:
  - \* Added Host reset function for card re-initialization.
  - \* Added Host\_ErrorRecovery function for host error recovery procedure.
  - \* Added cache maintain operation
  - \* Added HOST\_CARD\_INSERT\_CD\_LEVEL to improve compatibility.
- Bug fix:
  - \* Fixed card cannot detect dynamically.
- 2.1.3
  - Bug fix:
    - \* Non high-speed sdcard init fail at switch to high speed.
  - Miscellaneous:
    - \* Optimized tuning/mmc switch voltage/mmc select power class/mmc select timing function.
    - \* Added strobe dll for mmc HS400 mode.
    - \* Added Delay for SDCard power up.
- 2.1.2
  - New features:
    - \* Added fsl\_host.h to provide prototype to adapt different controller IPs(SDHC/SDIF).
    - \* Added adaptor code in SDMMC/Port folder to adapt different host controller IPs with different. transfer modes(interrupt/polling/freertos). Application includes a different adaptor code to make application more simple.
    - \* Adaptor code provides HOST\_Init/HOST\_Deinit/CardInsertDetect. APIs to do host controller initialize and transfer function configuration. SDMMC card stack uses adaptor code inside stack to wait card insert and configure host when calling card init APIs (SD\_Init/MMC\_Init/SDIO\_Init).
    - \* This change requires the user to include host adaptor code into the application. If not changed, link errors saying it cannot find the definition of HOST\_Init/HOST\_Deinit/CardInsertDetect appear.
  - New features: Improved SDMMC to support SD v3.0 and eMMC v5.0.
  - Bug fix:
    - \* Fixed incorrect comparison between count and length in MMC\_ReadBlocks/MMC\_WriteBlocks.
- 2.1.1
  - Bug fixes:
    - \* Fixed the block range boundary error when transferring data to MMC card.
    - \* Fixed the bit mask error in the SD card switch to high speed function.
  - Other changes:
    - \* Added error code to indicate that SDHC ADMA1 transfer type is not supported yet.
    - \* Optimized the SD card initialization function.
- 2.1.0
  - Bug fixes:
    - \* Changed the callback mechanism when sending a command.
    - \* Fixed the performance low issue when transferring data.
  - Other changes:
    - \* Changed the name of some error codes returned by internal function.

- \* Merged all host related attributes to one structure.
- \* Optimize the function of setting maximum data bus width for MMC card.

## **SDIO**

The current driver version is 2.2.7.

- 2.2.7
  - Bug fix:
    - \* Fixed MDK 66-D warning.
- 2.2.6
  - New features:
    - \* Add a unify transfer interface for SDIO.
  - Bug fix:
    - \* Wrong pointer address used by SDMMCHOST\_Init.
- 2.1.5
  - Bug fix:
    - \* Improved SDIO card init sequence and add retry option for SDIO\_SwitchToHighSpeed function.
- 2.1.4
  - Miscellaneous changes:
    - \* Added Go\_Idle function for SDIO card.
- 2.0.0
  - Initial version.

## **SDSPI**

The current driver version is 2.1.4.

- 2.1.4
  - Bug fix:
    - \* Fixed MDK 66-D warning.
- 2.1.3
  - Improved SDSPI code size and performance.
- 2.0.0
  - Initial version.



### 3 RTOS Change Log

#### FreeRTOS for MCUXpresso SDK

The current version is FreeRTOS 9.0.0. Original package is available at [freertos.org](http://freertos.org).

- 9.0.0\_rev3
  - New features:
    - \* Tickless idle mode support for Cortex-A7. Add fsl\_tickless\_epit.c and fsl\_tickless\_generic.h in portable/IAR/ARM\_CA9 folder.
    - \* Enabled float context saving in IAR for Cortex-A7. Added configUSE\_TASK\_FPU\_SUPPORT macros. Modified port.c and portmacro.h in portable/IAR/ARM\_CA9 folder.
  - Other changes:
    - \* Transformed ARM\_CM core specific tickless low power support into generic form under freertos/Source/portable/low\_power\_tickless/.
- 9.0.0\_rev2
  - New features:
    - \* Enabled MCUXpresso thread aware debugging. Add freertos\_tasks\_c\_additions.h and configINCLUDE\_FREERTOS\_TASK\_C\_ADDITIONS\_H and configFRTOS\_MEMORY\_SCHEME macros.
- 9.0.0\_rev1
  - New features:
    - \* Enabled -fno-plt optimization in GCC by adding **attribute((used))** for vTaskSwitchContext.
    - \* Enabled KDS Task Aware Debugger. Apply FreeRTOS patch to enable configRECORD\_STACK\_HIGH\_ADDRESS macro. Modified files are task.c and FreeRTOS.h.
- 9.0.0\_rev0
  - New features:
    - \* Example freertos\_sem\_static.
    - \* Static allocation support RTOS driver wrappers.
  - Other changes:
    - \* Tickless idle rework. Support for different timers is in separated files (fsl\_tickless\_systick.c, fsl\_tickless\_lptmr.c).
    - \* Removed configuration option configSYSTICK\_USE\_LOW\_POWER\_TIMER. Low power timer is now selected by linking of appropriate file fsl\_tickless\_lptmr.c.
    - \* Removed configOVERRIDE\_DEFAULT\_TICK\_CONFIGURATION in RVDS port. Use of **attribute((weak))** is the preferred solution. Not same as \_weak!
- 8.2.3
  - New features:
    - \* Tickless idle mode support.
    - \* Added template application for Kinetis Expert (KEx) tool (template\_application).
  - Other changes:
    - \* Folder structure reduction. Keep only Kinetis related parts.

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