MP3 Report

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# Assumptions

# Software Performance

Performance for software version running on the Zedboard's ARM core - Time per image, Time for all images

# Hardware Accelerated Performance

Performance for hardware accelerated version - Time per image, Time for all images, Speedup over software version

# Accuracy

Accuracy of your accelerated version. If your accuracy does not match the baseline, explain why.

# Accelerator Design

Include what layers you accelerate, and their individual designs

Talk about the interface between HW and SW. How did you choose your interfaces etc

Design philosophy - How you designed the accelerator, general idea/themes etc.

Talk about your HLS implementation (if you used HLS)

How did you choose which layers to accelerate

Did you have a particular strategy, if so, explain it.

Types of optimizations and pragmas used etc.

# Feedback

We would also like feedback on the MP, and how easy you think it was to accelerate LeNet

# Our Modifications

Briefly describe what you did, why and how you placed the pragmas.

Describe any changes that had to be made to each version of the code.

# Difficulties

The difficulties/bugs you encountered and how you solved them

A major difficulty we encountered was regarding the timing analysis.

# What We Learned

What you learned from this machine problem

# Workspace

Vivado version used and whether you worked on a personal machine or the EWS machines in ECE4022. (Also mention whether you worked on Linux/Windows)