Path Summary

	Property	Value
1	From Node	cpu:u9|rom8x1024_DE2:rom8x1024a|lpm_rom:lpm_rom_component|altrom:srom|altsyncram:rom_block|altsyncram_ol01:auto_generated|q_a[19]
2	To Node	VGA_CHAR_Ctrl:u8|regs_26[3]
3	Delay	18.671

Statistics

	Property	Value	Count	Total Delay	% of Total	Min	Max
1	Data Delay	18.671					
2	Number of Logic Levels		26				
3	Physical Delays						
1	IC		27	12.879	68	0.000	1.387
2	Cell		28	5.792	31	0.052	0.708

Data Path

	Total	Incr	RF	Type	Fanout	Location	Element
1	18.671	18.671					data path
1	0.000	0.000			1	M9K_X51_Y43_N0	cpu:u9|rom8x1024_DE2:rom8x1024a|lpm_rom:lpm_rom_component|altrom:srom|altsyncram:rom_block|altsyncram_ol01:auto_generated|q_a[19]
2	0.052	0.052	FF	CELL	197	M9K_X51_Y43_N0	u9|rom8x1024a|lpm_rom_component|srom|rom_block|auto_generated|ram_block1a0|portadataout[2]
3	1.414	1.362	FF	IC	1	LCCOMB_X50_Y35_N24	u9|register_file|reg_read_data2[3]~397|datab
4	1.855	0.441	FR	CELL	1	LCCOMB_X50_Y35_N24	u9|register_file|reg_read_data2[3]~397|combout
5	2.884	1.029	RR	IC	1	LCCOMB_X53_Y37_N0	u9|register_file|reg_read_data2[3]~398|datad
6	3.039	0.155	RR	CELL	1	LCCOMB_X53_Y37_N0	u9|register_file|reg_read_data2[3]~398|combout
7	3.245	0.206	RR	IC	1	LCCOMB_X53_Y37_N24	u9|register_file|reg_read_data2[3]~399|datad
8	3.400	0.155	RR	CELL	1	LCCOMB_X53_Y37_N24	u9|register_file|reg_read_data2[3]~399|combout
9	4.068	0.668	RR	IC	1	LCCOMB_X54_Y39_N14	u9|register_file|reg_read_data2[3]~400|datad

	Total	Incr	RF	Type	Fanout	Location	Element
10	4.223	0.155	RR	CELL	1	LCCOMB_X54_Y39_N14	u9|register_file| reg_read_data2[3]~400| combout
11	5.247	1.024	RR	IC	1	LCCOMB_X58_Y35_N10	u9|register_file| reg_read_data2[3]~401|datad
12	5.402	0.155	RR	CELL	1	LCCOMB_X58_Y35_N10	u9|register_file| reg_read_data2[3]~401| combout
13	6.023	0.621	RR	IC	1	LCCOMB_X63_Y35_N6	u9|register_file| reg_read_data2[3]~404|datad
14	6.178	0.155	RR	CELL	9	LCCOMB_X63_Y35_N6	u9|register_file| reg_read_data2[3]~404| combout
15	7.565	1.387	RR	IC	1	LCCOMB_X63_Y35_N16	u9|alu_b_sel_2|y[3]~20|datac
16	7.852	0.287	RR	CELL	9	LCCOMB_X63_Y35_N16	u9|alu_b_sel_2|y[3]~20| combout
17	8.938	1.086	RR	IC	2	LCCOMB_X61_Y39_N6	u9|alua|Add1~6|dataa
18	9.404	0.466	RR	CELL	1	LCCOMB_X61_Y39_N6	u9|alua|Add1~6|cout
19	9.404	0.000	RR	IC	2	LCCOMB_X61_Y39_N8	u9|alua|Add1~8|cin
20	9.470	0.066	RF	CELL	1	LCCOMB_X61_Y39_N8	u9|alua|Add1~8|cout
21	9.470	0.000	FF	IC	2	LCCOMB_X61_Y39_N10	u9|alua|Add1~10|cin
22	9.536	0.066	FR	CELL	1	LCCOMB_X61_Y39_N10	u9|alua|Add1~10|cout
23	9.536	0.000	RR	IC	2	LCCOMB_X61_Y39_N12	u9|alua|Add1~12|cin
24	9.602	0.066	RF	CELL	1	LCCOMB_X61_Y39_N12	u9|alua|Add1~12|cout
25	9.602	0.000	FF	IC	2	LCCOMB_X61_Y39_N14	u9|alua|Add1~14|cin
26	9.668	0.066	FR	CELL	1	LCCOMB_X61_Y39_N14	u9|alua|Add1~14|cout
27	9.668	0.000	RR	IC	2	LCCOMB_X61_Y39_N16	u9|alua|Add1~16|cin
28	9.734	0.066	RF	CELL	1	LCCOMB_X61_Y39_N16	u9|alua|Add1~16|cout
29	9.734	0.000	FF	IC	2	LCCOMB_X61_Y39_N18	u9|alua|Add1~18|cin
30	9.800	0.066	FR	CELL	1	LCCOMB_X61_Y39_N18	u9|alua|Add1~18|cout
31	9.800	0.000	RR	IC	2	LCCOMB_X61_Y39_N20	u9|alua|Add1~20|cin
32	9.866	0.066	RF	CELL	1	LCCOMB_X61_Y39_N20	u9|alua|Add1~20|cout
33	9.866	0.000	FF	IC	2	LCCOMB_X61_Y39_N22	u9|alua|Add1~22|cin
34	10.300	0.434	FF	CELL	1	LCCOMB_X61_Y39_N22	u9|alua|Add1~22|combout
35	11.331	1.031	FF	IC	1	LCCOMB_X65_Y36_N0	u9|alua|Mux50~5|datad
36	11.456	0.125	FF	CELL	1	LCCOMB_X65_Y36_N0	u9|alua|Mux50~5|combout
37	11.681	0.225	FF	IC	1	LCCOMB_X65_Y36_N6	u9|alua|Mux50~6|datad
38	11.831	0.150	FR	CELL	1	LCCOMB_X65_Y36_N6	u9|alua|Mux50~6|combout
39	12.034	0.203	RR	IC	1	LCCOMB_X65_Y36_N12	u9|alua|Mux50~7|datad
40	12.189	0.155	RR	CELL	1	LCCOMB_X65_Y36_N12	u9|alua|Mux50~7|combout
41	12.393	0.204	RR	IC	1	LCCOMB_X65_Y36_N10	u9|alua|Mux50~8|datad
42	12.548	0.155	RR	CELL	1	LCCOMB_X65_Y36_N10	u9|alua|Mux50~8|combout
43	12.752	0.204	RR	IC	1	LCCOMB_X65_Y36_N8	u9|alua|Mux50~9|datad
44	12.907	0.155	RR	CELL	11	LCCOMB_X65_Y36_N8	u9|alua|Mux50~9|combout

	Total	Incr	RF	Type	Fanout	Location	Element
45	13.883	0.976	RR	IC	1	LCCOMB_X66_Y34_N6	u8|Equal16~5|dataa
46	14.320	0.437	RF	CELL	5	LCCOMB_X66_Y34_N6	u8|Equal16~5|combout
47	14.655	0.335	FF	IC	1	LCCOMB_X66_Y34_N2	u8|Equal16~9|dataa
48	15.055	0.400	FF	CELL	39	LCCOMB_X66_Y34_N2	u8|Equal16~9|combout
49	15.346	0.291	FF	IC	1	LCCOMB_X66_Y34_N26	u8|regs_31[4]~2|datad
50	15.496	0.150	FR	CELL	8	LCCOMB_X66_Y34_N26	u8|regs_31[4]~2|combout
51	16.566	1.070	RR	IC	1	LCCOMB_X67_Y26_N26	u8|Decoder0~13|datad
52	16.721	0.155	RR	CELL	4	LCCOMB_X67_Y26_N26	u8|Decoder0~13|combout
53	17.463	0.742	RR	IC	1	LCCOMB_X67_Y29_N14	u8|Decoder0~40|datac
54	17.748	0.285	RR	CELL	6	LCCOMB_X67_Y29_N14	u8|Decoder0~40|combout
55	17.963	0.215	RR	IC	1	FF_X67_Y29_N1	u8|regs_26[3]|ena
56	18.671	0.708	RR	CELL	1	FF_X67_Y29_N1	VGA_CHAR_Ctrl:u8|regs_26[3]