

An instruction is a command to CPU for a basic operation. For example: add two numbers and store result to memory/another storage.

An instruction in low level language must contain information on

- Operation to be performed
- Data of sources of data
- Result field
- Other information (size of data, source of data etc) for the CPU

Show instruction format in low level language.

Operation	Result	Data -1	Data -2
field	field	Source of data-	Source of data-
		1	2

What is machine code?

Machine code is binary representation of an instruction in low level language.

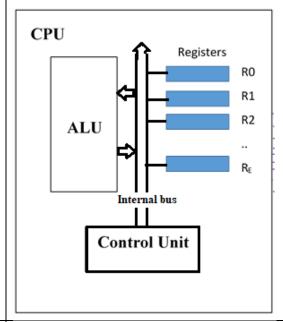
A machine code must contain all information of an instruction written in low level language but represented in binary. Moreover, machine code may contain more information required for the CPU.

A typical machine code: 0011010101111000011100

What is Instruction set?

A CPU is designed to perform only a limited numbers of basic operations and separate commands or instructions are used for basic operations. Instruction set refers to all instructions of a CPU.

Show internal architecture of a CPU.



What is a register?

A register is a high speed electronic storage within the CPU? There are a number of registers in a CPU.

What is the use of register?

CPU uses registers to hold data for arithmetic/logical operations. Registers are also used to store results

How registers are used in low level language?

Registers are only few in numbers, may be 8, 16, 32 so. Registers are addressed by names in	
low level language as assigned by CPU designers. Usually English letters and subscripts are	
used, for example R0, R1 or S0, S1, AX, BX etc.	
How registers are used in machine code?	
By specific codes, containing 1's and 0's.	
What is control unit?	
Control unit is designed to decode machine codes of instructions and control all other	
sections of CPU and computer in electronic form.	
What is ALU?	
ALU is a functional unit of a CPU that is designed to perform all arithmetic and logical	
operations. ALU contains digital circuits to perform basic operations.	