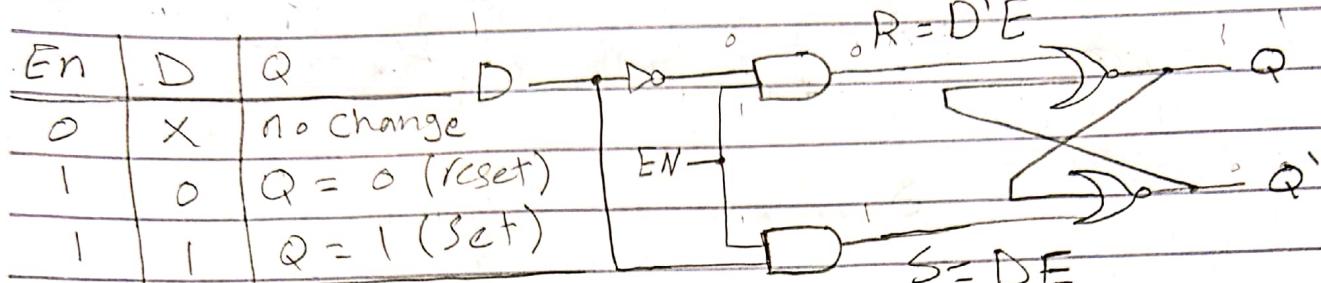


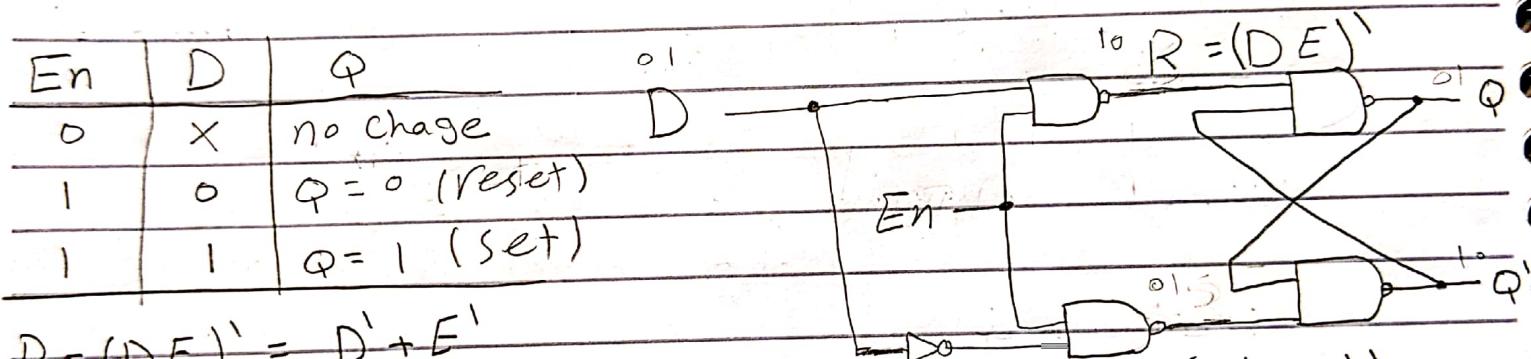
Exercise Six

1-The following D latch is constructed with NOR gates for the SR Latch Part and two AND gates for the Control Select and an inverter.



Obtain a D latch, draw the logic diagram and verify the circuit operation.

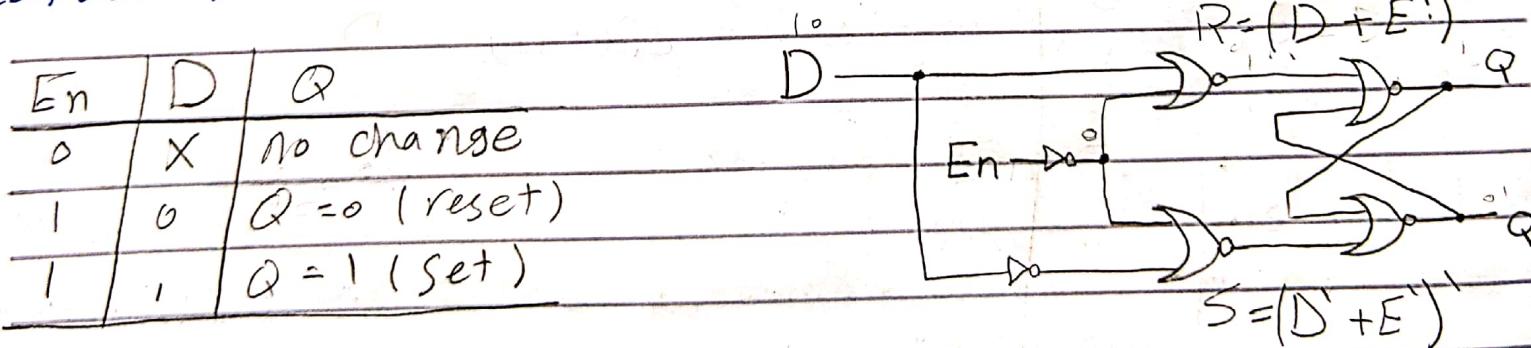
(a) Use four NAND gates and an inverter.



$$R = (DE)' = D' + E'$$

$$S = (D'E)' = D + E$$

(b) Use four NOR gates. Inverters may be needed.

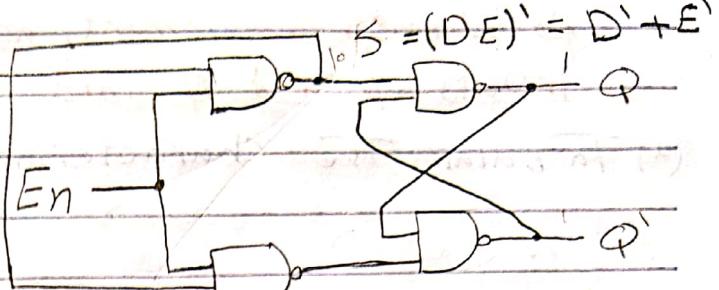


$$R = (D + E')' = D'E$$

$$S = (D'E)' = DE$$

(C) Use four NAND gates only (without an inverter)

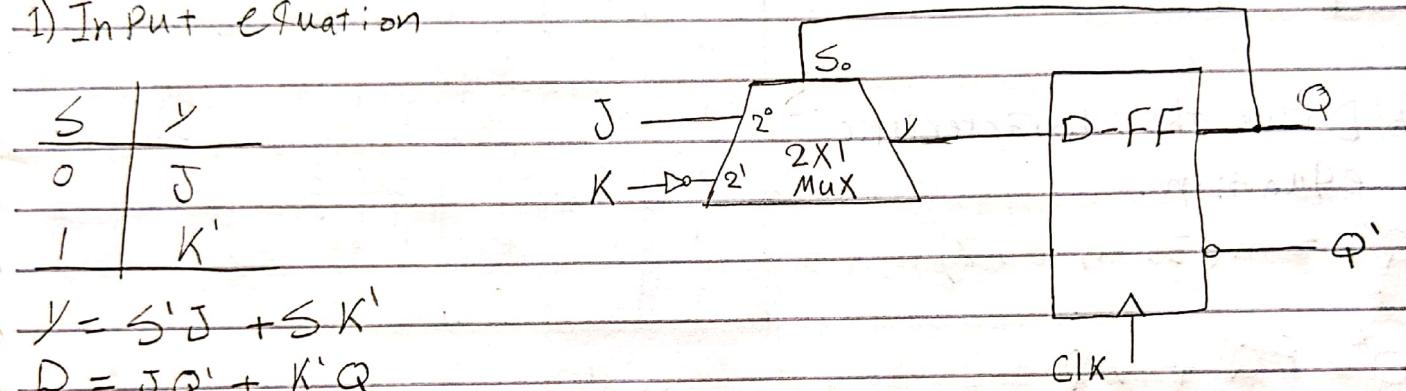
En	D	Next State of Q	D
0	X	no change	
1	0	$Q = 0$ (Reset)	
1	1	$Q = 1$ (Set)	



$$R = DE + E' = (E' + D)(E + E') = (E' + D) \quad R = ((DE)') \cdot E'$$

2- Construct ~~using~~ a JK Flip-Flop Using a D-FF, two-to-one multiplexers and an inverter.

1) Input equation



$$Y = S'J + SK'$$

$$D = JQ' + K'Q$$

2) Characteristic equation

$$Q(t+1) = D$$

$$= JQ' + K'Q$$

3- Show that the characteristic equation for the complement output of a JK flip-flop is

Transition Table $Q'^* = J'Q' + KQ$

J	K	Q	Q^*
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

$$Q'^* = \sum m(1, 4, 5, 6)$$

J	K	Q	Q'	Q'^*
0	0	0	1	1
0	0	1	0	0
0	1	0	1	1
0	1	1	0	0
1	0	0	1	1
1	0	1	0	0
1	1	0	1	1
1	1	1	0	0

$$Q'^* = J'Q' + KQ$$

41. A PN flip-flop has four operations: Clear to 0, No Change, Complement, and Set to 1, when inputs P and N are 00, 01, 10, and 11, respectively.

(a) Tabulate the characteristic table.

Transition Table

P	N	Q^*
0	0	0 (clear)
0	1	Q (no change)
1	0	Q' (Complement)
1	1	1 (Set)

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

(b) Drive the characteristic equation.

$$Q^* = \sum m(3, 4, 6, 7)$$

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

(c) Tabulate excitation table

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

P	N	Q	Q^*
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

5. Explain the differences among

- Truth Table: Describes The Combinational Circuits.
- State table: Describes The Sequential Circuits.
- Characteristic table: Describe the Flip-Flop operations.
- Excitation table: Predict the input of a Flip-Flop.
- Boolean equation: It's a boolean function the output is a function of the inputs only
- State equation: It's a boolean function but the output is a function of the inputs and the previous state of the function.
- Characteristic equation: Boolean Function describes the Flip-Flop operation.
- Direct Flip-Flop input equation: Boolean function describes the Present (Previous) state of a flip-flop

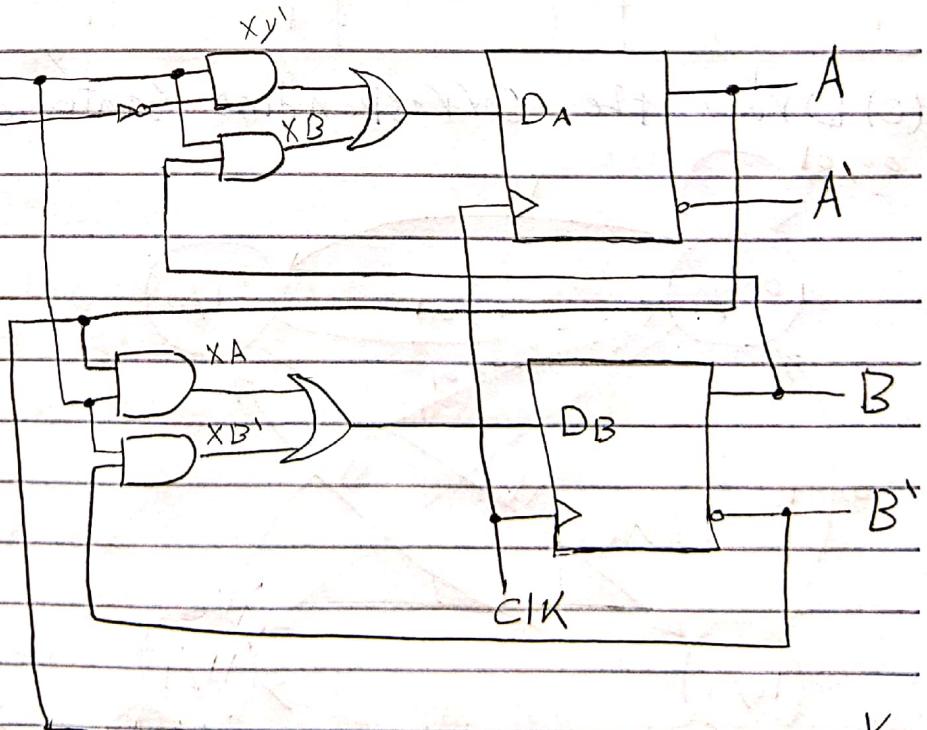
6. A Sequential Circuit with two D Flip-Flops A and B, two inputs X and Y, one output Z.

$$A^* = XY' + XB$$

$$B^* = XA + XB'$$

$$Z = A$$

(a) Draw logic diagram



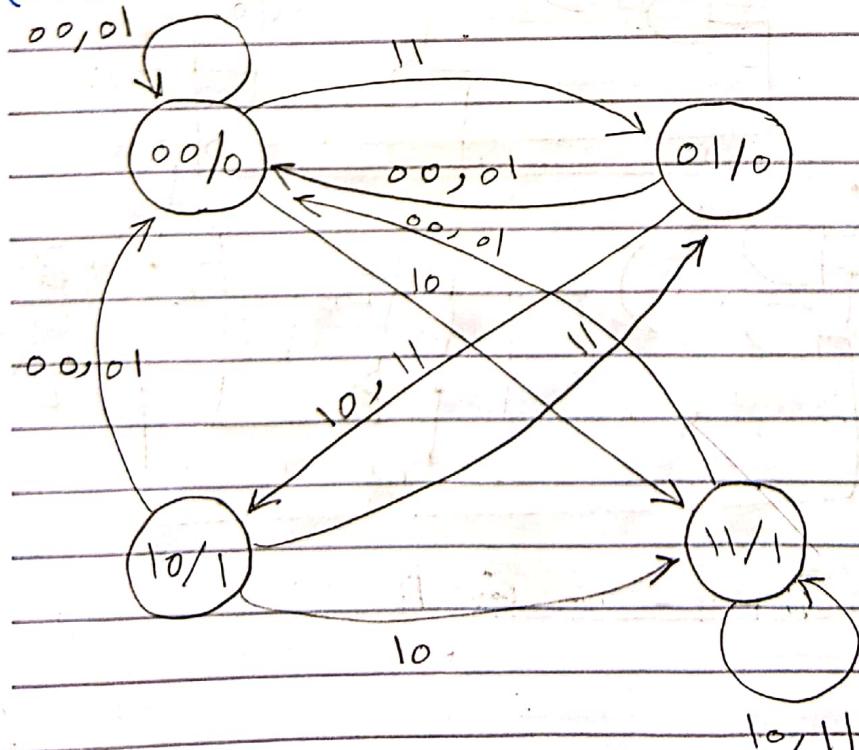
$$A^* = X(Y' + B)$$

$$B^* = X(A + B')$$

(b) list the State Table:

Present State	Inputs		Next State		Output	
A	B	X	Y	A^*	B^*	Z
0	0	0	0	0	0	0
0	0	0	1	0	0	0
0	0	1	0	1	1	0
0	0	1	1	0	1	0
0	1	0	0	0	0	0
0	1	0	1	0	0	0
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	0	0	1
1	0	0	1	0	0	1
1	0	1	0	1	1	1
1	0	1	1	0	1	1
1	1	0	0	0	0	1
1	1	0	1	0	0	1
1	1	1	0	1	1	1
1	1	1	1	1	1	1

(c) Draw the corresponding State diagram

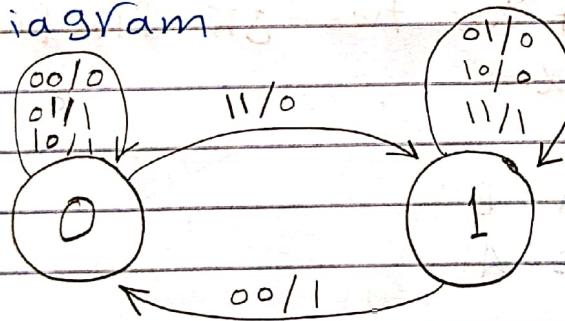


7- A Sequential Circuit has one flip-flop Q , two inputs X and Y , and one output S . It consists of a full-adder circuit connected to a D flip-flop. Drive

State table

Present State	Inputs		Next State	Output
Q	X	Y	$Q(t+1)$	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

State diagram



8- Drive the state table and State diagram of the Sequential Circuit Shown in Fig. P5.8. Explain the function that the circuit Perform.

1- Input equation

$$\overline{T_A} = A + B \quad \overline{T_B} = A' + B$$

2- Characteristic equation

$$A(t+1) = \overline{T_A} \oplus A \quad B(t+1) = \overline{T_B} \oplus B$$

3- Next State equation

$$A(t+1) = (A+B) \oplus A \quad B(t+1) = (A'+B) \oplus B$$

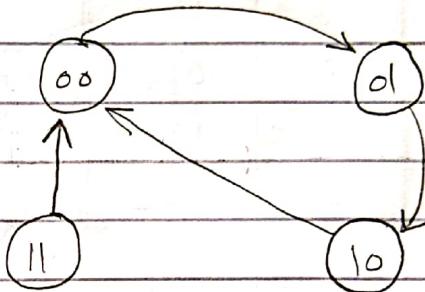
$$\begin{aligned}
 A_{(t+1)} &= (A+B) \oplus A \\
 &= (A+B)A' + (A+B)'A \\
 &= AA' + A'B + (A'B')A \\
 &= 0 + A'B + 0 \\
 &= A'B
 \end{aligned}$$

$$\begin{aligned}
 B_{(t+1)} &= (A'+B) \oplus B \\
 &= (A'+B)B' + (A+B)'B \\
 &= A'B' + BB' + AB'B \\
 &= A'B' + 0 + 0 \\
 &= A'B
 \end{aligned}$$

4-State table

Present state		Next state	
A	B	$A_{(t+1)}$	$B_{(t+1)}$
0	0	0	1
0	1	1	0
1	0	0	0
1	1	0	0

5-State diagram



The circuit performs as a Counter from zero to two.

9-A Sequential Circuit has two JK flip-flops A and B and one output input X.

Input equations:

$$\begin{aligned}
 J_A &= X \\
 K_A &= B \\
 J_B &= X \\
 K_B &= A
 \end{aligned}$$

(a) Drive State equations:-

1- JK Characteristic equations:-

$$A_{(t+1)} = J_A A' + K_A' A \quad B_{(t+1)} = J_B B' + K_B' B$$

2-State equations:-

$$\begin{aligned}
 A_{(t+1)} &= X A' + B' A \\
 &= A' X + A B
 \end{aligned}$$

$$\begin{aligned}
 B_{(t+1)} &= X B' + (A')' A \\
 &= X B' + A A \\
 &= X B' + A \\
 &= A + B' X
 \end{aligned}$$

(B) Draw State diagram:

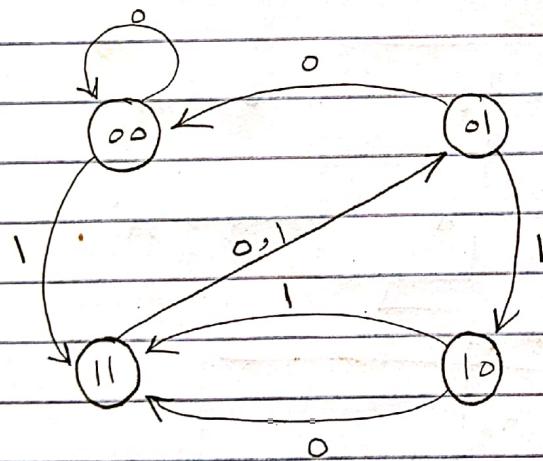
$$A^* = A'X + AB'$$

$$B^* = A + B'X$$

1- State Table

Present State		INPUT X	Next State	
A	B		A(t+1)	B(t+1)
0	0	0	0	0
0	0	1	1	1
0	1	0	0	0
0	1	1	1	0
1	0	0	1	1
1	0	1	1	1
1	1	0	0	1
1	1	1	0	1

2- State diagram



1. A Sequential Circuit has two JK Flip-Flop A and B, two inputs x and y , and one output Z .

1- Input equations:

$$J_A = Bx + B'y'$$

$$K_A = B'xy'$$

$$J_B = A'x$$

$$K_B = A + xy'$$

2- JK FF characteristic equations:

$$A(t+1) = J_A A' + K_A' A$$

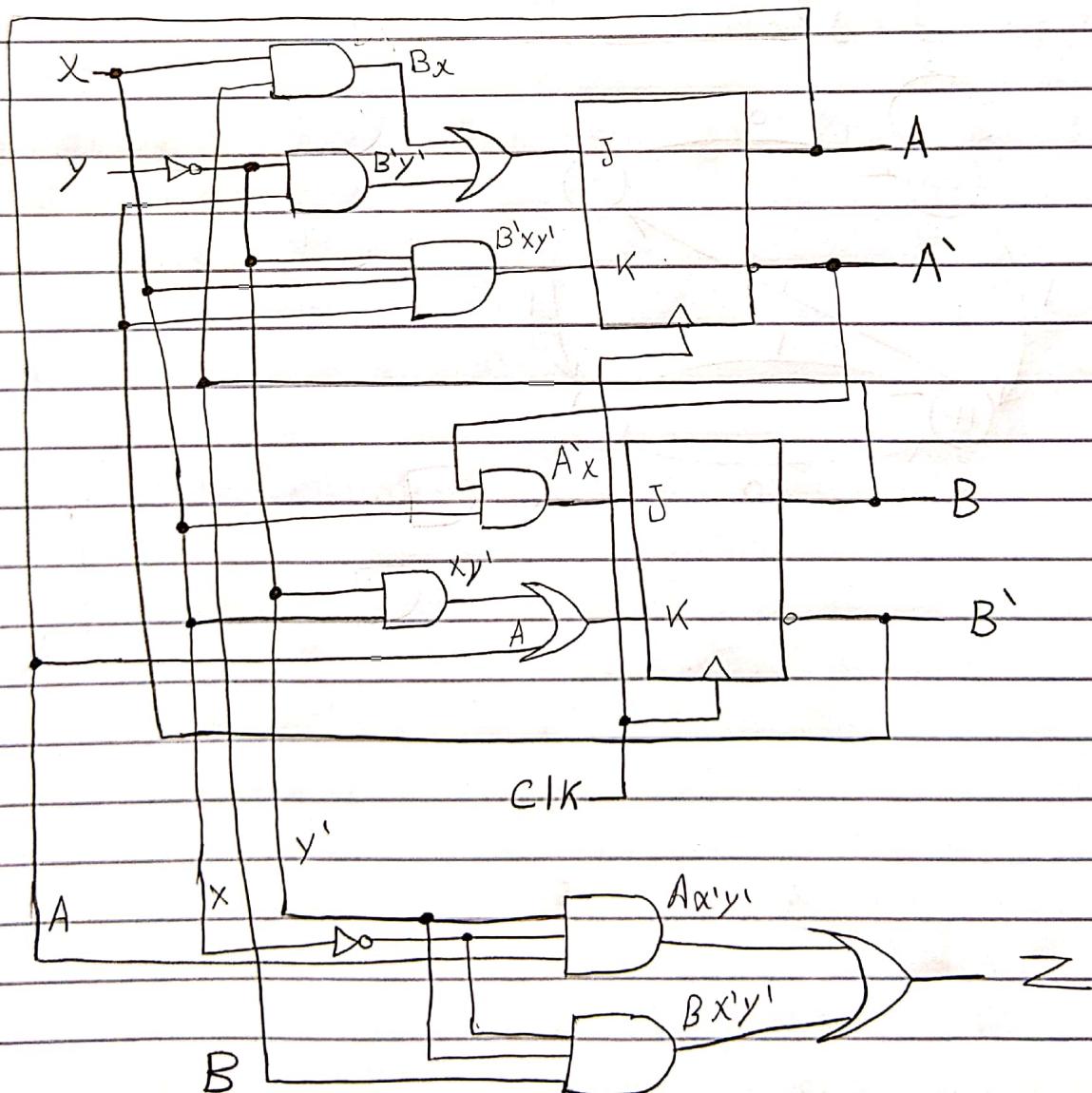
$$B(t+1) = J_B B' + K_B' B$$

3- Output and Next State equations:

$$A(t+1) = (Bx + B'y')A' + (B'xy')A \quad B(t+1) = (A'x)B' + (A + xy')B$$

$$Z = Ax'y' + Bx'y'$$

(a) Draw the logic diagram



(b) Tabulate the state table:-

Present	inputs	Next	output				
A	B	X	Y	A	B	Z	
0	0	0	0	0	0	0	$A = Ax + Ay + Bx + B'y$
0	0	0	1	0	0	0	$B = A'B'x + A'Bx' + A'By$
0	0	1	0	0	1	0	$= A'(B'x + Bx' + By)$
0	0	1	1	1	1	0	
0	1	0	0	0	1	1	
0	1	0	1	1	1	0	$Z = Ax'y' + Bx'y$
0	1	1	0	1	0	0	$= x'y'(A + B)$
0	1	1	1	1	1	0	
1	0	0	0	1	0	1	
1	0	0	1	1	0	0	
1	0	1	0	0	0	0	
1	0	1	1	1	0	0	
1	1	0	0	1	0	1	
1	1	0	1	1	0	0	
1	1	1	0	1	0	0	
1	1	1	1	1	0	0	

(c) State equations for A and B

$$\begin{aligned}
 A &= (Bx + B'y') A' + (B'x'y')' A & B &= (A'x) B' + (A + xy')' B \\
 &= A'Bx + A'B'y' + (B + x' + y) A & &= A'B'x + (A'(x' + y) B \\
 &= A'Bx + A'B'y' + \underline{AB} + Ax' + Ay & &= A'B'x + (A'x' + A'y) B \\
 &= B(A'x + A) + A'B'y' + Ax' + Ay & &= A'B'x + A'Bx' + A'By \\
 &= B(A + x) + A'B'y + Ax' + Ay & & \\
 &= \underline{AB} + \underline{Bx} + A'B'y + \underline{Ax'} + \underline{Ay} & &= \boxed{A'B'x + A'Bx' + A'By} \\
 &= Bx + \underline{A'B'y} + \underline{Ax'} + \underline{Ay} & & \\
 &= Bx + y(A'B' + A) + Ax' & & \\
 &= Bx + y(B' + A) + Ax' & & \\
 &= Bx + yB' + Ay + Ax' & & \\
 &= \boxed{Ax' + Ay + Bx + B'y}
 \end{aligned}$$

15- List a state table for the JK Flip-flop using

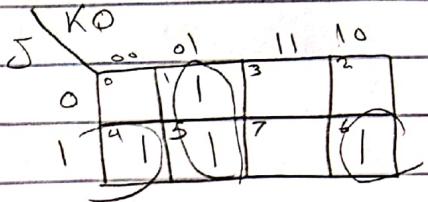
Q as P.S and N.S and J and K as inputs.

Design the sequential circuit specified by the state table.

~~JK flip-flop with Q as P.S and N.S~~

+ State table

INPUTS		P.S	N.S	$Q^* = \sum m(1, 4, 5, 6)$
J	K	Q	$(Q+1)$	
H {	0	0	0	0
	0	1	1	
R {	0	1	0	0
	0	1	1	0
S {	1	0	0	1
	1	0	1	1
Com {	1	1	0	1
	1	1	1	0



$$Q^* = JQ' + K'Q$$

Design the Sequential Circuit :-

