

# WEBSERVER

V:1

HTTP

WEBSERVER  
Web Server routines are used to run and configure services of the embedded web server



ZYNQ

FPGA

ANALOG MODEL

CONTROL SIGNALS I/O  
X CHANNELS ADC DATA I/O

AXI INTERCONNECT

CPU

MAIN  
LWIP\_INIT  
NETIF\_SET\_DEFAULT(NETIF)  
NETIF\_SET\_UP(NETIF)  
PLATFORM\_ENABLE\_INTERRUPTS()  
DHCP\_START(NETIF)  
DHCP\_TIMEOUTCNTR  
TXPERCONNMONCNTR  
XEMACIF\_INPUT(NETIF)  
TCP\_FASTTMR()  
IP4\_ADDR(&(NETIF->IP\_ADDR))  
IP4\_ADDR(&(NETIF->NETMASK))  
IP4\_ADDR(&(NETIF->GW))

TCPXXXXTMRFLAG()  
START\_APPLICATIONS()

INIT GPIO  
INIT PL LOGICS  
HTTP ACCEPT CALLBACKS  
HTTP RECV CALLBACKS

GENERATE RESPONSE

[DO GET]-CLIENT REQUEST	FILE_SYSYEM IN MEMEORY/APPLICATIONS
[DO POST]-SERVER-RESPONSE	CONTROLLING/OBTAINING/STATUS
[DO_404]	404 MESSAGE

ETHERNET PERIPHERAL  
DRIVERS

DDR CONTROLLER

MIO PINS

ETHERNET-PINS

DDR-PINS

ETHERNET

DDR