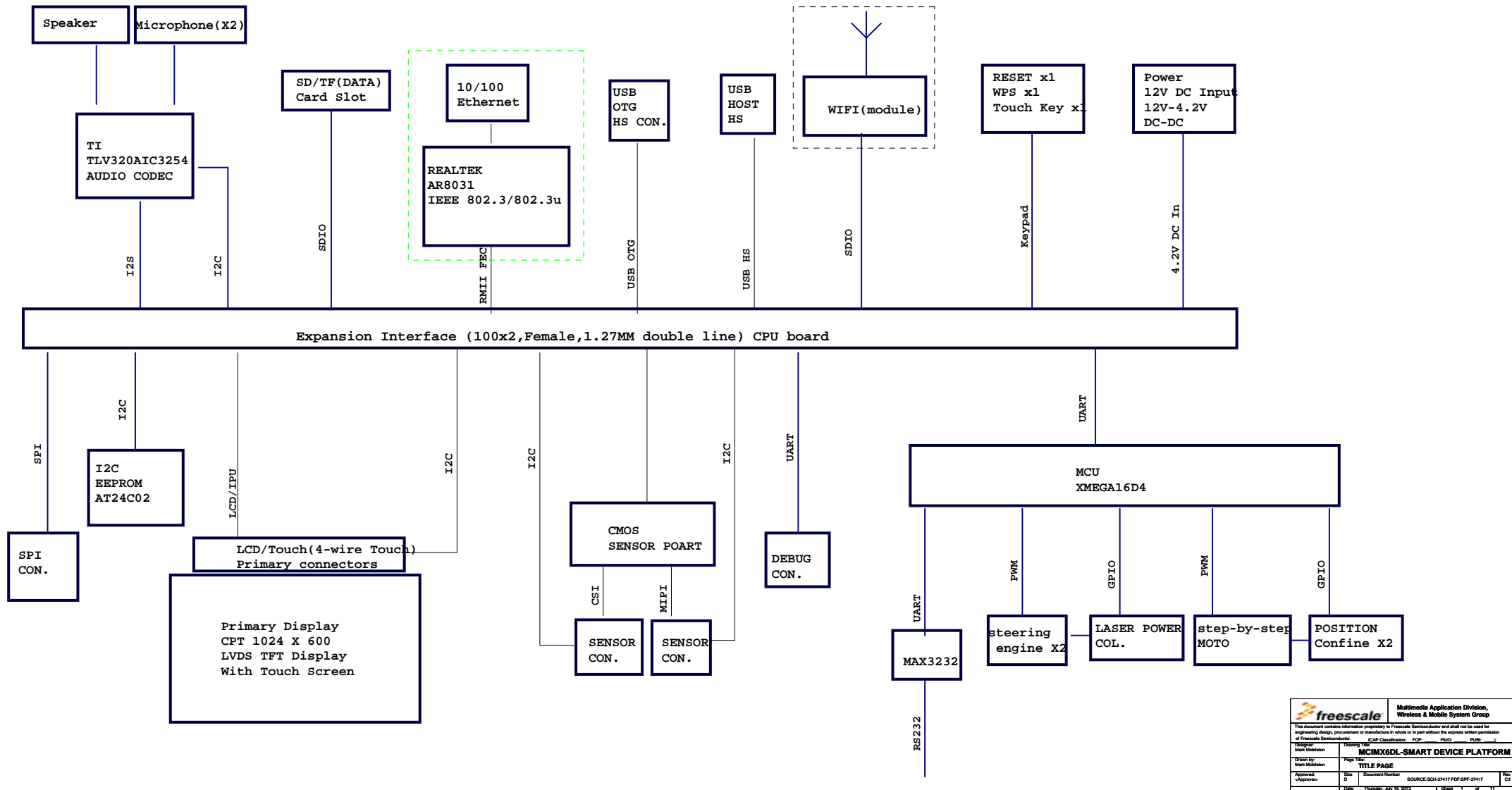
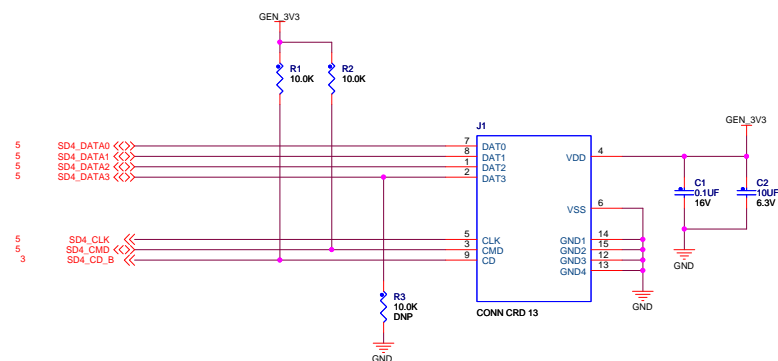


MCIMX6L

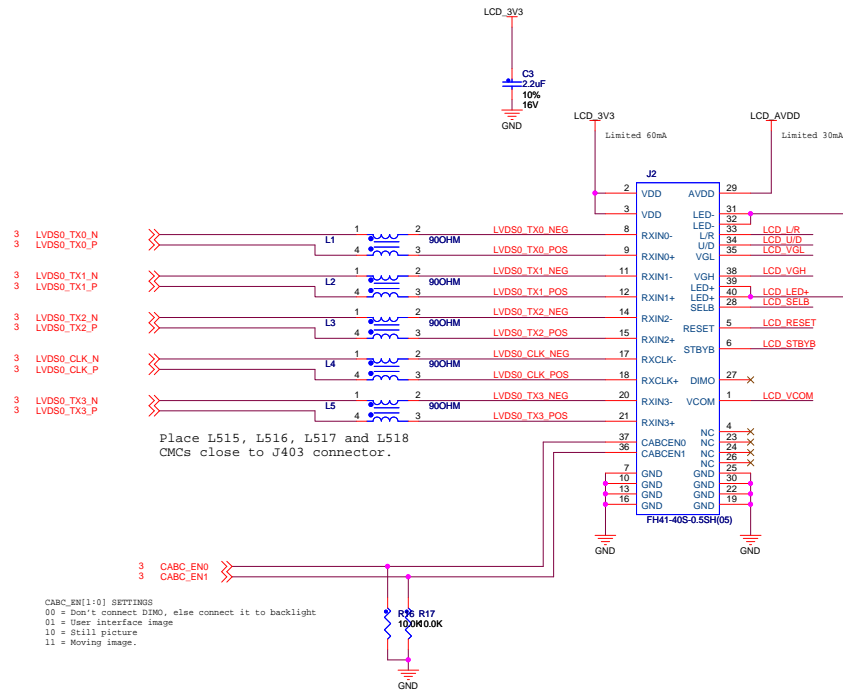


SD CARD SOCKET

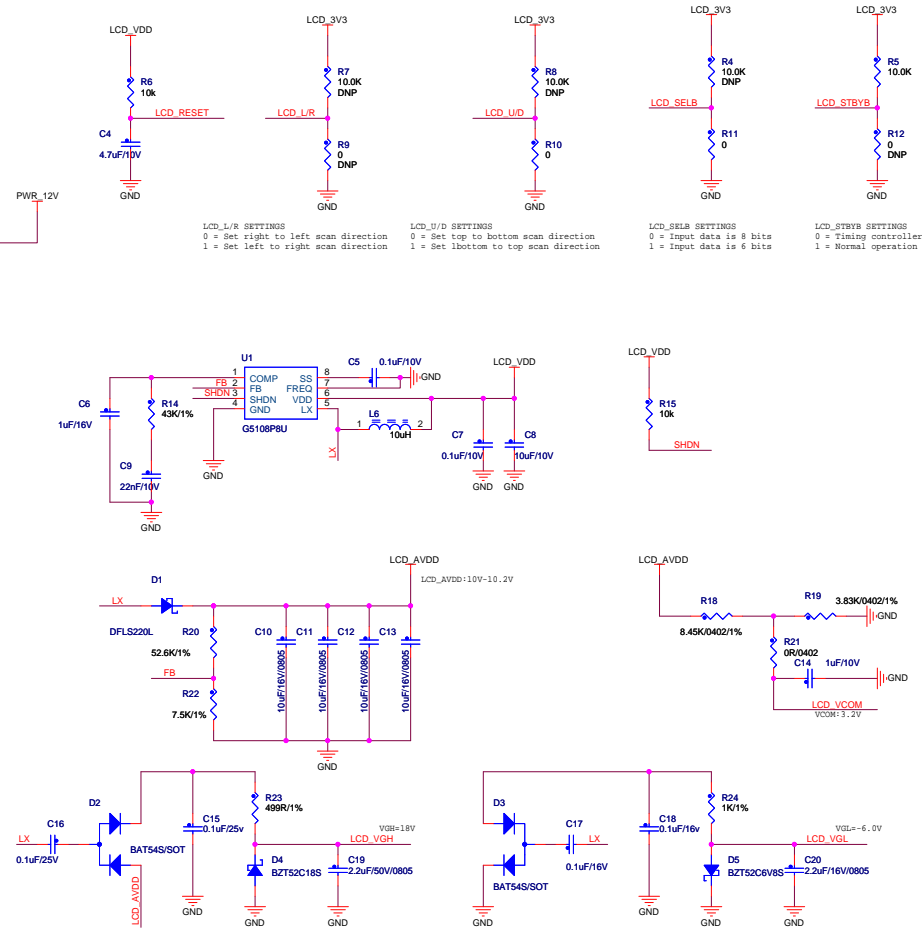
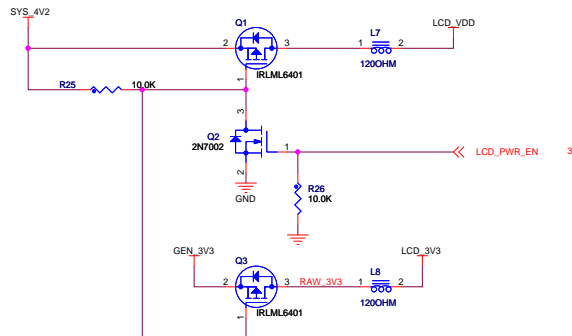


Layout:
50ohm, SD signals(SD_DATAx, SD_CMD, SD_CLK) length equal

LVDS

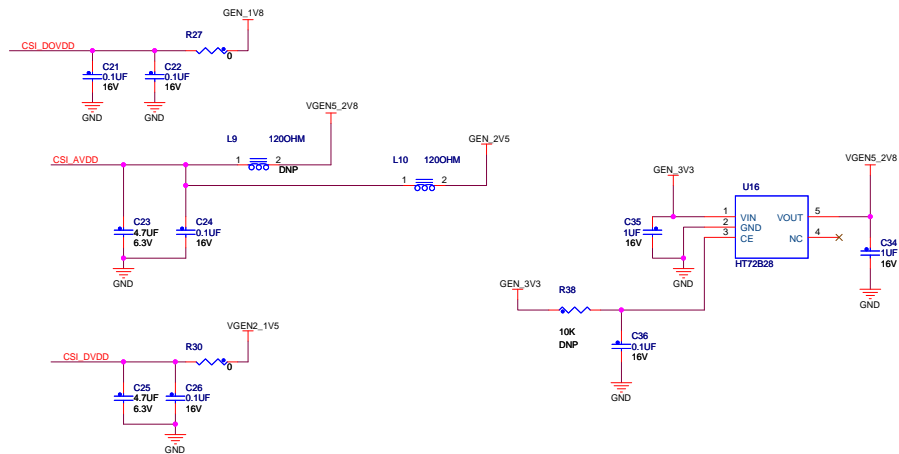
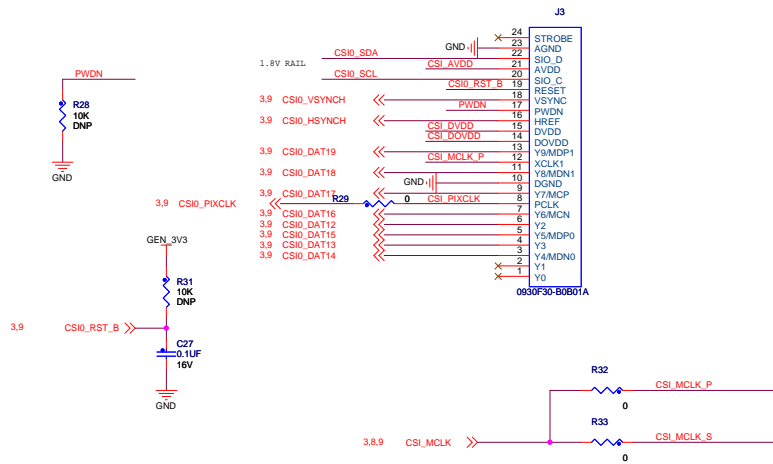


Layout: LVDS 100 ohm differential pairs



CSI CMOS Sensor
OV5642 5M Pixel

Place R180 close to connector J9 to in the event
a reflected signal from camera needs to be supressed.

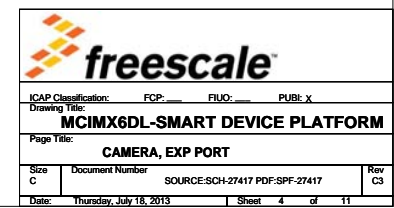
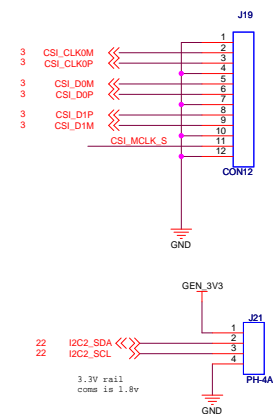


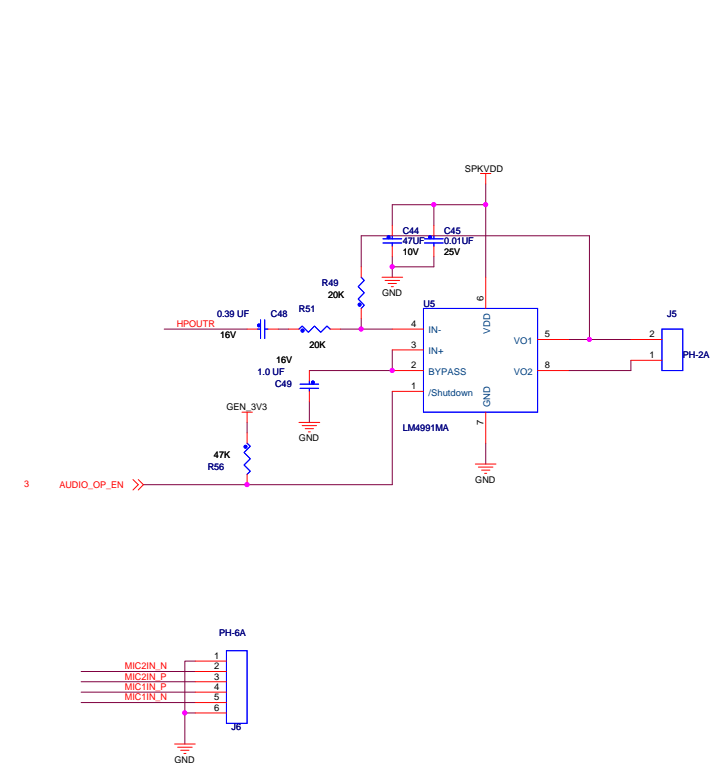
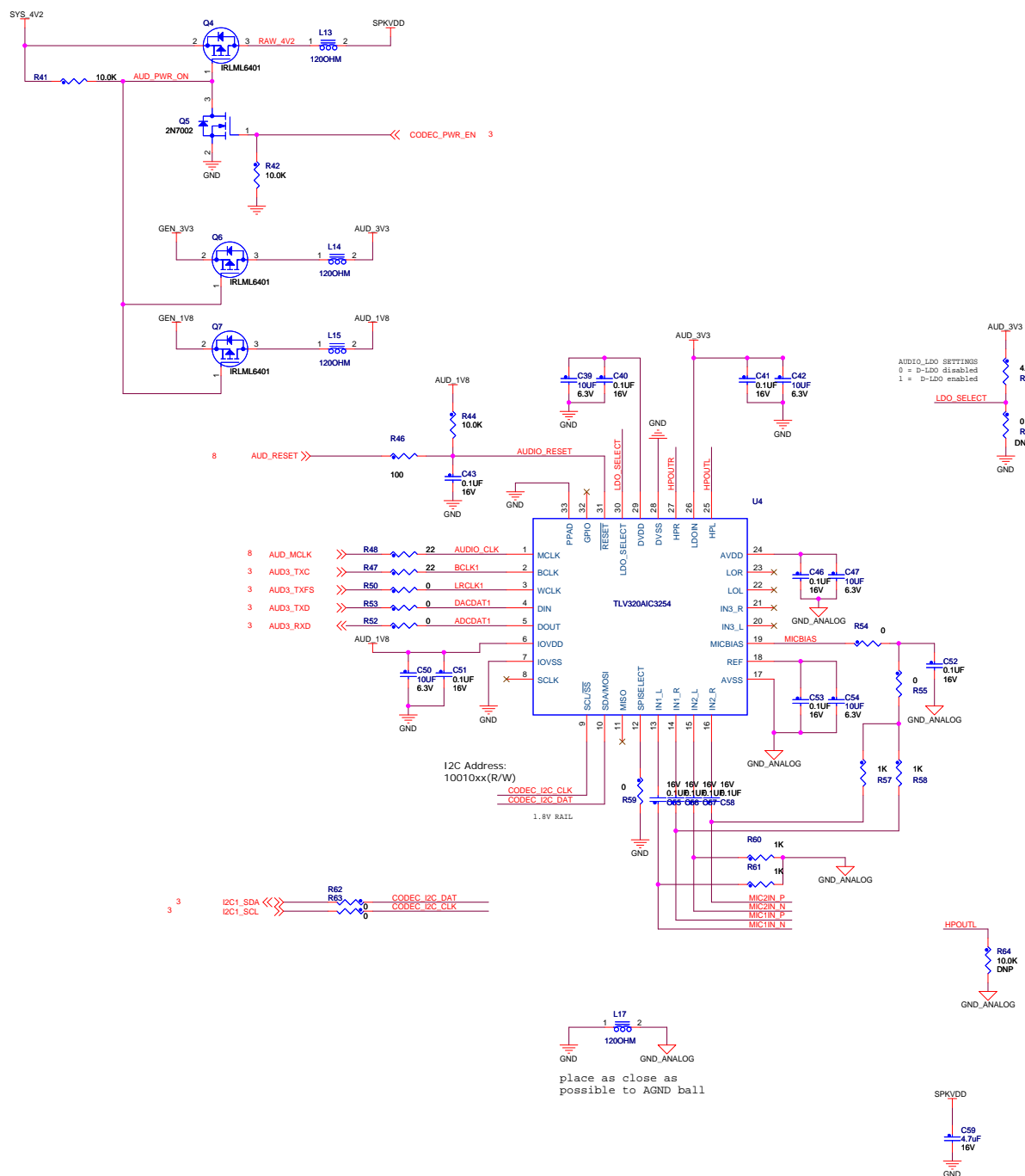
NOTE:
The Camera Analog Power supply has been moved to VGEN3. Freescale SW will program VGEN3 to operate at 2.8V. L25 and L26 are now populated and L10 and L17 are depopulated. See the Freescale HW User Guide for the Smart Device board for details (to be published 4Q12).

Place R158 and R159 near U15.
Acts as source termination.



MIPI CMOS Sensor
OV5640 5M Pixel

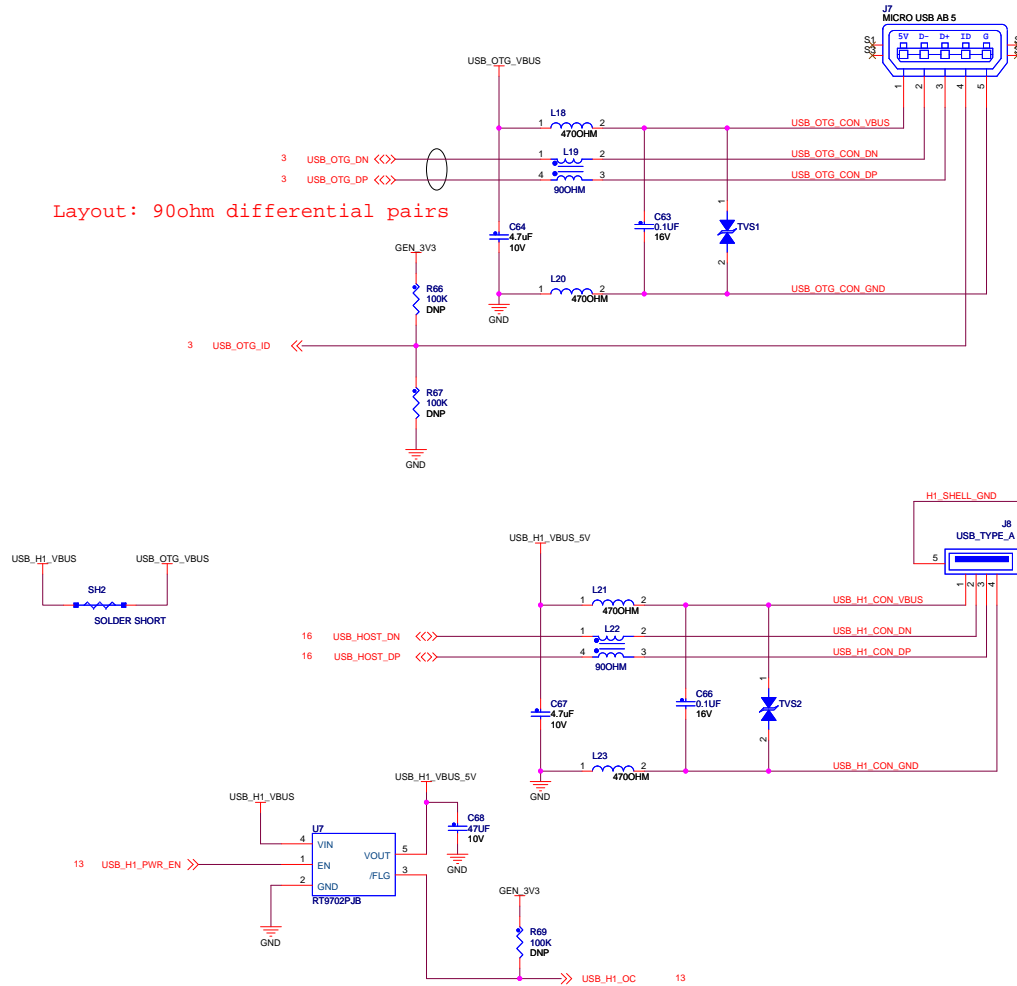




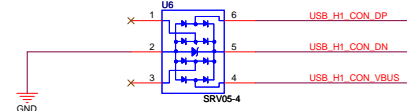
NOTE:
On Rev B4 boards, buffers U500 and U520 are removed, and a wire is added by hand connecting pins 2 and 4 together. On Rev C boards, the layout will be modified to remove the hand rework.

NOTE:
Due to placement requirements on the SABRE SD board set,
layout routing for the USB_OTG_DP and USB_OTG_DN traces
are sub-optimal. It is recommended that customers consider using
the USB layout on the Freescale i.MX6 SABRE AI board as a
better example of proper USB trace routing.

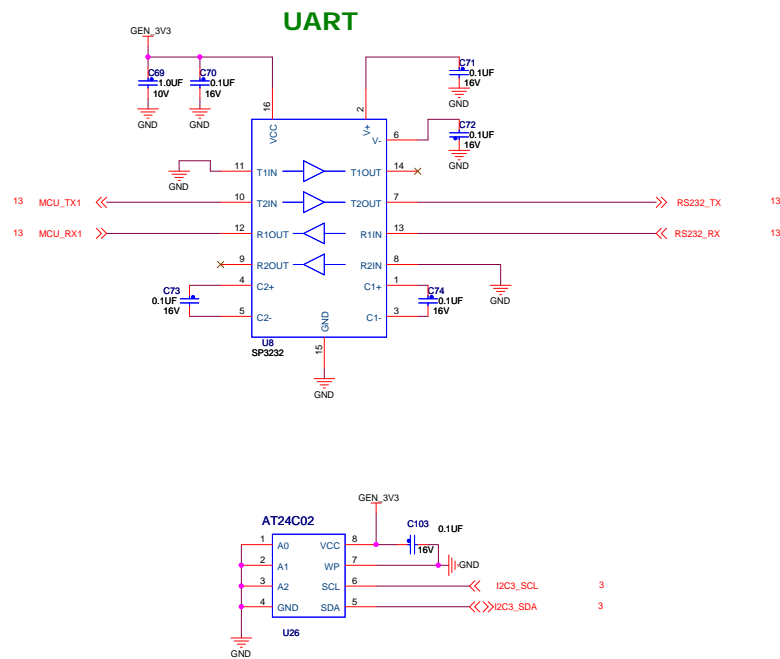
Layout: 90ohm differential pairs



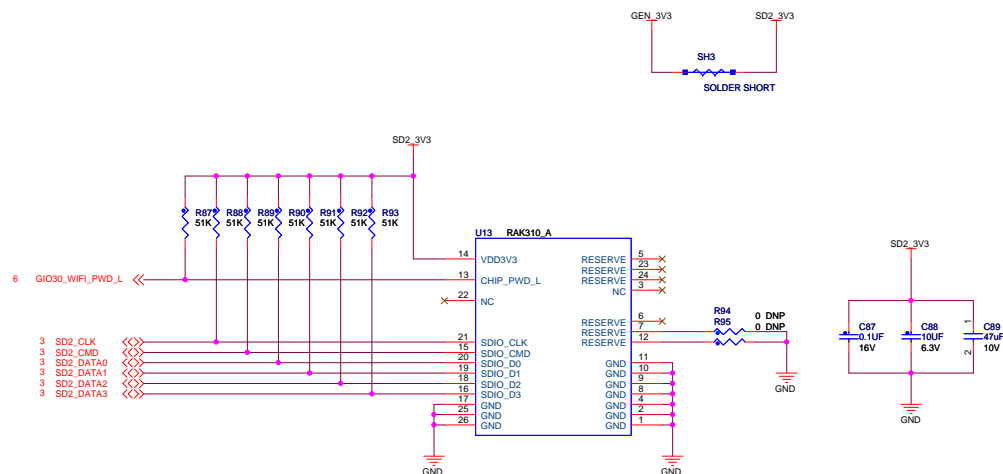
ESD Protection



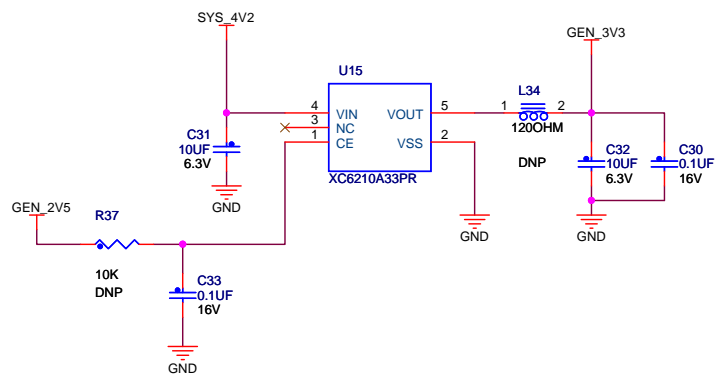
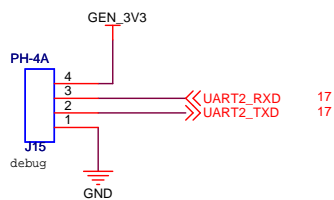
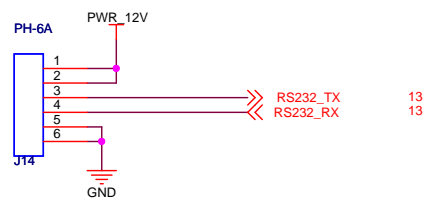
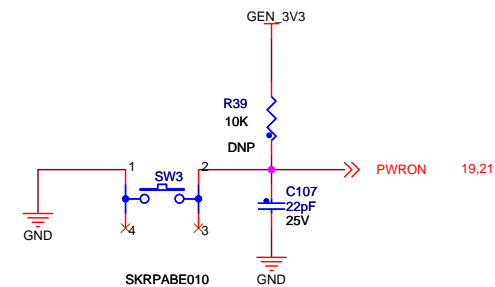
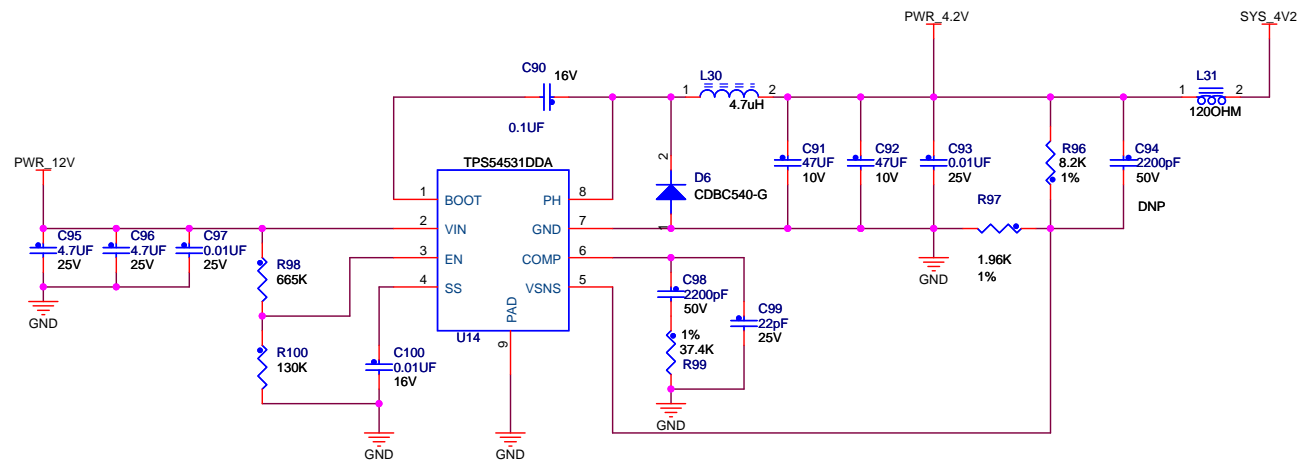
Cut Trace on Rev B1 Boards



AUX SDIO CARD SOCKET

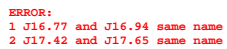


Layout:
50ohm, SD signals(SD_DATAx, SD_CMD, SD_CLK) length equal



ICAP Classification:		FCP:	FIUC:	PUBI: X
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Page Title:				
BATTERY CHARGER				
Size B	Document Number			Rev C3
		SOURCE: SCH-27417 PDF: SPF-27417		
Date:	Thursday, August 15, 2013	Sheet	10	of 11

Mating connector on Base Board



1 If not Android, can be used as GPIO on Main Board. Main Board schematic cross-reference table does not apply to MX6.
2 Could be used as spare GPIO if SPI NOR is not needed.
3 Could be used as spare open-drain GPIO if not needed for local CAN PHY wake up.