Boolean Algebra and Logic Gates

CS207 Chapter 2

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Boolean Algebra



- The previous binary logic is two-valued Boolean algebra.
 - On a set of two elements: 0 and 1.
 - With rules for the three binary operators: +, · and '.
- Common properties: and
 - A + 0 = A and $A \cdot 1 = A$.

 - a boolean variable: o/,

```
• A+1=1 and A\cdot 0=0.
• A+A'=1 and A\cdot A'=0.
• A+A=A and A\cdot A=A.
```



- Closure: A set S is closed with respect to a binary operator if, for every pair of elements of S, the binary operator specifies a rule for obtaining a unique element of S.
- Associative law: A + (B + C) = (A + B) + C and A(BC) = (AB)C.
- Commutative law: A + B = B + A and AB = BA.
- \star is NoT• Identity element: A set S is to have an identity element with respect to a binary operation * on S, if there exists an element $E \in S$ with the property the "AND"
- out represent E * A = A * E = A
 - Element 0 is an identity element of +, and 1 is an identity element of .
 - Distributive law: A(B+C) = AB + AC and A + BC = (A+B)(A+C).
- DeMorgan: (A+B)' = A'B' and (AB)' = A' + B'.

 Absorption: A + AB = A and A(A+B) = A.

 - $A + \overline{A}B = A + B$

But you can regarded it as the

you can prove it.

Distributive Laws:
$$A(B+C) = AB + AC$$

$$A+BC = (A+B)(A+C)$$

$$(A+B)(A+C) = AA + AC + AB + BC$$

$$D AA = A$$

$$NOT 0 !!!!$$

$$\Rightarrow (A+B)(A+C) = A + AC + AB + BC$$

$$\Rightarrow (A+B)(A+C) = A + AC + AB + BC$$

$$\Rightarrow In Boolean Algebra, the order of percedence is Parenthesis
$$NOT$$

$$AND$$

$$OR$$

$$\Rightarrow A+AB$$$$

$$= A(1+B)$$
as $1+B=1$

$$A + AB = A$$

$$\Rightarrow$$
 (A+B)(A+C)

$$= AA + AC + AB + BC$$

$$= A + AC + AB + BC$$
$$= A + BC$$

Absorption Laws:
$$A+AB=A$$
 $proof: A+AB=A(I+B)$
 $=A$

$$A + AB = A + B$$
 $A + \overline{A}B = A + (A + B')'$
 $= [A' \cdot (A + B')]'$
 $= [A' \cdot (A + B')]'$

= A+B

Duality property

A duality is a situation in which two opposite ideas or feelings exist at the same time.

If you deduce something or deduce that something is true, you reach that conclusion because of other things that you know to be true.



- Every algebraic expression deducible from the postulates of Boolean algebra remains valid if the operators and identity elements are interchanged.
- Change + to · and vice versa. Use versa is used to indicate that the Vice versa is used

A vice is a habit which is regarded as a weakness in someone's character, but not reverse of what you usually as a serious fault.

Change 0 to 1 and vice versa, have said is true.

$$\bullet \ A + A' = 1 \to A \cdot A' = 0.$$

$$\bullet \ A + B = B + A \to AB = BA.$$

•
$$A(B+C) = AB + AC \to A + BC = (A+B)(A+C)$$
.

•
$$(A+B)' = A'B' \to (AB)' = A' + B'$$
.

Boolean function



- Binary variables have two values, either 0 or 1.
- A Boolean function is an expression formed with *binary variables*, the two *binary operators* **AND** and **OR**, one *unary operator* **NOT**, *parentheses* and *equal sign*.
- The value of a function may be 0 or 1, depending on the values of variables present in the Boolean function or expression.
- Example: F = AB'C. 3 Intervals
 - F = 1 when A = C = 1 and B = 0,
 otherwise F = 0.
 - MAT AND AND

Boolean function



• Boolean functions can also be represented by truth tables.

Tabular form of the values of a Boolean function according to the all possible values of its variables.

• n number of variables $\rightarrow 2^n$ combinations of 1's and 0's

One column representing function values according to the different combinations.

• Example: F = AB + C.

Independent, simple components of a logical statement are represented by either lowercase or capital letter variables. These variables are "independent" in that each variable can be either true or false independently of the others, and a truth table is a chart of all of the possibilities.

A	B	C	F
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

header

A header is text such as a name or a page number that can be automatically displayed at the top of each page of a prioted document. Compare footer.

main body

Boolean function simplification



- A Boolean function from an algebraic expression can be realized to a logic diagram composed of logic gates.
- Minimization of the number of literals and the number of terms leads to less complex circuits as well as less number of gates.
 - We first try use postulates and theorems of Boolean algebra to simplify.

F =
$$AB + BC + BC$$
 F = $A'B'C + A'BC + AB'$ F = $XYZ + XY'Z + XYZ'$
= $AB + C(B + B')$ = $A'C(B' + B) + AB'$ = $XZ(Y + Y') + XY(Z + Z')$
= $AB + C$ | = $A'C + AB'$ = $XZ + XY = X(Y + Z)$

 Each Boolean function has one representation in truth table, but a variety of ways in algebraic form.

Algebraic manipulation = boolean function Simplification



- Reduce the total number of terms and literals. bject
- Usually not possible by hand for complex functions, use computer minimization program.
- More advanced techniques in the next lectures.

If you manipulate something that requires skill, such as a complicated piece of equipment or a difficult idea, you operate it or process it.

If you say that someone manipulates people, you disapprove of them because they skilfully force or persuade people to do what they want.

Boolean function complement

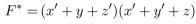


- Complement a Boolean function from F to F'.
 - Change 0's to 1's and vice versa in the truth table.
 - Use Use DeMorgan's theorem for multiple variables.
- Example: F = x'yz' + x'y'z.

consisting of two parts, elements, or aspects: Dual:

Complement:

F' = (x'yz' + x'y'z)' = (x'yz')'(x'y'z)' = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z') = (x + y' + z)(x + y + z')



Canonical forms



- Logical functions are generally expressed in terms of different combinations of logical variables with their true forms as well as the complement forms: x and x'.
- An arbitrary logic function can be expressed in the following forms, called canonical forms:
 - Sum of products (SOP), and
 - Product of sums (POS).
- What are the products and sums?

If something has canonical status, it is accepted as having all the qualities that a thing of its kind should have.

Canonical forms

If one thing corresponds to another, there is a close similarity or connection between them. You can also say that two things correspond.



- The logical product of several variables on which a function depends is considered to be a product term.
- considered to be a product term.

 Special

 Called minterns when all variables are involved: For x and y, xy, xy', and x'y' product term are a light princerms. Eq. 3 variable x b.c.: 8 minterns
 - The logical sum of several variables on which a function depends is considered to be a sum term.
- Called maxterms when all variables are involved: For x and y, x + y, x' + y, x + y', and x' + y' are all the maxterms.
 - SOP: The logical sum of two or more logical product terms is referred to as a sum of products expression.

 sum of mintern (cononical sop)
 - POS: The logical product of two or more logical sum terms is referred to as a product of sums expression.

In Sum of Products (what you call ANDs) only one of the minterms must be true for the expression to be true. In **Product of Sums** (what you call ORs) all the maxterms must be true for the expression to be true.

In Sum Of Products (SOP), each term of the SOP expression is called a "minterm" because, say, an **SOP** expression is given as: F(X,Y,Z) = X'.Y'.Z + X.Y'.Z' + X.Y'.Z + X.Y.Z

for this **SOP** expression to be "1" or true (being a **positive logic**), ANY of the term of the expression should be 1. thus the word "minterm".

they are called "minterms". On the other hand, In Product Of Sum (POS), each term of the POS expression is called a

"maxterm" because. say an **POS** expression is given as: F(X,Y,Z) = (X+Y+Z).(X+Y'+Z).(X+Y'+Z').(X'+Y'+Z')

for this **POS** expression to be "0" (because **POS** is considered as a **negative logic** and we consider **0** terms). **ALL** of the terms of the expression should be 0. thus the word "max term"!!

i.e for F(X,Y,Z) to be 0, each of the terms (X+Y+Z), (X+Y'+Z), (X+Y'+Z') and (X'+Y'+Z) should be

i.e. anv of the term (X'Y'Z), (XY'Z'), (XY'Z) or (XYZ) being 1, results in F(X,Y,Z) to be 1!! Thus

equal to "0", otherwise F won't be zero!!

Minterms





- If you possess something, you have it or own it.
- In the minterm, a variable will possess the value 1 if it is in true or uncomplemented form, whereas, it contains the value ∅ if it is in complemented form.
- It possesses the value of 1 for only one combination of n input variables
 - The rest of the $2^n 1$ combinations have the logic value of 0.

Minterms





• Canonical SOP expression, or sum of minterms: A Boolean function expressed as the logical sum of all the minterms from the rows of a truth table with value

as the logical sufficient	I LITE	minte	11115 1	וווווווו	ie rows or a trut	ii table witti va
1. F=AB+C 对应 trush table 中 F 所在 column	~	input		output	t	
在 A.B. (为 同 - 行 的 值 所 得 的 F 值	A	B	C	F	Minterms	
m Minterms column 罗出的式子来自于左侧 3 到		0			ALDIGI (-)	
中 A.B.(为o时 取 complement. 为1 时取 time value	0	0	0		A'B'C' (2003) - (0)(0
所冒出的 minterm 式子.	0	0	1	\odot	A'B'C	
	0	1	0	0	A'BC'	
欲将F用 (anonical SoP 去进行 卷示,	0	1	1	\bigcirc	A'BC	
找到 truth table 中 F=1 时 A.B.C 值 对应的 minferm	1	0	0	0	AB'C'	
并将其相加即可	1	0	1		AB'C	
	1	1	0	(1)	ABC'	mixtem
	1	1	1	4	$ABC (III)_{2} = (1)_{10}$	
<u> </u>		h	1,		<u> </u>	represent su
• $F = AB + C = A'B'C$	+A	'BC +	AB'	C + A	BC' + ABC = 1	$\sum (1,3,5,6,7).$
 A compact form by I minterms. 	istin	g the co	orresp	onding	decimal-equivale	ent codes of the

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· 我们 日下使 F=1 6 行 对应的 minterm 且截的 canonical SOP. 这个SOP即FBSOP表示. 在F=1 份行中. 行上 literals 对应必值度 F=1. 同时也使举行的 minterm =1. 且这见literals保值只定本行及minterm=1. 其他行及minterm=0. The sop to sum of product. sum = "+" = OR 由予企些 minterm 之间的 oR. 所以只要有一个 minterm = 1. 则F=1. 而在每·但供F=1 As literals 值中. 都有且仅有唯一minterm=1. 即这但literals af应思minterm. 友到mintermed为法 F= \(\(\(\) \) 到知言章及逐况是从哪里来的了. 注意比处设序多系从的开始标 l literals 络夏北美似 Gray Code

Minterms



- The canonical sum of products form of a logic function can be obtained by using the following procedure.

 To retain something means to
 - 1 Check each term in the given logic function. Retain if it is a minterm, continue to examine the next term in the same manner.
 - 2 Examine for the variables that are missing in each product which is not a minterm.
 - 3 If the missing variable in the minterm is X, multiply that minterm with (X + X').
 - Example: $A + B \rightarrow A(B + B') + B(A + A')$
 - 4 Multiply all the products and discard the redundant terms.

Minterms



• Example: F(A, B, C, D) = AB + ACD.

$$F(A, B, C, D) = AB + ACD$$

$$= AB(C + C')(D + D') + ACD(B + B')$$

$$= (ABC + ABC')(D + D') + ABCD + AB'CD$$

$$= ABCD + ABCD' + ABC'D + ABC'D' + ABC'D + AB'CD$$

$$= \overline{ABCD} + ABCD' + ABC'D + ABC'D' + \overline{AB'CD}$$

O check we have how many variable double

+X means

x OR X

=> simply it which a

2 rewrite: look up the tems

if is mintern or not : AB:X ACD:X

3 multiply the missing variable with (8+xi)=1

Maxterms



 In the maxterm, a variable will possess the value 0, if it is in true or uncomplemented form, whereas, it contains the value 1, if it is in complemented form.

\overline{A}	B	C	Maxterm
0	0	0	A+B+C
0	0	1	A + B + C'
0	1	0	A + B' + C
0	1	1	A + B' + C'
1	0	0	A' + B + C
1	0	1	A' + B + C'
minterm: \	1	0	A' + B' + C
11	1	1	A' + B' + C'
\ <u>\</u>			

Ψ	
minterm :	1 → un complement
	0 => complement

- It possesses the value of \emptyset for only one combination of n input variables
 - The rest of the $2^n 1$ combinations have the logic value of 1.

Maxterms



- Canonical POS expression, or product of maxterms: A Boolean function expressed as the logical product of all the maxterms from the rows of a truth table with value 0.
- table with value 0. binary zero $F = (A + B + C)(A + B' + C)(A' + B + C') = \prod_{i=1}^{n} (0, 2, 5).$
 - A compact form by listing the corresponding decimal-equivalent codes of the maxterms.

Maxterms



• Example: $F(A, B, C, \mathbf{k}) = A + B'C$.

$$\begin{split} F(A,B,C,\mathbf{N}) &= A+B'C \quad \text{no maxterm} \quad \text{the sun of product} \\ &= (A+B')(A+C) \quad \text{rewrite to product form} \\ &= (A+B'+CC')(A+C+BB') \quad \text{and} \quad \text{and}$$

Derive from a truth table



					F=1	F=o
a row will in	A	B	C	F	Minterm	Maxterm
	0	0	0	0		A+B+C
either mintem of	0	0	1	0		A + B + C'
maxtem.	0	1	0	1	A'BC'	
mak (ont)	0	1	1	0		A + B' + C'
But NOT in both	1	0	0	1	AB'C'	
	1	0	1	1	AB'C	
or in rone	1	1	0	1	ABC'	
	1	1	1	0		A' + B' + C'

- The final **canonical SOP** for the output *F* is derived by summing or performing an **OR** operation of the four product terms as shown below:
 - $F = A'BC' + AB'C' + AB'C + ABC' = \sum (2, 4, 5, 6)$.
- The final **canonical POS** for the output *F* is derived by summing or performing an **AND** operation of the four sum terms as shown below:

•
$$F = (A + B + C)(A + B + C')(A + B' + C')(A' + B' + C') = \prod_{i=1}^{n} (0, 1, 3, 7).$$

Mintens	&	Maxterms
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1) derive canonical SOP & POS of F canonical SOP: wite the sum of mintern on which row F=1.

canonical POS: write she product of maxtern on which row F = 0.

why: As SOP is the SUM (OR), so if there exist 1, F=1. But in OR, if there is a O, F might NOT equals O. i.e. As F is a sum, a I can determine its value, but a 0 coult.

But for a Pos, a o can determine F to be o.

Why: As mintern is the logical product of all variables, it is a PRODUCT.

Conversion between minterms and maxterms



- Minterms are the complement of corresponding maxterms: $m_i = M'_i$.
 - Example: A' + B' + C' = (ABC)'.

$$F(A,B,C) = \sum (2,4,5,6) = m_2 + m_4 + m_5 + m_6$$

$$= A'BC' + AB'C' + AB'C + ABC'$$

$$F'(A,B,C) = \sum (0,1,3,7) = m_0 + m_1 + m_3 + m_7$$

$$F(A,B,C) = (F'(A,B,C))' = (m_0 + m_1 + m_3 + m_7)'$$

$$= m'_0 m'_1 m'_3 m'_7$$

$$= M_0 M_1 M_3 M_7$$

$$= \prod (0,1,3,7)$$

$$= (A+B+C)(A+B+C')(A+B'+C')(A'+B'+C').$$

Other logic operators



• When the binary operators AND and OR are applied on two variables A and B, they form two Boolean functions AB and A+B respectively.

ME

you have an intuitive idea or feeling about something, you feel that it is true although you have no evidence or proof of it.

to human

being

Other logic operators



• When the three operators AND, OR, and NOT are applied on two variables A and B, they form 16 Boolean functions:

শ	Y	F=>	۲۲,
0	o	0/1	
0	. 1	'	
١	0		
- 1	1 '		
		n swey	
2	4 - 1/	b kin	di 🔨
_			

Boolean Functions	Operator Symbol	Name	Comments
$F_0 = 0$		Null	Binary constant 0
$F_1 = xy$	$x \cdot y$	AND	x and y
$F_2 = xy'$	x/y	Inhibition	x, but not y
$F_3 = x$		Transfer	x
$F_4 = x'y$	y/x	Inhibition	y, but not x
$F_5 = y$		Transfer	у
$F_6 = xy' + x'y$	$x \oplus y$	Exclusive-OR	x or y, but not both
$F_7 = x + y$	x + y	OR	x or y
$F_8 = (x + y)'$	$x \downarrow y$	NOR	Not-OR
$F_9 = xy + x'y'$	$(x \oplus y)'$	Equivalence	x equals y
$F_{10} = y'$	y'	Complement	Not y
$F_{11} = x + y'$	$x \subset y$	Implication	If y, then x
$F_{12} = x'$	x'	Complement	Not x
$F_{13} = x' + y$	$x \supset y$	Implication	If x , then y
$F_{14} = (xy)'$	$x \uparrow y$	NAND	Not-AND
$F_{15} = 1$		Identity	Binary constant 1

Digital logic gates



- As Boolean functions are expressed in terms of AND, OR, and NOT operations, it is easier to implement the Boolean functions with these basic types of gates.
 - It is possible to construct other types of logic gates.
- The following factors are to be considered for construction of other types of gates.
 the state or degree of being easily or conveniently done:
 - The **feasibility** and economy of producing the gate with physical parameters.
 - The possibility of **extending** to more than two inputs.
 - The basic properties of the binary operator such as commutability and associability.
 - The ability of the gate to implement Boolean functions alone or in conjunction with other gates.

instance of two or more events or things occurring at the same point in time or space

Digital logic gates



			A	В	F
			0	0	0
AND	$ \begin{array}{ccc} A & & \\ $	0	1	0	
AND	B-	F = AD	1	0	0
		1	1	1	
			0	0	0
OR	A - F	F = A + B	0	1	1
OK	B-1		1	0	1
			1	1	1
NOT	A- F	F = A'	0	-	1
INU I		$\Gamma = A$	1	-	0
ge Duffo	$r A \longrightarrow F$	E = A	0	-	0
bulle ———		$\Gamma = A$	1	-	1

Digital logic gates

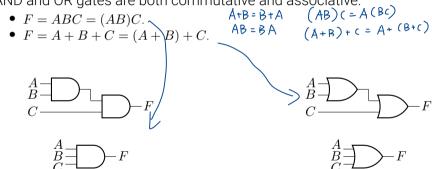


				A	B	F
				0	0	1
	NAND	$A = \bigcap_{B = A} F$	E = (AD)I	0	1	1
	NAND	B-	$F = (AB)^r$	1	0	1
				1	1	0
	make	SR latches		0	0	1
	NOD		F = (A + B)'	0	1	0
	NOR	$B - \mathcal{L}^{r}$	$F = (A + B)^r$	1	0	0
				1	1	0
NAND. NOR. X	0 R &			0	also alled	0
QXOR 有特殊 运车符号 ①	* VOD	$A \rightarrow F$	F = AB' + A'B	0	different	1
远弄符号 A	, VOK	B + L	$=A\oplus B$	1	checker .	1
,	n birole	expensive to 7	nanu facture	1	only ARB	0
				•	: 90C + #	Doduce

Multiple input logic gates



- A gate can be extended to have multiple inputs if its binary operation is commutative and associative.
- AND and OR gates are both commutative and associative.



Multiple input logic gates



- The NAND and NOR functions are the complements of AND and OR functions respectively. A+B=B+A
 - They are commutative, but not associative. \iff AND & OR are both com & ass
 - $((AB)'C)' \neq (A(BC)')'$: does not follow associativity.
 - $((A+B)'+C)' \neq (A+(B+C)')'$: does not follow associativity.
- We modify the definition of multi-input NAND and NOR:

$$A = B = C$$

$$C = A + B + C'$$

$$A = B = C$$

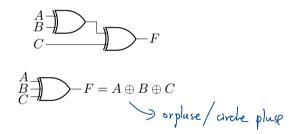
$$C = A + B + C'$$

$$A = A'B'C'$$

Multiple input logic gates



- The XOR gates and equivalence gates both possess commutative and associative properties.
 - Gate output is low when even numbers of 1's are applied to the inputs, and when the number of 1's is odd the output is logic ∅.
 - Multiple-input exclusive-OR and equivalence gates are uncommon in practice.

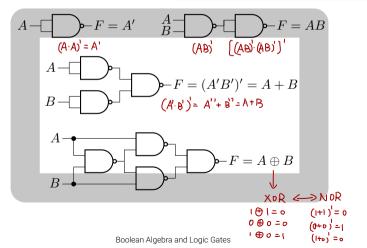


Κ

Universal gates If you describe a task or problem as tricky, you mean that it is difficult to



- NAND gates and NOR gates are called *universal gates* or *universal building* blocks.
 - Any type of gates or logic functions can be implemented by these gates.



$$A+B: [(A+B)^{1}+(A+B)^{1}]^{1} = (A+B)^{1}.(A+B)^{1} = A+B$$

$$A \cdot B : \left(A^{1} + B^{2}\right)^{1} = A^{2} \cdot B^{2} = A \cdot B$$

Universal gates



- Universal gates are easier to fabricate with electronic components.
 - Also reduce the number of varieties of gates.
- Example: F = AB + CD requires two AND and one OR gates.
 - Or three NAND gates.
 - F = AB + CD = ((AB + CD)')' = ((AB)'(CD)')'

