

```

; ===== B E G I N N I N G   O F   P R O C E D U R E =====

; Variables:
;   saved_fp: 0
;   var_8: int64_t, -8
;   var_10: int64_t, -16
;   var_18: int64_t, -24
;   var_20: int64_t, -32
;   var_28: int64_t, -40
;   var_30: int64_t, -48
;   var_38: int64_t, -56
;   var_40: int64_t, -64
;   var_44: int32_t, -68
;   var_48: int32_t, -72
;   var_4C: int32_t, -76

_sprr_test:
sub     sp, sp, #0x80 ; CODE XREF=_main+884
stp     x29, x30, [sp, #0x70]
add     x29, sp, #0x70
stur    x0, [x29, var_8]
stur    x1, [x29, var_10]
adrp    x0, #0x100003000 ; 0x100003989@PAGE
add     x0, x0, #0x989 ; 0x100003989@PAGEOFF, "Jumped to sprr_test.. Step 2...\n"
bl      imp__stubs_printf ; printf
bl      imp__stubs_clock ; clock
stur    x0, [x29, var_18]
adrp    x0, #0x100003000 ; 0x1000039aa@PAGE
add     x0, x0, #0x9aa ; 0x1000039aa@PAGEOFF, "Now at sprr_test before uint64_t a, b\n"
bl      imp__stubs_printf ; printf
adrp    x8, #0x100003000 ; 0x1000039d1@PAGE
add     x8, x8, #0x9d1 ; 0x1000039d1@PAGEOFF, "Completed at sprr_test after uint64_t a, b\n"
mov     x0, x8
bl      imp__stubs_printf ; printf
adrp    x8, #0x100003000 ; 0x1000039fd@PAGE
add     x8, x8, #0x9fd ; 0x1000039fd@PAGEOFF, "Now at sprr_test before a = read_sprp_perm()\n\n"
mov     x0, x8
bl      imp__stubs_printf ; printf
bl      _read_sprp_perm ; _read_sprp_perm
stur    x0, [x29, var_20]
adrp    x0, #0x100003000 ; 0x100003a2c@PAGE
add     x0, x0, #0xa2c ; 0x100003a2c@PAGEOFF, "Completed at sprr_test following a = read_sprp_perm()\n\n"
bl      imp__stubs_printf ; printf
ldur    x8, [x29, var_10]
mov     x0, x8
bl      _write_sprp_perm ; _write_sprp_perm
adrp    x0, #0x100003000 ; 0x100003a64@PAGE
add     x0, x0, #0xa64 ; 0x100003a64@PAGEOFF, "Completed at sprr_test following write_sprp_perm(v)\n\n"
bl      imp__stubs_printf ; printf
adrp    x8, #0x100003000 ; 0x100003a9a@PAGE
add     x8, x8, #0xa9a ; 0x100003a9a@PAGEOFF, "Now at sprr_test before b = read_sprp_perm()\n\n"
mov     x0, x8
bl      imp__stubs_printf ; printf
bl      _read_sprp_perm ; _read_sprp_perm
stur    x0, [x29, var_28]
adrp    x0, #0x100003000 ; 0x100003ac9@PAGE
add     x0, x0, #0xac9 ; 0x100003ac9@PAGEOFF, "Finished at sprr_test after b = read_sprp_perm()\n\n"
bl      imp__stubs_printf ; printf
ldur    x8, [x29, var_28]
ldur    x9, [x29, var_8]
mov     x0, x9
str     x8, [sp, #0x70 + var_40]
bl      _can_read ; _can_read
movz    w10, #0x2d
movz    w11, #0x72
tst     w0, #0x1
csel    w11, w11, w10, ne
ldur    x0, [x29, var_8]
str     w10, [sp, #0x70 + var_44]
str     w11, [sp, #0x70 + var_48]
bl      _can_write ; _can_write
movz    w10, #0x77
tst     w0, #0x1
ldr     w11, [sp, #0x70 + var_44]
csel    w10, w10, w11, ne
ldur    x0, [x29, var_8]
str     w10, [sp, #0x70 + var_4C]
bl      _can_exec ; _can_exec
movz    w10, #0x78
tst     w0, #0x1
ldr     w11, [sp, #0x70 + var_44]
csel    w10, w10, w11, ne
adrp    x0, #0x100003000 ; 0x100003afc@PAGE
add     x0, x0, #0xafc ; 0x100003afc@PAGEOFF, "Register Value:%llx: %c%c%c\n"
mov     x8, sp
ldr     x9, [sp, #0x70 + var_40]
str     x9, [x8]
ldr     w12, [sp, #0x70 + var_48]
mov     x1, x12
str     x1, [x8, #0x8]
ldr     w13, [sp, #0x70 + var_4C]
mov     x1, x13
str     x1, [x8, #0x10]
mov     x1, x10
str     x1, [x8, #0x18]
bl      imp__stubs_printf ; printf
bl      imp__stubs_clock ; clock
stur    x0, [x29, var_30]
ldur    x8, [x29, var_30]
ldur    x9, [x29, var_18]
subs    x8, x8, x9
ucvtf   d0, x8
adrp    x8, #0x100003000
ldr     d1, [x8, #0x4c0] ; double_value_1000
fmul    d0, d0, d1
adrp    x8, #0x100003000
ldr     d1, [x8, #0x4b8] ; double_value_1E06
fddiv   d0, d0, d1
str     d0, [sp, #0x70 + var_38]
ldr     d0, [sp, #0x70 + var_38]
adrp    x0, #0x100003000 ; 0x100003b19@PAGE
add     x0, x0, #0xb19 ; 0x100003b19@PAGEOFF, "Finished.... Time elapsed for sprr_test in ms: %f\n\n"
mov     x8, sp ; argument "format" for method imp__stubs_printf
str     d0, [x8]
bl      imp__stubs_printf ; printf
ldp     x29, x30, [sp, #0x70]
add     sp, sp, #0x80
ret

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