Lab 2 Evaluation Sheet

Section:_____
Student Name:_____
Mg:_____

Prelab Exercises	TA Initials	Score
Complete VHDL code for the design	_____	_____/05
Adequate commenting of the ALU VHDL code	_____	_____/05
Student test bench for ALU	_____	_____/05
Simulation results demonstrating correct operations of ALU using student test bench	_____	_____/05
Successful clean net list synthesis	_____	_____/02

Lab Exercises
Hardware verification
(4) NOR, OR, AND, XOR
(2) SL
(2) SR
(2) ADD
(2) SUB   _____   _____/12

TA Grading Script
Successful simulation of ALU source
using submitted design and TA test bench   _____   _____/32

Draw a schematic of the internal logic of
the barrel shifter. (Hint: it can be done
with 5 muxes and 5 static shifters)   _____   _____/02