Netlist	*Function*	t_{CD} *(ns)*	t_{PD} *(ns)*	t_R *(ns/pf)*	t_F *(ns/pf)*	*load (pf)*	*size* (μ^2)
`Xid z constant0`	$Z = 0$	—	—	—	—	—	0
`Xid z constant1`	$Z = 1$	—	—	—	—	—	0
`Xid a z inverter`	$Z = \overline{A}$	.005	.02	2.3	1.2	.007	10
`Xid a z inverter_2`		.009	.02	1.1	.6	.013	13
`Xid a z inverter_4`		.009	.02	.56	.3	.027	20
`Xid a z inverter_8`		.02	.11	.28	.15	.009	56
`Xid a z buffer`	$Z = A$	.02	.08	2.2	1.2	.003	13
`Xid a z buffer_2`		.02	.07	1.1	.6	.005	17
`Xid a z buffer_4`		.02	.07	.56	.3	.01	30
`Xid a z buffer-8`		.02	.07	.28	.15	.02	43
`Xid e a z tristate`	$Z = A$ when e=1 else Z not driven	.03	.15	2.3	1.3	.004	23
`Xid e a z tristate_2`		.03	.13	1.1	.6	.006	30
`Xid e a z tristate_4`		.02	.12	.6	.3	.011	40
`Xid e a z tristate_8`		.02	.11	.3	.17	.02	56
`Xid a b z and2`	$Z = A \cdot B$	.03	.12	4.5	2.3	.002	13
`Xid a b c z and3`	$Z = A \cdot B \cdot C$	.03	.15	4.5	2.6	.002	17
`Xid a b c d z and4`	$Z = A \cdot B \cdot C \cdot D$	.03	.16	4.5	2.5	.002	20
`Xid a b z nand2`	$Z = \overline{A \cdot B}$	.01	.03	4.5	2.8	.004	10
`Xid a b c z nand3`	$Z = \overline{A \cdot B \cdot C}$	.01	.05	4.2	3.0	.005	13
`Xid a b c d z nand4`	$Z = \overline{A \cdot B \cdot C \cdot D}$	.01	.07	4.4	3.5	.005	17
`Xid a b z or2`	$Z = A + B$	.03	.15	4.5	2.5	.002	13
`Xid a b c z or3`	$Z = A + B + C$	.04	.21	4.5	2.5	.003	17
`Xid a b c d z or4`	$Z = A + B + C + D$	.06	.29	4.5	2.6	.003	20
`Xid a b z nor2`	$Z = \overline{A + B}$	.01	.05	6.7	2.4	.004	10
`Xid a b c z nor3`	$Z = \overline{A + B + C}$	.02	.08	8.5	2.4	.005	13
`Xid a b c d z nor4`	$Z = \overline{A + B + C + D}$	.02	.12	9.5	2.4	.005	20
`Xid a b z xor2`	$Z = A \oplus B$	.03	.14	4.5	2.5	.006	27
`Xid a b z xnor2`	$Z = \overline{A \oplus B}$	.03	.14	4.5	2.5	.006	27
`Xid a1 a2 b z aoi21`	$Z = \overline{(A1 \cdot A2) + B}$	.02	.07	6.8	2.7	.005	13
`Xid a1 a2 b z oai21`	$Z = \overline{(A1 + A2) \cdot B}$	.02	.07	6.7	2.7	.005	17
`Xid s d0 d1 z mux2`	Z = D0 when S = 0 Z = D1 when S = 1	.02	.12	4.5	2.5	.005	27
`Xid s0 s1 d0 d1 d2 d3 z mux4` *(Note order of s0 and s1!)*	Z=D0 when S_0=0, S_1=0 Z=D1 when S_0=1, S_1=0 Z=D2 when S_0=0, S_1=1 Z=D3 when S_0=1, S_1=1	.04	.19	4.5	2.5	.006	66
`Xid d clk q dreg` t_{setup} = .15, t_{hold} = 0	D→Q on CLK↑	.03	.19	4.3	2.5	.002	56