|  |  |  |
| --- | --- | --- |
| Device Name | Start Address | End Address |
| External Memory | 0x0 | 0x1FFF\_FFFF |
| Boot Rom | 0x4000\_0000 | 0x4002\_BFFF |
| Sram internal | 0x402F\_0400 | 0x402F\_FFFF |
| L3F CFG Regs | 0x4400\_0000 | 0x443F\_FFFF |
| L3S CFG Regs | 0x4480\_0000 | 0x44BF\_FFFF |
| McASP0/1 Data | 0x4600\_0000 | 0x467F\_FFFF |
| USBSS | 0x4740\_0000 | 0x4740\_0FFF |
| USB0 | 0x4740\_1000 | 0x4740\_17FF |
| USB1 | 0x4740\_1800 | 0x4740\_1FFF |
| USB scheduler | 0x4740\_2000 | 0x4740\_2FFF |
| USB Queue Manager | 0x4740\_4000 | 0x4740\_3FFF |
| MMCHS2 | 0x4781\_0000 | 0x4781\_FFFF |
| TPCC | 0x4900\_0000 | 0x490F\_FFFF |
| TPTC | 0x4980\_0000 | 0x49AF\_FFFF |
| GPMC | 0x5000\_0000 | 0x50FF\_FFFF |
| ADC\_TSC\_DMA | 0x54C0\_0000 | 0x54FF\_FFFF |
| EMIF0 SDRAM | 0x8000\_0000 | 0xBFFF\_FFFF |
| CM | 0x44E0\_0000 | 0x44E0\_0AFF |
| PRM | 0x44E0\_0B00 | 0x44E0\_12FF |
| GPIO0 | 0x44E0\_7000 | 0x44E0\_8FFF |
| UART0 | 0x44E0\_9000 | 0x44E0\_AFFF |
| I2C0 | 0x44E0\_B000 | 0x44E0\_CFFF |
| UART1 | 0x4802\_2000 | 0x4802\_4FFF |
| I2C1 | 0x4802\_A000 | 0x4802\_BFFF |
| McSPI0 | 0x4803\_0000 | 0x4803\_1FFF |
| GPIO1 | 0x4804\_C000 | 0x4804\_CFFF |
| GPIO2 | 0x481A\_C000 | 0x481A\_CFFF |
| GPIO3 | 0x481A\_E000 | 0x481A\_EFFF |
| PWM | 0x4830\_0000 | 0x4830\_425F |
| LCD Controller | 0x4830\_E000 | 0x4830\_EFFF |
| McSPI1 | 0x481A\_0000 | 0x481A\_0FFF |

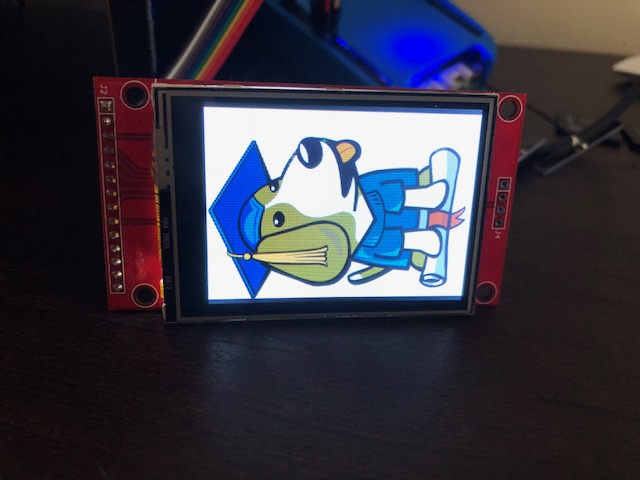


Figure 1 : Side ways Beagle



Exhibit 2: Edited text on LCD screen