Unit 1 Introduction of semiester

2012학년 2학기

GOAL

To understand

- Organizations of microprocessors and behaviors of major internal/external component
 - ALU, bus, memory, and I/O
- Machine instruction and programs
 - Memory addressing mechanism
 - Data transfer mechanism
 - Assembly language
- Basic software-hardware interface mechanism
 - I/O programming
 - Interrupt handling

To design

- Optimal software program for simple microprocessor-controlled systems
- Microprocessor-based embedded systems

Grade

→ Midterm : 30%

+ Final : 30%

+ Lab : 30%

Attendance : 10% (attendance + quiz)



COURSE SCHEDULE [1 OF 2]

Course Schedule

- + 1 week
 - Introduction
- + 2 week
 - Review Digital Logic Circuit
- + 3 week
 - Explain how to install and use the class simulator
 - Test-drive a simple code on the simulator
- + 4 week
 - Study machine instruction set of 8051 processor
- + 5 week
 - Write text on LCD using 8051 kit
- + 6 week
 - Learn how to use interrupt controller in 8051 processor
- 7 week
 - Midterm exams
- + 8 week
 - Learn how to use timer and counter in 8051 processor

COURSE SCHEDULE [2 OF 2]

Course Schedule

- + 9 week
 - Operate DC motor using 8051 kit
- + 10 week
 - Practice key interface and FND display using 8051 kit
- + 11 week
 - Practice dot matrix display using 8051 kit
- + 12 week
 - Present plan of team project
- + 13 week
 - Team project
- + 14 week
 - Team project
- + 15 week
 - Final exams

MORE INFORMATION

- Teaching Assistants
 - Hyunwoo Kim

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Homepage

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THANK YOU