SCO740 Error Code Definition v1.1.0.0

F740

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Revision History

Revision	Description	Date
1.0.0.0	Initial version.	2013/08/09
1.1.0.0	Add error code 0x30000013	2014/02/14

1. SCO740 Error Code Definition Summary

Reference: error_codes.h – ver. 1.1.0.0

Error Code	Error Name
0x00000000	FAVSC_STATUS_OK
0x00000001	FAVSC_MAC_CSM_ERR_UNKNOWNCMD
0x00000002	FAVSC_MAC_CSM_ERR_PREEXECPROC
0x00000003	FAVSC_MAC_CSM_ERR_POSTEXECPROC
0x00000004	FAVSC_MAC_CSM_ERR_BADENGTST1SUBCMD
0x00000005	FAVSC_MAC_CSM_ERR_MBPRDADDR
0x00000006	FAVSC_MAC_CSM_ERR_MBPWRADDR
0x00000007	FAVSC_MAC_CSM_ERR_SUBSYSINIT_CPU
0x00000008	FAVSC_MAC_CSM_ERR_SUBSYSINIT_DBG
0x00000009	FAVSC_MAC_CSM_ERR_SUBSYSINIT_CSM
0x0000000A	FAVSC_MAC_CSM_ERR_SUBSYSINIT_OEMCFG
0x0000000B	FAVSC_MAC_CSM_ERR_SUBSYSINIT_HOSTIF
0x0000000C	FAVSC_MAC_CSM_ERR_SUBSYSINIT_TILIF
0x0000000D	FAVSC_MAC_CSM_ERR_SUBSYSINIT_BIST
0x0000000F	FAVSC_MAC_CSM_ERR_SUBSYSINIT_GPIO
0x00000010	FAVSC_MAC_CSM_ERR_SUBSYSINIT_RFTC
0x00000011	FAVSC_MAC_CSM_ERR_SUBSYSINIT_PROT
0x00000012	FAVSC_MAC_CSM_ERR_PROTSCHED_UNKST
0x00000013	FAVSC_MAC_CSM_ERR_PROTSCHED_AMBANT
0x00000014	FAVSC_MAC_CSM_ERR_PROTSCHED_NODESC
0x00000015	FAVSC_MAC_CSM_ERR_PROTSCHED_PORTDEF
0x00000016	FAVSC_MAC_CSM_ERR_PROTSCHED_NOFRQCH
0x00000017	FAVSC_MAC_CSM_ERR_PROTSCHED_BADREGION
0x00000018	FAVSC_MAC_CSM_ERR_PROTSCHED_BADFTIME
0x00000019	FAVSC_MAC_CSM_ERR_PROTSCHED_FTUNETO
0x0000001A	FAVSC_MAC_CSM_ERR_SUBSYSINIT_OEMHWOPTS
0x0000001B	FAVSC_MAC_CSM_ERR_SUBSYSINIT_NVMEMUPD
0x0000001C	FAVSC_MAC_CSM_ERR_BAD_RESET_KEY
0x0000001D	FAVSC_MAC_CSM_ERR_DEV_RESET_FAILED

Error Code	Error Name
0x0000001E	FAVSC_MAC_CSM_ERR_NVMEMUPD_ABORT_MACERRNO
0x0000001F	FAVSC_MAC_CSM_ERR_NVMEMUPD_INT_MEMBNDS
0x00000020	FAVSC_MAC_CSM_ERR_NVMEMUPD_ENTRYKEY
0x00000021	FAVSC_MAC_CSM_ERR_NVMEMUPD_NVFLUSH
0x00000022	FAVSC_MAC_CSM_ERR_NVMEMUPD_WRVERFAIL
0x00000023	FAVSC_MAC_CSM_ERR_INVAL_START_CHAN
0x00000024	FAVSC_MAC_CSM_ERR_PROTSCHED_UNK_ALGO
0x00000025	FAVSC_MAC_CSM_ERR_INVAL_PWRMODE
0x00000026	FAVSC_MAC_CSM_ERR_PWRMODE_CORRUPT
0x00000027	FAVSC_MAC_CSM_ERR_NVMEMUPD_TXFAIL
0x00000028	FAVSC_MAC_CSM_ERR_NVMEMUPD_UPD_BOUNDS
0x00000029	FAVSC_MAC_CSM_ERR_NVMEMUPD_UNKNOWN
0x0000002A	FAVSC_MAC_CSM_ERR_NVMEMUPD_RXTO
0x0000002B	FAVSC_MAC_CSM_ERR_GPIO_NOTAVAIL
0x0000002C	FAVSC_MAC_CSM_ERR_ANT_NOTAVAIL
0x0000002D	FAVSC_MAC_CSM_ERR_CMDNOTAVAILABLE
0x0000002E	FAVSC_MAC_CSM_ERR_NOCORDICDEF
0x0000002F	FAVSC_MAC_CSM_ERR_SUBSYSINIT_DEBUG
0x00000030	FAVSC_MAC_CSM_ERR_SUBSYSINIT_TRACE
0x00000031	FAVSC_MAC_CSM_ERR_BUILD_TARGET_DEVICE_MISMATCH
0x00000032	FAVSC_MAC_CSM_ERR_DIAGNOSTICS
0x00000033	FAVSC_MAC_CSM_ERR_SUBSYSINIT_HOSTIFREGS_INIT
0x00000034	FAVSC_MAC_CSM_ERR_SUBSYSINIT_HANDSHAKE
0x00000035	FAVSC_MAC_CSM_ERR_NVMEMUPD_INVALID_MODE
0x00000036	FAVSC_MAC_CSM_ERR_INVALID_CMD_WHILE_IN_CRIT_ERROR
0x00000037	FAVSC_MAC_CSM_ERR_CRITICAL_ERROR_UNKNOWN
0x00000101	FAVSC_MAC_RESERVED_0x0101
0x00000102	FAVSC_MAC_HOSTIF_ERR_USBDESC
0x00000103	FAVSC_MAC_HOSTIF_ERR_USBDESCIDX
0x00000104	FAVSC_MAC_HOSTIF_ERR_USBTXEP0
0x00000105	FAVSC_MAC_RESERVED_0x0105
0x00000106	FAVSC_MAC_HOSTIF_ERR_USBRXBUFFSZ

Error Code	Error Name
0x00000107	FAVSC_MAC_HOSTIF_ERR_RXUNKNOWN
0x00000108	FAVSC_MAC_HOSTIF_ERR_TXUNKNOWN
0x00000109	FAVSC_MAC_HOSTIF_ERR_BADIFSTATE
0x0000010A	FAVSC_MAC_RESERVED_0x010A
0x0000010B	FAVSC_MAC_HOSTIF_ERR_REGADDR
0x0000010C	FAVSC_MAC_RESERVED_0x010C
0x0000010D	FAVSC_MAC_HOSTIF_ERR_STRDESCINIT
0x0000010E	FAVSC_MAC_HOSTIF_ERR_SELECTORBNDS
0x0000010F	FAVSC_MAC_RESERVED_0x010F
0x00000110	FAVSC_MAC_HOSTIF_ERR_PKTALIGN
0x00000111	FAVSC_MAC_HOSTIF_ERR_BADRAWMODE
0x00000112	FAVSC_MAC_HOSTIF_ERR_UNKLNKSTATE
0x00000113	FAVSC_MAC_HOSTIF_ERR_UNKUSBSETUP
0x00000114	FAVSC_MAC_HOSTIF_ERR_UARTRXBUFFSZ
0x00000115	FAVSC_MAC_HOSTIF_ERR_RAWMODECTL
0x00000116	FAVSC_MAC_HOSTIF_ERR_UNKHOSTIF
0x00000117	FAVSC_MAC_HOSTIF_ERR_UNKREGSTD
0x00000118	FAVSC_MAC_HOSTIF_ERR_DEBUGID
0x00000119	FAVSC_MAC_HOSTIF_ERR_DEBUGOVERFLOW
0x0000011A	FAVSC_MAC_HOSTIF_ERR_REGREADONLY
0x0000011B	FAVSC_MAC_HOSTIF_ERR_REGWRITEONLY
0x0000011C	FAVSC_MAC_HOSTIF_ERR_BADREGIONINITVALUES
0x0000011D	FAVSC_MAC_HOSTIF_ERR_INVALIDENGTSTARG
0x0000011E	FAVSC_MAC_HOSTIF_ERR_INVALIDSETFREQARG
0x0000011F	FAVSC_MAC_HOSTIF_ERR_INVALID_RSSI_FILTERING
0x00000120	FAVSC_MAC_HOSTIF_ERR_INVALID_TAGACC_CNT
0x00000121	FAVSC_MAC_HOSTIF_ERR_INVALID_BW_MODE
0x00000122	FAVSC_MAC_HOSTIF_ERR_OEM_MAC_REG_INIT_CTRL_ERROR
0x00000123	FAVSC_MAC_HOSTIF_ERR_OEM_MAC_REG_INIT_WRITE_ERROR
0x00000200	FAVSC_MAC_PROTOCOL_ERR_TRUNCATION_UNSUPPORTED
0x00000300	FAVSC_MAC_RFTC_ERR_BADFRQCHAN

Error Code	Error Name
0x00000301	FAVSC_MAC_RFTC_ERR_BADHOPMODE
0x00000302	FAVSC_MAC_RFTC_ERR_PLLFAILEDTOLOCK
0x00000303	FAVSC_MAC_RFTC_ERR_XCVRADC_TIMEDOUT
0x00000304	FAVSC_MAC_RFTC_ERR_FILTTUNE_TIMEOUT
0x00000305	FAVSC_MAC_RFTC_ERR_AMBIENTTEMPTOOHOT
0x00000306	FAVSC_MAC_RFTC_ERR_XCVRTEMPTOOHOT
0x00000307	FAVSC_MAC_RFTC_ERR_PATEMPTOOHOT
0x00000308	FAVSC_MAC_RFTC_ERR_PADELTATEMPTOOBIG
0x00000309	FAVSC_MAC_RFTC_ERR_REVPWRLEVTOOHIGH
0x0000030A	FAVSC_MAC_RFTC_ERR_BADIFLNAGAIN
0x0000030B	FAVSC_MAC_RFTC_ERR_TXRF_BIT_FAILED
0x0000030C	FAVSC_MAC_RFTC_ERR_TXRF_BYTE_FAILED
0x0000030D	FAVSC_MAC_RFTC_ERR_TXRF_EOT_FAILED
0x0000030E	FAVSC_MAC_RFTC_ERR_TXRF_PREAM_FAILED
0x0000030F	FAVSC_MAC_RFTC_ERR_TXRF_FSYNC_FAILED
0x00000310	FAVSC_MAC_RFTC_ERR_RXRF_ISR_TIMEOUT
0x00000311	FAVSC_MAC_RFTC_ERR_INVALIDLINKPARMS
0x00000312	FAVSC_MAC_RFTC_ERR_RXRF_INTERPKTTIMEOUT
0x00000313	FAVSC_MAC_RFTC_ERR_NO_LINKPROFHDR
0x00000314	FAVSC_MAC_RFTC_ERR_PROFILE_INVALID
0x00000315	FAVSC_MAC_RFTC_ERR_DBMVALOUTOFRANGE
0x00000316	FAVSC_MAC_RFTC_ERR_FWDPWRLEVTOOHIGH
0x00000317	FAVSC_MAC_RFTC_ERR_NO_GROSSPWRENTRY
0x00000318	FAVSC_MAC_RFTC_ERR_TARGETPWRTOOHIGH
0x00000319	FAVSC_MAC_RESERVED_0x0318
0x0000031A	FAVSC_MAC_RFTC_ERR_ANTENNADISCONNECTED
0x0000031B	FAVSC_MAC_RFTC_ERR_UNREC_HWOPTFORMAT
0x0000031C	FAVSC_MAC_RFTC_ERR_HWOPT_BADFWDPWROPT
0x0000031D	FAVSC_MAC_RFTC_ERR_HWOPT_BADREVPWROPT
0x0000031E	FAVSC_MAC_RFTC_ERR_HWOPT_BADDRMFILTOPT
0x0000031F	FAVSC_MAC_RFTC_ERR_HWOPT_BADAMBTEMPOPT
0x00000320	FAVSC_MAC_RFTC_ERR_HWOPT_BADPATEMPOPT
0x00000321	FAVSC_MAC_RFTC_ERR_HWOPT_BADXCVRTEMPOPT
0x00000322	FAVSC_MAC_RFTC_ERR_HWOPT_BADANTSENSOPT

Error Code	Error Name
0x00000323	FAVSC_MAC_RFTC_ERR_BADIFLNAAGCRANGE
0x00000324	FAVSC_MAC_RFTC_ERR_LPROFBADSELECTOR
0x00000325	FAVSC_MAC_RFTC_ERR_BADXCVRADDR
0x00000326	FAVSC_MAC_RFTC_ERR_XCVRADDRNOTINLIST
0x00000327	FAVSC_MAC_RFTC_ERR_BAD_RFLNA_GAIN_REQ
0x00000328	FAVSC_MAC_RFTC_ERR_BAD_IFLNA_GAIN_REQ
0x00000329	FAVSC_MAC_RFTC_ERR_BAD_AGCMIX_GAIN_REQ
0x0000032A	FAVSC_MAC_RFTC_ERR_HWOPT_BADFWDPWRCOMPOPT
0x0000032B	FAVSC_MAC_RFTC_ERR_INVALID_PLL_DIVIDER_VALUE
0x0000032C	FAVSC_MAC_RFTC_ERR_SJC_EXTERNALLOTOOLOW
0x0000032D	FAVSC_MAC_RFTC_ERR_SJC_EXTERNALLONOTSELECTED
0x0000032E	FAVSC_MAC_RFTC_ERR_BADLOSOURCE
0x0000032F	FAVSC_MAC_RFTC_ERR_GENERALRANDOMDATA
0x00000330	FAVSC_MAC_RFTC_ERR_XVCR_HEALTH_CHECK_FAIL
0x00000331	FAVSC_MAC_RFTC_ERR_INVALID_OEM_PROFILE_HEADER
0x00000332	FAVSC_MAC_RFTC_ERR_AUTO_READ_RX_FIFO
0x00000333	FAVSC_MAC_RFTC_ERR_DC_OFFSET_CALIBRATION
0x00000334	FAVSC_MAC_RFTC_ERR_LBT_RSSI_CALIBRATION
0x00000335	FAVSC_MAC_RFTC_ERR_PA_BIAS_CAL_CONFIG
0x00000336	FAVSC_MAC_RFTC_ERR_FWDPWRLEVERROR
0x00000337	FAVSC_MAC_RFTC_ERR_HWOPT_BADPABIASDACCTL
0x00000338	FAVSC_MAC_RFTC_ERR_PA_BIAS_CAL_MEASUREMENT
0x00000339	FAVSC_MAC_RFTC_ERR_PA_BIAS_CAL_NOT_FOUND
0x0000033A	FAVSC_MAC_RFTC_ERR_GROSSGAIN_CONFIG_INVALID
0x0000033B	FAVSC_MAC_RFTC_ERR_SJC_NOT_AVAILABLE_R500
0x0000033C	FAVSC_MAC_RFTC_ERR_GROSSGAIN_CALIBRATION
0x00000401	FAVSC_MAC_IO_INVAL_RDMASK
0x00000402	FAVSC_MAC_IO_INVAL_WRMASK
0x00000403	FAVSC_MAC_IO_INVAL_PTR_RAM
0x00000404	FAVSC_MAC_IO_INVAL_PTR_NV
0x00000405	FAVSC_MAC_IO_INVAL_PTR_NV_ALIGN
0x00000406	FAVSC_MAC_IO_NV_LOCK_ERR
0x00000407	FAVSC_MAC_IO_NV_PROG_ERR
0x00000408	FAVSC_MAC_IO_OEMCFG_ADDR_BOUNDS

Error Code	Error Name
0x00000409	FAVSC_MAC_IO_OEMCFG_NV_BOUNDS
0x0000040A	FAVSC_MAC_IO_OEMCFG_FMT_KEY
0x0000040B	FAVSC_MAC_IO_OEMCFG_FLUSH
0x0000040C	FAVSC_MAC_IO_OEMCFG_FORMAT
0x0000040D	FAVSC_MAC_IO_INVAL_IORSVD
0x0000040E	FAVSC_MAC_IO_OEMCFG_STRING_TYPE
0x0000040F	FAVSC_MAC_IO_OEMCFG_STRING_LENGTH
0x00000410	FAVSC_MAC_IO_OEMCFG_STRING_CHARACTER
0x00000411	FAVSC_MAC_IO_OEMCFG_STRING_CURRENT_INVALID
0x00000412	FAVSC_MAC_IO_OEMCFG_FORMAT_KEY_INVALID
0x00000413	FAVSC_MAC_IO_OEMCFG_FORMAT_CONFIGURATION_INVALID
0x00000414	FAVSC_MAC_IO_INVAL_NV_SECTOR
0x00000601	FAVSC_MAC_TILDENIF_ERR_ADDRMISMAT
0x00000602	FAVSC_MAC_TILDENIF_ERR_RDFAILSAFE
0x00000603	FAVSC_MAC_TILDENIF_ERR_INVALPWRST
0x00000604	FAVSC_MAC_TILDENIF_ERR_INVALID_SETTING_R500
0x00000701	FAVSC_MAC_BIST_ERR_RF_IO_REG_CHK
0x00000702	FAVSC_MAC_BIST_ERR_RF_REG_BITS
0x10000001	FAVSC_GEN_ERR
0x10000002	FAVSC_GEN_ERR_INVALID_PREAMBLE
0x10000003	FAVSC_GEN_ERR_INVALID_MSG_TYPE
0x10000004	FAVSC_GEN_ERR_INVALID_MSG_CODE
0x10000005	FAVSC_GEN_ERR_INVALID_CHECKSUM
0x10000006	FAVSC_GEN_ERR_INVALID_END_MARK
0x10000007	FAVSC_GEN_ERR_INVALID_PACKET_FORMAT
0x10000008	FAVSC_GEN_ERR_INVALID_PAYLOAD_LENGTH
0x10000009	FAVSC_GEN_ERR_INVALID_PARAMETER
0x1000000A	FAVSC_GEN_ERR_COMMAND_NOT_SUPPORT
0x1000000B	FAVSC_GEN_ERR_BUSY
0x1000000C	FAVSC_GEN_ERR_TIMEOUT
0x1000000D	FAVSC_GEN_ERR_WAIT_READER_INIT
0x1000000E	FAVSC_GEN_ERR_READER_MODULE_RESPONSE_TIMEOU

Error Code	Error Name
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0x1000000F	FAVSC_GEN_ERR_WRITE_CONFIG
0x10000010	FAVSC_GEN_ERR_READ_CONFIG
0x10000011	FAVSC_GEN_ERR_NO_MEMORY
0x10000012	FAVSC_GEN_ERR_NETWORK_DEVICE_NOT_READY
0x10000013	FAVSC_GEN_ERR_NETWORK_CONNECTION_NOT_START
0x10000014	FAVSC_GEN_ERR_WIRELESS_PARSE_ERROR
0x10000015	FAVSC_GEN_ERR_VALUE_NOTAVAIL
0x20000001	FAVSC_GPIO_ERR
0x20000002	FAVSC_GPIO_ERR_SERIAL_PORT_NOT_OPEN
0x20000003	FAVSC_GPIO_ERR_INVALID_VERSION
0x20000004	FAVSC_GPIO_ERR_INVALID_TYPE
0x20000005	FAVSC_GPIO_ERR_INVALID_CMD
0x20000006	FAVSC_GPIO_ERR_INVALID_TIME
0x20000007	FAVSC_GPIO_ERR_INVALID_MASK
0x20000008	FAVSC_GPIO_ERR_INVALID_CHECKSUM
0x20000009	FAVSC_GPIO_ERR_COMMAND_TIMEOUT
0x2000000A	FAVSC_GPIO_ERR_COMMAND_BYTE_TIMEOUT
0x30000001	FAVSC_READER_ERR_INVALID_INV_CALLBACK
0x30000002	FAVSC_READER_ERR_INVALID_ACCESS_CALLBACK
0x30000003	FAVSC_READER_OK_ACCESS_CMD_CALLBACK
0x30000004	FAVSC_READER_ERR_ACCESS_CMD_CALLBACK
0x30000005	FAVSC_READER_ERR_SET_ACCESS_ANTENNA_PORT
0x30000006	FAVSC_READER_ERR_RESTORE_ACCESS_ANTENNA_PORT
0x30000007	FAVSC_READER_ERR_SET_ANTENNA_TYPE_BI
0x30000008	FAVSC_READER_ERR_DISABLE_ANTENNA_STATE
0x30000009	FAVSC_READER_ERR_SET_PORT_DWELL_TIME_ZERO
0x3000000A	FAVSC_READER_ERR_SET_PORT_INV_CYCLE_ZERO
0x3000000B	FAVSC_READER_ERR_NO_READ_PARMS
0x3000000C	FAVSC_READER_ERR_NO_WRITE_PARMS
0x3000000D	FAVSC_READER_ERR_NO_KILL_PARMS
0x3000000E	FAVSC_READER_ERR_NO_LOCK_PARMS
0x3000000F	FAVSC_READER_ERR_NO_BLOCKWRITE_PARMS
0x30000010	FAVSC_READER_ERR_NO_BLOCKERASE_PARMS

Error Code	Error Name
0x30000011	FAVSC_READER_ERR_SET_ANTENNA_STATE
0x30000013	FAVSC_READER_ERR_NO_FAV_READ_PARMS
0xFFFFD8F1	FAVSC_RFID_ERR_ALREADY_OPEN
0xFFFFD8F2	FAVSC_RFID_ERR_BUFFER_TOO_SMALL
0xFFFFD8F3	FAVSC_RFID_ERR_FAILURE
0xFFFFD8F4	FAVSC_RFID_ERR_DRIVER_LOAD
0xFFFFD8F5	FAVSC_RFID_ERR_DRIVER_MISMATCH
0xFFFFD8F6	FAVSC_RFID_ERR_RESERVED_01
0xFFFFD8F7	FAVSC_RFID_ERR_INVALID_ANTENNA
0xFFFFD8F8	FAVSC_RFID_ERR_INVALID_HANDLE
0xFFFFD8F9	FAVSC_RFID_ERR_INVALID_PARAMETER
0xFFFFD8FA	FAVSC_RFID_ERR_NO_SUCH_RADIO
0xFFFFD8FB	FAVSC_RFID_ERR_NOT_INITIALIZED
0xFFFFD8FC	FAVSC_RFID_ERR_NOT_SUPPORTED
0xFFFFD8FD	FAVSC_RFID_ERR_OPERATION_CANCELLED
0xFFFFD8FE	FAVSC_RFID_ERR_OUT_OF_MEMORY
0xFFFFD8FF	FAVSC_RFID_ERR_RADIO_BUSY
0xFFFFD900	FAVSC_RFID_ERR_RADIO_FAILURE
0xFFFFD901	FAVSC_RFID_ERR_RADIO_NOT_PRESENT
0xFFFFD902	FAVSC_RFID_ERR_CURRENTLY_NOT_ALLOWED
0xFFFFD903	FAVSC_RFID_ERR_RADIO_NOT_RESPONDING
0xFFFFD904	FAVSC_RFID_ERR_NONVOLATILE_INIT_FAILED
0xFFFFD905	FAVSC_RFID_ERR_NONVOLATILE_OUT_OF_BOUNDS
0xFFFFD906	FAVSC_RFID_ERR_NONVOLATILE_WRITE_FAILED
0xFFFFD907	FAVSC_RFID_ERR_RECEIVE_OVERFLOW
0xFFFFD908	FAVSC_RFID_ERR_UNEXPECTED_VALUE
0xFFFFD909	FAVSC_RFID_ERROR_NONVOLATILE_CRC_FAILED
0xFFFFD90A	FAVSC_RFID_ERROR_NONVOLATILE_PACKET_HEADER
0xFFFFD90B	FAVSC_RFID_ERROR_NONVOLATILE_MAX_PACKET_LENGTH

2. SCO740 Error Code Definition Details

2.1 FAVSC_STATUS_OK

- Value: 0x00000000

Command is successful with no errors.

2.2 FAVSC_MAC_CSM_ERR_UNKNOWNCMD

- Value: 0x00000001

This error is set when an invalid command has been issued to the Indy Firmware. The Indy Firmware performs basic bounds checking on command values.

2.3 FAVSC_MAC_CSM_ERR_PREEXECPROC

- Value: 0x00000002

An error occurred during pre-command execution processing. This may happen if the Indy Firmware is unable to transmit a Command-Begin packet to the host.

2.4 FAVSC_MAC_CSM_ERR_POSTEXECPROC

- Value: 0x00000003

An error occurred during post-command execution processing. This map happen if the Indy Firmware is unable to flush host TX buffers after the main processing of a given command is completed.

2.5 FAVSC_MAC_CSM_ERR_BADENGTST1SUBCMD

- Value: 0x00000004

This is set when an unsupported ENGTST1 sub-command has been indicated via the HST_ENGTST_ARG0 register, bits 7:0. FYI - BUG - currently only set if particular engineering test sub-commands have not been compile into the Indy Firmware image. Eventually this will be reported for all invalid sub-command values in HST_ENGTST_ARG0.

2.6 FAVSC_MAC_CSM_ERR_MBPRDADDR

- Value: 0x00000005

Set if an invalid / unsupported UHF RFID Transceiver register is detected in the HST_MBP_ADDR after an MBPRDREG command is issued to the Indy Firmware.

2.7 FAVSC_MAC_CSM_ERR_MBPWRADDR

- Value: 0x00000006

Set if an invalid / unsupported register is detected in the HST_MBP_ADDR after an MBPWRREG command is issued to the Indy Firmware.

2.8 FAVSC_MAC_CSM_ERR_SUBSYSINIT_CPU

- Value: 0x00000007

Set if the CPU module fails to initialize on Indy Firmware boot.

2.9 FAVSC_MAC_CSM_ERR_SUBSYSINIT_DBG

- Value: 0x00000008

Set if the Debug module fails to initialize on Indy Firmware boot.

2.10 FAVSC_MAC_CSM_ERR_SUBSYSINIT_CSM

- Value: 0x00000009

Set if the Core State Machine fails to initialize on Indy Firmware boot.

2.11 FAVSC_MAC_CSM_ERR_SUBSYSINIT_OEMCFG

- Value: 0x0000000A

Set if the OEM configuration module fails to initialize on Indy Firmware boot.

2.12 FAVSC_MAC_CSM_ERR_SUBSYSINIT_HOSTIF

- Value: 0x0000000B

Set of the HOST interface module fails to initialize on Indy Firmware boot.

2.13 FAVSC_MAC_CSM_ERR_SUBSYSINIT_TILIF

- Value: 0x0000000C

Set if the UHF RFID Transceiver low level interface module fails to initialize on Indy Firmware boot.

2.14 FAVSC_MAC_CSM_ERR_SUBSYSINIT_BIST

- Value: 0x0000000D

Set if the BIST module fails to initialize on Indy Firmware boot.

2.15 FAVSC_MAC_CSM_ERR_SUBSYSINIT_GPIO

- Value: 0x0000000F

Set if the GPIO module fails to initialize on Indy Firmware boot.

2.16 FAVSC_MAC_CSM_ERR_SUBSYSINIT_RFTC

- Value: 0x00000010

Set of the RF Transceiver Control module fails to initialize on Indy Firmware boot.

2.17 FAVSC_MAC_CSM_ERR_SUBSYSINIT_PROT

- Value: 0x00000011

Set if the RFID Protocol module(s) fail to initialize on Indy Firmware boot.

2.18 FAVSC_MAC_CSM_ERR_PROTSCHED_UNKST

- Value: 0x00000012

Set if the RFID protocol scheduler module detects an unknown state - likely indicates firmware corruption or runtime SRAM corruption by errant code.

2.19 FAVSC_MAC_CSM_ERR_PROTSCHED_AMBANT

- Value: 0x00000013

Set if the Antenna configuration dwell time and inventory round count are both zero - which is illegal and ambiguous.

2.20 FAVSC_MAC_CSM_ERR_PROTSCHED_NODESC

- Value: 0x00000014

Set if the protocol scheduler detects that no logical antennas have been enabled using the HST_ANT_DESC_CFG register bank.

2.21 FAVSC_MAC_CSM_ERR_PROTSCHED_PORTDEF

- Value: 0x00000015

Set when a bogus physical antenna port definition value is used - this likely means that the TX and RX port values are not the same - which is required for Indy Development Platform.

2.22 FAVSC_MAC_CSM_ERR_PROTSCHED_NOFRQCH

- Value: 0x00000016

Set by the protocol scheduler when no frequency channels have been enabled.

2.23 FAVSC_MAC_CSM_ERR_PROTSCHED_BADREGION

- Value: 0x00000017

Set by the protocol scheduler when a bogus regulatory region has been detected in HST_REGULATORY_REGION.

2.24 FAVSC_MAC_CSM_ERR_PROTSCHED_BADFTIME

- Value: 0x00000018

Set by the protocol schedulers FCC state machine when a bogus FCC frequency hop value has been written to HST_PROTSCH_FTIME, Bank 0 - only 100,200,400 milliseconds are valid values.

2.25 FAVSC_MAC_CSM_ERR_PROTSCHED_FTUNETO

- Value: 0x00000019

Not currently set by firmware.

2.26 FAVSC_MAC_CSM_ERR_SUBSYSINIT_OEMHWOPTS

- Value: 0x0000001A

Set if the OEM hardware-option configuration module fails to initialize on Indy Firmware boot.

2.27 FAVSC_MAC_CSM_ERR_SUBSYSINIT_NVMEMUPD

- Value: 0x0000001B

Set if the firmware failed to initialize the NV Memory Update module at boot time.

2.28 FAVSC_MAC_CSM_ERR_BAD_RESET_KEY

- Value: 0x0000001C

Set if the firmware CPU module's reset device logic is called with a bogus key. This will generally only happen if the system has experienced a crash and this logic is being called through an invalid call chain - likely due to some sort of corruption.

2.29 FAVSC_MAC_CSM_ERR_DEV_RESET_FAILED

- Value: 0x0000001D

Set if the device reset logic fails to actually reset the device - likely due to a MCU related hardware failure or system corruption.

2.30 FAVSC_MAC_CSM_ERR_NVMEMUPD_ABORT_MACERRNO

- Value: 0x0000001E

Set *prior* to entering non volatile memory update mode if the current global Indy Firmware error status is indicating an error. The MAC will not enter non volatile memory update mode if there is currently an error. The host should use the CLRERR command to clear any errors; if this doesn't work the device may need to be manually updated using the recovery method indicated in the Indy Firmware Datasheet.

2.31 FAVSC_MAC_CSM_ERR_NVMEMUPD_INT_MEMBNDS

- Value: 0x0000001F

Set if an internal memory bounds check fails while in Non volatile memory update mode. If these errors occur the Indy Firmware tries very hard not to update non-volatile memory with bogus data. This error occurs likely due to a system corruption.

2.32 FAVSC_MAC_CSM_ERR_NVMEMUPD_ENTRYKEY

- Value: 0x00000020

Set if the non volatile memory mode entry logic detects an invalid key. This would occur if the calling logic erroneously called the non-volatile memory logic due to system corruption / firmware error.

2.33 FAVSC_MAC_CSM_ERR_NVMEMUPD_NVFLUSH

- Value: 0x00000021

Set if, during non volatile memory update mode the firmware fails to write flash at the lowest level. This is likely due to flash lock bits being set (i.e. via tools like SAM-BA) or a system corruption.

2.34 FAVSC_MAC_CSM_ERR_NVMEMUPD_WRVERFAIL

- Value: 0x00000022

Set if write verification logic fails after writing data at the lowest level to flash. This may indicate problems with MCU device flash hardware. This can occur if the MCU device flash has been updated too many times.

2.35 FAVSC_MAC_CSM_ERR_INVAL_START_CHAN

- Value: 0x00000023

Set by the protocol scheduler if the HST_RFTC_FRQCH_CMDSTART register has been set to an invalid channel.

2.36 FAVSC_MAC_CSM_ERR_PROTSCHED_UNK_ALGO

- Value: 0x00000024

Set by the protocol scheduler if an invalid protocol algorithm has been selected via the HST_INV_CFG register.

2.37 FAVSC_MAC_CSM_ERR_INVAL_PWRMODE

- Value: 0x00000025

Set by the core state machine if an invalid power management mode has been specified in the HST_PWRMGMT register.

2.38 FAVSC_MAC_CSM_ERR_PWRMODE_CORRUPT

- Value: 0x00000026

This is set if a system corruption has occurred and the logic is unable to determine the desired power management mode.

2.39 FAVSC_MAC_CSM_ERR_NVMEMUPD_TXFAIL

- Value: 0x00000027

Set if the non volatile memory mode logic fails to transmit a packet to the host during non volatile memory update.

2.40 FAVSC_MAC_CSM_ERR_NVMEMUPD_UPD_BOUNDS

- Value: 0x00000028

Set during non volatile memory update if the range indicated for updates falls outside the valid non-volatile memory ranges available on the device.

2.41 FAVSC_MAC_CSM_ERR_NVMEMUPD_UNKNOWN

- Value: 0x00000029

An unknown error has occurred during non-volatile memory updates - likely a system corruption.

2.42 FAVSC_MAC_CSM_ERR_NVMEMUPD_RXTO

- Value: 0x0000002A

Set during non volatile memory mode if the firmware does not receive a packet from the host within 60 seconds. This may occur if the host side application has crashed or the physical interface has been removed or corrupted.

2.43 FAVSC_MAC_CSM_ERR_GPIO_NOTAVAIL

- Value: 0x0000002B

This error code is generated when the host / user attempts to use a GPIO pin that has previously been configured as unavailable in the OEM Configuration Area entry GPIO_AVAIL.

2.44 FAVSC_MAC_CSM_ERR_ANT_NOTAVAIL

- Value: 0x0000002C

This error code is generated when the host / user attempts to use an antenna pin that has previously been configured as unavailable in the OEM Configuration Area entry ANT_AVAIL.

2.45 FAVSC_MAC_CSM_ERR_CMDNOTAVAILABLE

- Value: 0x0000002D

Set by the command processor when a command is invoked from the host, which has been defined, but is not available in the Indy Firmware codebase. This situation can occur if, for instance, a command is disabled by means of a compile-time switch.

2.46 FAVSC_MAC_CSM_ERR_NOCORDICDEF

- Value: 0x0000002E

Set by the protocol scheduler when no CORDIC values are found in the OEM configuration area. CORDIC values are part of the LBT configuration. See the OEM Configuration Section of the Firmware Datasheet for more details on these settings. Cordic configuration values are only required when LBT is enabled.

2.47 FAVSC_MAC_CSM_ERR_SUBSYSINIT_DEBUG

- Value: 0x0000002F

Set if the firmware failed to initialize the Debug subsystem at boot time.

2.48 FAVSC_MAC_CSM_ERR_SUBSYSINIT_TRACE

- Value: 0x00000030

Set if the firmware failed to initialize the Trace subsystem at boot time.

2.49 FAVSC_MAC_CSM_ERR_BUILD_TARGET_DEVICE_MIS MATCH

- Value: 0x00000031

Set if the firmware failed the Target Build and Physical Device Check at boot time.

2.50 FAVSC_MAC_CSM_ERR_DIAGNOSTICS

- Value: 0x00000032

Set if the firmware failed to properly set MAC Error diagnostic codes. Actual MAC Error may not correctly be reflected by the MAC Error register.

2.51 FAVSC_MAC_CSM_ERR_SUBSYSINIT_HOSTIFREGS_INI T

- Value: 0x00000033

Set if the MAC register default value initialization module fails to initialize on Indy Firmware boot.

2.52 FAVSC_MAC_CSM_ERR_SUBSYSINIT_HANDSHAKE

- Value: 0x00000034

Set if the firmware failed to initialize the Handshake interface subsystem at boot time.

2.53 FAVSC_MAC_CSM_ERR_NVMEMUPD_INVALID_MODE

- Value: 0x00000035

Set if the HST_NV_UPDATE_CONTROL MAC register had an invalid update_mode set.

2.54 FAVSC_MAC_CSM_ERR_INVALID_CMD_WHILE_IN_CRIT_ERROR

- Value: 0x00000036

Set if a Gen2 command is attempted following a critical error during system initialization. Typically caused by a failed OEM read attempt and can usually be resolved by formatting OEM.

2.55 FAVSC_MAC_CSM_ERR_CRITICAL_ERROR_UNKNOWN

- Value: 0x00000037

Set if an unknown critical error is detected at the end of system initialization. Typically caused by a failed OEM read attempt and can usually be resolved by formatting OEM.

2.56 FAVSC_MAC_RESERVED_0x0101

- Value: 0x00000101

Reserved

2.57 FAVSC_MAC_HOSTIF_ERR_USBDESC

- Value: 0x00000102

Set by the USB interface module when an unsupported descriptor TYPE has been requested by the host (i.e. not a device, string, configuration descriptor type). This may be due to compatibility problems with the USB host.

2.58 FAVSC_MAC_HOSTIF_ERR_USBDESCIDX

- Value: 0x00000103

Set by the USB interface module when an unsupported device descriptor index has been requested by the Host.

2.59 FAVSC_MAC_HOSTIF_ERR_USBTXEP0

- Value: 0x00000104

Set by the USB interface module when it is unable to transmit the response to a request on USB endpoint 0 (aka control endpoint). This may be due to compatibility or synchronization problems with the USB host.

2.60 FAVSC_MAC_RESERVED_0x0105

- Value: 0x00000105

Reserved

2.61 FAVSC_MAC_HOSTIF_ERR_USBRXBUFFSZ

- Value: 0x00000106

Set by the USB interface module when higher level firmware requests an unsupported buffer length. This may be due to a firmware build error or corrupted firmware in flash.

2.62 FAVSC_MAC_HOSTIF_ERR_RXUNKNOWN

- Value: 0x00000107

This is set by the Host interface module when the underlying physical interface module returns an unknown error code on receive from the host. This may be due to a firmware build issue, corrupted firmware image or corrupted SRAM due to errant Indy Firmware code.

2.63 FAVSC_MAC_HOSTIF_ERR_TXUNKNOWN

- Value: 0x00000108

This is set by the Host interface module when the underlying physical interface module returns an unknown error code on transmit to the Host. This may be due to a firmware build issue, corrupted firmware image or corrupted SRAM due to errant code.

2.64 FAVSC_MAC_HOSTIF_ERR_BADIFSTATE

- Value: 0x00000109

This is set when the Host interface code detects that its internal state machine out of sync. This could be due to a corrupted firmware image or corrupted SRAM due to errant Indy Firmware code.

2.65 FAVSC_MAC_RESERVED_0x010A

- Value: 0x0000010A

Reserved

2.66 FAVSC_MAC_HOSTIF_ERR_REGADDR

- Value: 0x0000010B

Set by the host interface module when an invalid Indy Firmware register read or write is attempted (either by the host or internally by the Indy Firmware).

2.67 FAVSC_MAC_RESERVED_0x010C

- Value: 0x0000010C

Reserved

2.68 FAVSC_MAC_HOSTIF_ERR_STRDESCINIT

- Value: 0x0000010D

This is set by the host interface module during initialization if it is unable to retrieve USB string descriptors from non-volatile memory (i.e. flash) OEM configuration area. This may be due to a corrupt or unformatted OEM Configuration area. It may also be due to a firmware build issue if the OEM configuration definition is out of sync with the Indy Firmware code.

2.69 FAVSC_MAC_HOSTIF_ERR_SELECTORBNDS

- Value: 0x0000010E

This is set when the host attempts to write a value to a selector type register which is out of range for that selector.

2.70 FAVSC_MAC_RESERVED_0x010F

- Value: 0x0000010F

Reserved

2.71 FAVSC_MAC_HOSTIF_ERR_PKTALIGN

- Value: 0x00000110

Not currently set by Indy Firmware.

2.72 FAVSC_MAC_HOSTIF_ERR_BADRAWMODE

- Value: 0x00000111

Set by the low level host interface logic if an upper level requests an unsupported raw mode. This may occur if the system is corrupted.

2.73 FAVSC_MAC_HOSTIF_ERR_UNKLNKSTATE

- Value: 0x00000112

Set by the low level host interface logic if a system corrupt occurs and the link manager can not determine the current link state.

2.74 FAVSC_MAC_HOSTIF_ERR_UNKUSBSETUP

- Value: 0x00000113

Set by the low level host interface logic if an unknown / unsupported control command is received from the host. This may occur if the host logic and the Indy Firmware logic are out of sync in terms of the lowest level host interface (UART, USB).

2.75 FAVSC_MAC_HOSTIF_ERR_UARTRXBUFFSZ

- Value: 0x00000114

This is set if the upper layer host logic attempts to receive data and the lower layer cannot support the buffer size requested. This will happen if the system is corrupted.

2.76 FAVSC_MAC_HOSTIF_ERR_RAWMODECTL

- Value: 0x00000115

Set by the low level host interface logic if a control command is received from the host while in raw mode - which is not allowed. This would happen if the host caused the Indy Firmware to enter non-volatile memory update mode, which uses raw mode, and then the host proceeded to issue control commands.

2.77 FAVSC_MAC_HOSTIF_ERR_UNKHOSTIF

- Value: 0x00000116

Set by the host interface module at boot time if the OEM configuration area is specifying an unsupported host interface.

2.78 FAVSC_MAC_HOSTIF_ERR_UNKREGSTD

- Value: 0x00000117

Set by the host interface module at boot time if the OEM configuration area is specifying an unsupported regulatory standard.

2.79 FAVSC_MAC_HOSTIF_ERR_DEBUGID

- Value: 0x00000118

Set by host interface module if Debug Id is invalid.

2.80 FAVSC_MAC_HOSTIF_ERR_DEBUGOVERFLOW

- Value: 0x00000119

Set by host interface module if Debug Buffer overflows.

2.81 FAVSC_MAC_HOSTIF_ERR_REGREADONLY

- Value: 0x0000011A

Set by the host interface module when a Read-Only Indy Firmware register write is attempted by the host.

2.82 FAVSC_MAC_HOSTIF_ERR_REGWRITEONLY

- Value: 0x0000011B

Set by the host interface module when an Write Only Indy Firmware register read is attempted by host.

2.83 FAVSC_MAC_HOSTIF_ERR_BADREGIONINITVALUES

- Value: 0x0000011C

Set by the host interface module if the default region dependent parameters are invalid.

2.84 FAVSC_MAC_HOSTIF_ERR_INVALIDENGTSTARG

- Value: 0x0000011D

Set by an EngTest Sub Command with an invalid argument.

2.85 FAVSC_MAC_HOSTIF_ERR_INVALIDSETFREQARG

- Value: 0x0000011E

Set by Set Frequency Command with an invalid argument. When this error is set, the result registers will be set to 0xFFFFFFFF.

2.86 FAVSC_MAC_HOSTIF_ERR_INVALID_RSSI_FILTERING

- Value: 0x0000011F

Set when an invalid Inventory RSSI Filtering configuration has been configured.

2.87 FAVSC_MAC_HOSTIF_ERR_INVALID_TAGACC_CNT

- Value: 0x00000120

Set when an invalid HST_TAGACC_CNT value is specified.

2.88 FAVSC_MAC_HOSTIF_ERR_INVALID_BW_MODE

- Value: 0x00000121

Set when an invalid BlockWrite mode is specified in HST_IMPINJ_EXTENSIONS.

2.89 FAVSC_MAC_HOSTIF_ERR_OEM_MAC_REG_INIT_CTRL_ERROR

- Value: 0x00000122

Set when an invalid MAC Register Initialization pair (Control/Data) is found during the MAC Register initialization.

2.90 FAVSC_MAC_HOSTIF_ERR_OEM_MAC_REG_INIT_WRITE_ERROR

- Value: 0x00000123

Set when an invalid MAC Register Initialization write occurs found during the MAC Register initialization.

2.91 FAVSC_MAC_PROTOCOL_ERR_TRUNCATION_UNSUPPORTED

- Value: 0x00000200

Set by protocol if truncation is set in the Select configuration register, since truncation is unsupported.

2.92 FAVSC_MAC_RFTC_ERR_BADFRQCHAN

- Value: 0x00000300

This is set during the PLL lock logic when a bounds check fails while checking the frequency channel configuration registers.

2.93 FAVSC_MAC_RFTC_ERR_BADHOPMODE

- Value: 0x00000301

This is set if an unsupported frequency hopping mode is detected - during the PLL lock logic.

2.94 FAVSC_MAC_RFTC_ERR_PLLFAILEDTOLOCK

- Value: 0x00000302

This is set if the PLL fails to lock.

2.95 FAVSC_MAC_RFTC_ERR_XCVRADC_TIMEDOUT

- Value: 0x00000303

This is set when the RFTC module's AUX ADC function times out waiting for an ADC conversion.

2.96 FAVSC_MAC_RFTC_ERR_FILTTUNE_TIMEOUT

- Value: 0x00000304

This is set when the RFTC module times out waiting for UHF RFID Transceiver to indicate RX or TX filter tuning is complete.

2.97 FAVSC_MAC_RFTC_ERR_AMBIENTTEMPTOOHOT

- Value: 0x00000305

This is set when the RFTC module detects ambient temperature sensor indicates too hot.

2.98 FAVSC_MAC_RFTC_ERR_XCVRTEMPTOOHOT

- Value: 0x00000306

This is set when the RFTC module detects transceiver temperature sensor indicates too hot.

2.99 FAVSC_MAC_RFTC_ERR_PATEMPTOOHOT

- Value: 0x00000307

This is set when the RFTC module detects PA temperature sensor indicates too hot.

2.100 FAVSC_MAC_RFTC_ERR_PADELTATEMPTOOBIG

- Value: 0x00000308

This is set when the RFTC module detects that the delta between the PA temperature and the ambient temperature is too great.

2.101 FAVSC_MAC_RFTC_ERR_REVPWRLEVTOOHIGH

- Value: 0x00000309

This is set when the reverse power level is too high as measured by the configured reverse power level threshold in the register set.

2.102 FAVSC_MAC_RFTC_ERR_BADIFLNAGAIN

- Value: 0x0000030A

This is set when an incorrect current gain setting is passed into the IFLNA gain adjustment logic. May indicate corrupted code.

2.103 FAVSC_MAC_RFTC_ERR_TXRF_BIT_FAILED

- Value: 0x0000030B

Returned by RFTC code when errors occur in transmitting a bit over the RF interface.

2.104 FAVSC_MAC_RFTC_ERR_TXRF_BYTE_FAILED

- Value: 0x0000030C

Returned by RFTC code when errors occur in transmitting a buffer of bytes over the RF interface.

2.105 FAVSC_MAC_RFTC_ERR_TXRF_EOT_FAILED

- Value: 0x0000030D

Returned by RFTC code when errors occur in transmitting an "end of transfer" command over the RF interface.

2.106 FAVSC_MAC_RFTC_ERR_TXRF_PREAM_FAILED

- Value: 0x0000030E

Returned by RFTC code when errors occur in transmitting a "preamble" command over the RF interface.

2.107 FAVSC_MAC_RFTC_ERR_TXRF_FSYNC_FAILED

- Value: 0x0000030F

Returned by RFTC code when errors occur in transmitting a "frame-sync" command over the RF interface.

2.108 FAVSC_MAC_RFTC_ERR_RXRF_ISR_TIMEOUT

- Value: 0x00000310

Indicates that the RF Transceiver failed to set expected ISR bits in a timely fashion. Indicates a failure in either the RFTC state machine logic or in the RF Transceiver state machine logic.

2.109 FAVSC_MAC_RFTC_ERR_INVALIDLINKPARMS

- Value: 0x00000311

This is set when invalid link parameters are detected when the filter tuning logic is run.

2.110 FAVSC_MAC_RFTC_ERR_RXRF_INTERPKTTIMEOUT

- Value: 0x00000312

This indicates a failure in either the RFTC state machine logic or in the RF Transceiver state machine logic. This error can only occur if the RF Transceiver starts filling its RX FIFO with received data, but fails return the requested number of bits in a timely fashion.

2.111 FAVSC_MAC_RFTC_ERR_NO_LINKPROFHDR

- Value: 0x00000313

Not currently in use. It may be occurred in the future when switching between linked profiles if some of the required information is not properly coded in the Indy Firmware.

2.112 FAVSC_MAC_RFTC_ERR_PROFILE_INVALID

- Value: 0x00000314

This error occurs if the RF Transceiver is loaded an invalid profile.

2.113 FAVSC_MAC_RFTC_ERR_DBMVALOUTOFRANGE

- Value: 0x00000315

Internal error. The error is the result of the Indy Firmware having to do a "dBm to linear" conversion on a dBm measurement that is outside the range of -99dBm through +45dBm. It the unlikely event that this error is encountered, it is probably the result of a faulty RF Peak Detector, a bug in the code that computes the dBm value from the RF Peak Detector ADC reading, or a faulty external PA circuit.

2.114 FAVSC_MAC_RFTC_ERR_FWDPWRLEVTOOHIGH

- Value: 0x00000316

If, during RF power-ramping, it is determined that the RF power at the antenna port has momentarily exceeded 35dBm, or has exceeded 33dBm steady-state, this error will be caused. Encountering this error is the result of an incorrect calibration of the "gross gains". See Indy Firmware command 0x1B for more information about how to calibrate the system.

2.115 FAVSC_MAC_RFTC_ERR_NO_GROSSPWRENTRY

- Value: 0x00000317

Internal error that may occur if memory is corrupted.

2.116 FAVSC_MAC_RFTC_ERR_TARGETPWRTOOHIGH

- Value: 0x00000318

Indicates that the target power (in Indy Firmware Virtual Register 0x706) is higher than the maximum allowed output power, which is +33dBm.

2.117 FAVSC_MAC_RESERVED_0x0318

- Value: 0x00000319

Reserved. Deprecated RFTC_ERR_REFVOLT_OUTOFBOUNDS.

2.118 FAVSC_MAC_RFTC_ERR_ANTENNADISCONNECTED

Value: 0x0000031A

Indicates that the measured value of the antenna-sense resistor (reported in the Indy Firmware Virtual Register 0x703) exceeds the threshold specified (specified in the Indy Firmware Virtual register 0xB12). To determine which antenna was disconnected, the list of enabled antennas will need to be scanned for the one exceeding the threshold (this is done by iterating through all valid selectors in register 0x701 and examining the MAC_ANT_DESC_STAT register at address 0x703.

2.119 FAVSC_MAC_RFTC_ERR_UNREC_HWOPTFORMAT

- Value: 0x0000031B

Indicates that the OEMCFG's HW_OPTIONS_FORMAT value is not recognized by the RFTC subsystem.

2.120 FAVSC_MAC_RFTC_ERR_HWOPT_BADFWDPWROPT

- Value: 0x0000031C

Indicates that the forward power detection option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.121 FAVSC_MAC_RFTC_ERR_HWOPT_BADREVPWROPT

- Value: 0x0000031D

Indicates that the reverse power detection option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.122 FAVSC_MAC_RFTC_ERR_HWOPT_BADDRMFILTOPT

- Value: 0x0000031E

Indicates that the DRM Filter option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.123 FAVSC_MAC_RFTC_ERR_HWOPT_BADAMBTEMPOPT

- Value: 0x0000031F

Indicates that ambient temperature sensor option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.124 FAVSC_MAC_RFTC_ERR_HWOPT_BADPATEMPOPT

- Value: 0x00000320

Indicates that PA temperature sensor option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.125 FAVSC_MAC_RFTC_ERR_HWOPT_BADXCVRTEMPOPT

- Value: 0x00000321

Indicates that transceiver temperature sensor option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.126 FAVSC_MAC_RFTC_ERR_HWOPT_BADANTSENSOPT

- Value: 0x00000322

Indicates that antenna-sense resistor sensor option found in OEMCFG's HW_OPTIONS0 field is not recognized by the RFTC subsystem.

2.127 FAVSC_MAC_RFTC_ERR_BADIFLNAAGCRANGE

- Value: 0x00000323

The range specified for the IF LNA AGC gain limits is bad. Either the "min" is higher than the "max", or the min or max setting is incorrect.

2.128 FAVSC_MAC_RFTC_ERR_LPROFBADSELECTOR

- Value: 0x00000324

When invoking the CMD_LPROF_RDXCVRREG or CMD_LPROF_WRXCVRREG commands, one of the arguments is the selector of a valid link profile. New link profile selectors cannot be created through these commands, so if a selector outside this range is passed, the RFTC_ERR_LPROFBADSELECTOR error will be generated.

2.129 FAVSC_MAC_RFTC_ERR_BADXCVRADDR

- Value: 0x00000325

One of the arguments to the CMD_LPROF_RDXCVRREG or CMD_LPROF_WRXCVRREG commands is the RF transceiver register address to configure. If the address passed is not a valid transceiver address, this error will be thrown. This error is also generated if an invalid transceiver address is detected in an OEM custom profile.

2.130 FAVSC_MAC_RFTC_ERR_XCVRADDRNOTINLIST

- Value: 0x00000326

Not all valid transceiver addresses may be configured through the link profiles. The excluded addresses include those registers which are read-only (refer to the transceiver register map), and the indirect address for the R2T command register: 0x0105.

2.131 FAVSC_MAC_RFTC_ERR_BAD_RFLNA_GAIN_REQ

- Value: 0x00000327

Set by the RFTC module if an unsupported RFLNA gain level is requested.

2.132 FAVSC_MAC_RFTC_ERR_BAD_IFLNA_GAIN_REQ

- Value: 0x00000328

Set by the RFTC module if an unsupported IFLNA gain level is requested.

2.133 FAVSC_MAC_RFTC_ERR_BAD_AGCMIX_GAIN_REQ

- Value: 0x00000329

Set by the RFTC module if an unsupported AGC/MIXER gain level is requested.

2.134 FAVSC_MAC_RFTC_ERR_HWOPT_BADFWDPWRCOMPOPT

- Value: 0x0000032A

Set by the RFTC module if an unsupported compensation option is detected at OEMCFG address 0xA1.

2.135 FAVSC_MAC_RFTC_ERR_INVALID_PLL_DIVIDER_VALUE

- Value: 0x0000032B

This error is generated if the PLL Divider Value is zero.

2.136 FAVSC_MAC_RFTC_ERR_SJC_EXTERNALLOTOOLOW

- Value: 0x0000032C

This error is generated if the external LO signal level is below the threshold specified in register HST_RFTC_SJC_EXTERNALLOTHRSH.

2.137 FAVSC_MAC_RFTC_ERR_SJC_EXTERNALLONOTSELECTED

- Value: 0x0000032D

This error is generated if SJC is enabled, and the LO source is not external.

2.138 FAVSC_MAC_RFTC_ERR_BADLOSOURCE

- Value: 0x0000032E

This error is generated if the LO source is incorrectly defined in the OEM Config registers.

2.139 FAVSC_MAC_RFTC_ERR_GENERALRANDOMDATA

- Value: 0x0000032F

This error is generated if there is a general error in the Random Data Transmit function.

2.140 FAVSC_MAC_RFTC_ERR_XVCR_HEALTH_CHECK_FAIL

- Value: 0x00000330

This error is generated if there is Transceiver Health check failure and the handler is set to enable Mac Error. See OEM Config XCVR_HEALTH_CHECK_CFG.

2.141 FAVSC_MAC_RFTC_ERR_INVALID_OEM_PROFILE_HEADER

- Value: 0x00000331

This error is generated if the OEM custom profile header is invalid.

2.142 FAVSC_MAC_RFTC_ERR_AUTO_READ_RX_FIFO

- Value: 0x00000332

This error is generated if an error during the Auto Read of the Rx FIFO Read is detected.

2.143 FAVSC_MAC_RFTC_ERR_DC_OFFSET_CALIBRATION

- Value: 0x00000333

This error is general error generated if an error occurs during the DC Offset Calibration.

2.144 FAVSC_MAC_RFTC_ERR_LBT_RSSI_CALIBRATION

- Value: 0x00000334

This error is general error generated if an error occurs during the LBT RSSI Calibration. If noise floor versus calibration value do not have a significant difference this error will occur. User should check the injected reference signal for level and frequency.

2.145 FAVSC_MAC_RFTC_ERR_PA_BIAS_CAL_CONFIG

- Value: 0x00000335

This error is related to a PA Bias Calibration Configuration error.

2.146 FAVSC_MAC_RFTC_ERR_FWDPWRLEVERROR

- Value: 0x00000336

This error is generated when the requested forward power level is not achieved during power ramp.

See HST_ANT_DESC_RFPOWER for the power level requested, MAC_RFTC_PAPWRLEV for the power level achieved, and HST_RFTC_FWDPWRTHRSH for the error threshold.

2.147 FAVSC_MAC_RFTC_ERR_HWOPT_BADPABIASDACCTL

• Value: 0x00000337

Indicates that PA Bias DAC Control option found in OEMCFG's HW_OPTIONS2 field is not recognized by the RFTC subsystem.

2.148 FAVSC_MAC_RFTC_ERR_PA_BIAS_CAL_MEASUREMENT

• Value: 0x00000338

This error is related to a PA Bias Calibration measurement variation error.

2.149 FAVSC_MAC_RFTC_ERR_PA_BIAS_CAL_NOT_FOUND

• Value: 0x00000339

This error is related to a PA Bias Calibration when the target current is not found.

2.150 FAVSC_MAC_RFTC_ERR_GROSSGAIN_CONFIG_INVALID

• Value: 0x0000033A

This error is generated when the Gross Gain Config Value in the OEM is invalid. Min index must be less than Max, and Max must be less than the absolute max of 32.

2.151 FAVSC_MAC_RFTC_ERR_SJC_NOT_AVAILABLE_R500

• Value: 0x0000033B

This error is generated if SJC is enabled with an R500 device.

2.152 FAVSC_MAC_RFTC_ERR_GROSSGAIN_CALIBRATION

• Value: 0x0000033C

This error is general error generated if an error occurs during the Gross Gain Calibration. User should check the gross gain calibration configuration.

2.153 FAVSC_MAC_IO_INVAL_RDMASK

- Value: 0x00000401

This is set by the CPU support module when an attempt is make to read IO lines not configured for input. This may be due to internal firmware error or the host having incorrectly configured the Indy Development Platform GPIO lines.

2.154 FAVSC_MAC_IO_INVAL_WRMASK

- Value: 0x00000402

This is set by the CPU support module when an attempt is make to write IO lines not configured for output. This may be due to internal firmware error or the host having incorrectly configured the Indy Development Platform GPIO lines.

2.155 FAVSC_MAC_IO_INVAL_PTR_RAM

- Value: 0x00000403

This is set by the CPU module when a bounds check fails when accessing non-volatile memory - the caller has passed an incorrect RAM address. This is likely due to errant Indy Firmware code.

2.156 FAVSC_MAC_IO_INVAL_PTR_NV

- Value: 0x00000404

This is set by the CPU module when a bounds check fails when attempting to read or write to non-volatile memory. This is likely due to errant Indy Firmware code.

2.157 FAVSC_MAC_IO_INVAL_PTR_NV_ALIGN

- Value: 0x00000405

This is set by the CPU module when a bounds check fails when attempting to read or write to non-volatile memory. This is likely due to errant Indy Firmware code.

2.158 FAVSC_MAC_IO_NV_LOCK_ERR

- Value: 0x00000406

This is set by the CPU module while attempting to write to non-volatile memory (i.e. flash). This is a flash lock error and may be due to corrupted image or misconfigured firmware or hardware problems. If this error is detected by the host, it may which to attempt to read the devices OEM configuration area and save it on the host in order to preserve device specific settings.

2.159 FAVSC_MAC_IO_NV_PROG_ERR

- Value: 0x00000407

This is set by the CPU module while attempting to write to non-volatile memory (i.e. flash). This is a low-level flash write error and may be due to misconfigured firmware image, timing problems stemming from board hardware failures, or because the flash has exceeded is limitations for writes. . If this error is detected by the host, it may which to attempt to read the devices OEM configuration area and save it on the host in order to preserve device specific settings.

2.160 FAVSC_MAC_IO_OEMCFG_ADDR_BOUNDS

- Value: 0x00000408

This is set by the OEM Configuration module when an OEM configuration Address bounds check fails when accessing the OEM configuration space. This may be due to errant Indy Firmware code or errant Host code.

2.161 FAVSC_MAC_IO_OEMCFG_NV_BOUNDS

- Value: 0x00000409

This is set by the OEM Configuration module when a non-volatile memory bounds check fails when accessing the OEM configuration space. This may be due to errant Indy Firmware code or errant Host code.

2.162 FAVSC_MAC_IO_OEMCFG_FMT_KEY

- Value: 0x0000040A

This is set by the OEM Configuration module's format facility used as the code calling it fails to pass in the correct "format key" argument. This is a failsafe to prevent errant code from inadvertently reformatting flash - due to an invalid branch instruction etc. This will occur when errant code jumps to the format facility incorrectly.

2.163 FAVSC_MAC_IO_OEMCFG_FLUSH

- Value: 0x0000040B

This is set by the OEM Configuration module when it fails to flush in memory buffers to non-volatile memory. This may be due to misconfigured firmware image, timing problems stemming from board hardware failures, or because the flash has exceeded its limitations for writes. If this error is detected by the host, it may switch to attempt to read the devices OEM configuration area and save it on the host in order to preserve device specific settings.

2.164 FAVSC_MAC_IO_OEMCFG_FORMAT

- Value: 0x0000040C

This is set by the OEM Configuration module when it fails to detect the correct low level file system headers for the OEM configuration area. This means that the OEM configuration area has not been formatted - due to a misconfigured board or that the OEM Configuration area has become corrupt and should not be trusted without attempting recovery or reconfiguration.

2.165 FAVSC_MAC_IO_INVAL_IORSVD

- Value: 0x0000040D

This is set by the CPU module when an attempt is made to configure reserved IO pins. This is likely due to a misconfigured firmware build or errant Indy Firmware code.

2.166 FAVSC_MAC_IO_OEMCFG_STRING_TYPE

- Value: 0x0000040E

This is set by the OEM Configuration module when an invalid string type is selected.

2.167 FAVSC_MAC_IO_OEMCFG_STRING_LENGTH

- Value: 0x0000040F

This is set by the OEM Configuration module when an invalid string length is entered.

2.168 FAVSC_MAC_IO_OEMCFG_STRING_CHARACTER

- Value: 0x00000410

This is set by the OEM Configuration module when an invalid character is entered.

2.169 FAVSC_MAC_IO_OEMCFG_STRING_CURRENT_INVALID

- Value: 0x00000411

This is set by the OEM Configuration module when an string read cannot be read correctly since the current string has an invalid header.

2.170 FAVSC_MAC_IO_OEMCFG_FORMAT_KEY_INVALID

- Value: 0x00000412

This is set by the OEM Configuration module when the generated key does not match the check key when attempting to format the OEM Configuration space.

2.171 FAVSC_MAC_IO_OEMCFG_FORMAT_CONFIGURATION_INVALID

- Value: 0x00000413

This is set by the OEM Configuration module when an invalid format configuration is specified.

2.172 FAVSC_MAC_IO_INVAL_NV_SECTOR

- Value: 0x00000414

This is set by the CPU module while attempting to lock or unlock a flash sector and the specified sector is invalid.

2.173 FAVSC_MAC_TILDENIF_ERR_ADDRMISMAT

- Value: 0x00000601

This is set by the UHF RFID Transceiver interface module when an UHF RFID Transceiver register read, when configured for Serial port mode, returns the incorrect register address in the serial response frame. This could be due to board or UHF RFID Transceiver hardware problems or errant Indy Firmware code.

2.174 FAVSC_MAC_TILDENIF_ERR_RDFAILSAFE

- Value: 0x00000602

This is set by the UHF RFID Transceiver interface module when failsafe logic is activated due to no response from UHF RFID Transceiver. This happens on UHF RFID Transceiver register reads. This could be due to board or UHF RFID Transceiver hardware problems.

2.175 FAVSC_MAC_TILDENIF_ERR_INVALPWRST

- Value: 0x00000603

Set by the low level interface logic if, during power management, an invalid power state is requested. This will likely only occur if the system is corrupted.

2.176 FAVSC_MAC_TILDENIF_ERR_INVALID_SETTING_R500

- Value: 0x00000604

Set by the low level interface logic if, during a write, an invalid setting is selected.

2.177 FAVSC_MAC_BIST_ERR_RF_IO_REG_CHK

- Value: 0x00000701

This error code is set during firmware boot when the Built In Self Test code is executed. This error indicates that certain register power up defaults on UHF RFID Transceiver were not detected - possibly indicating a hardware problem.

2.178 FAVSC_MAC_BIST_ERR_RF_REG_BITS

- Value: 0x00000702

This error code is set during firmware boot when the Built In Self Test code is executed. This error indicates that a walking 1's or walking 0's bus test failed - possibly indicating a hardware problem.

2.179 FAVSC_GEN_ERR

- Value: 0x10000001

Server encountered an error.

2.180 FAVSC_GEN_ERR_INVALID_PREAMBLE

- Value: 0x10000002

Socket Command Packet Preamble is incorrect.

2.181 FAVSC_GEN_ERR_INVALID_MSG_TYPE

- Value: 0x10000003

Socket Command Packet Message Type is invalid.

2.182 FAVSC_GEN_ERR_INVALID_MSG_CODE

- Value: 0x10000004

Socket Command Packet Message Code is invalid.

2.183 FAVSC_GEN_ERR_INVALID_CHECKSUM

- Value: 0x10000005

Socket Command Packet Checksum is incorrect.

2.184 FAVSC_GEN_ERR_INVALID_END_MARK

- Value: 0x10000006

Socket Command Packet End Mark is incorrect.

2.185 FAVSC_GEN_ERR_INVALID_PACKET_FORMAT

- Value: 0x10000007

Socket Command Packet Format is incorrect.

2.186 FAVSC_GEN_ERR_INVALID_PAYLOAD_LENGTH

- Value: 0x10000008

Socket Command Packet Length is incorrect.

2.187 FAVSC_GEN_ERR_INVALID_PARAMETER

- Value: 0x10000009

The parameter of Socket Command Packet is invalid.

2.188 FAVSC_GEN_ERR_COMMAND_NOT_SUPPORT

- Value: 0x1000000A

The Socket Command is not supported.

2.189 FAVSC_GEN_ERR_BUSY

- Value: 0x1000000B

Server is busy.

2.190 FAVSC_GEN_ERR_TIMEOUT

- Value: 0x1000000C

Command Operation is time out.

2.191 FAVSC_GEN_ERR_WAIT_READER_INIT

- Value: 0x1000000D

Server is waiting for Reader module to be initialized.

2.192 FAVSC_GEN_ERR_READER_MODULE_RESPONSE_TIMEOUT

- Value: 0x1000000E

Reader operation is time out.

2.193 FAVSC_GEN_ERR_WRITE_CONFIG

- Value: 0x1000000F

Server is failed to write config value.

2.194 FAVSC_GEN_ERR_READ_CONFIG

- Value: 0x10000010

Server is failed to read config value.

2.195 FAVSC_GEN_ERR_NO_MEMORY

- Value: 0x10000011

Server is running out of memory.

2.196 FAVSC_GEN_ERR_NETWORK_DEVICE_NOT_READY

- Value: 0x10000012

Network device is not ready.

2.197 FAVSC_GEN_ERR_NETWORK_CONNECTION_NOT_START

- Value: 0x10000013

Network connection is not started.

2.198 FAVSC_GEN_ERR_WIRELESS_PARSE_ERROR

- Value: 0x10000014

Server is failed to parse wireless result data.

2.199 FAVSC_GEN_ERR_VALUE_NOTAVAIL

- Value: 0x10000015

The requested value is not available.

2.200 FAVSC_GPIO_ERR

- Value: 0x20000001

GPIO encountered an error.

2.201 FAVSC_GPIO_ERR_SERIAL_PORT_NOT_OPEN

- Value: 0x20000002

GPIO module is not connected.

2.202 FAVSC_GPIO_ERR_INVALID_VERSION

- Value: 0x20000003

GPIO Command Packet Version is invalid.

2.203 FAVSC_GPIO_ERR_INVALID_TYPE

- Value: 0x20000004

GPIO Command Packet Type Code is invalid.

2.204 FAVSC_GPIO_ERR_INVALID_CMD

- Value: 0x20000005

GPIO Command Packet Command Code is invalid.

2.205 FAVSC_GPIO_ERR_INVALID_TIME

- Value: 0x20000006

GPIO is failed by the process time.

2.206 FAVSC_GPIO_ERR_INVALID_MASK

- Value: 0x20000007

GPIO is failed by the mask value.

2.207 FAVSC_GPIO_ERR_INVALID_CHECKSUM

- Value: 0x20000008

GPIO Command Packet Checksum is incorrect.

2.208 FAVSC_GPIO_ERR_COMMAND_TIMEOUT

- Value: 0x20000009

GPIO Command Operation is time out.

2.209 FAVSC_GPIO_ERR_COMMAND_BYTE_TIMEOUT

- Value: 0x2000000A

The data transited back to SCO server is over time.

2.210 FAVSC_READER_ERR_INVALID_INV_CALLBACK

- Value: 0x30000001

Inventory callback is invalid.

2.211 FAVSC_READER_ERR_INVALID_ACCESS_CALLBACK

- Value: 0x30000002

Access commands callback is invalid.

2.212 FAVSC_READER_OK_ACCESS_CMD_CALLBACK

- Value: 0x30000003

Access commands callback is successful.

2.213 FAVSC_READER_ERR_ACCESS_CMD_CALLBACK

- Value: 0x30000004

Access commands callback failed.

2.214 FAVSC_READER_ERR_SET_ACCESS_ANTENNA_PORT

- Value: 0x30000005

Set antenna port parameters failed.

2.215 FAVSC_READER_ERR_RESTORE_ACCESS_ANTENNA_PORT

- Value: 0x30000006

Restore antenna port settings for access command failed.

2.216 FAVSC_READER_ERR_SET_ANTENNA_TYPE_BI

- Value: 0x30000007

Set antenna type to Bi-static failed.

2.217 FAVSC_READER_ERR_DISABLE_ANTENNA_STATE

- Value: 0x30000008

Disable antenna port state failed.

2.218 FAVSC_READER_ERR_SET_PORT_DWELL_TIME_ZERO

- Value: 0x30000009

Set dwell time of the antenna port to ZERO failed.

2.219 FAVSC_READER_ERR_SET_PORT_INV_CYCLE_ZERO

- Value: 0x3000000A

Set inventory cycle of the antenna port to ZERO failed.

2.220 FAVSC_READER_ERR_NO_READ_PARMS

- Value: 0x3000000B

The parameter of Read command is empty.

2.221 FAVSC_READER_ERR_NO_WRITE_PARMS

- Value: 0x3000000C

The parameter of Write command is empty.

2.222 FAVSC_READER_ERR_NO_KILL_PARMS

- Value: 0x3000000D

The parameter of Kill command is empty.

2.223 FAVSC_READER_ERR_NO_LOCK_PARMS

- Value: 0x3000000E

The parameter of Lock command is empty.

2.224 FAVSC_READER_ERR_NO_BLOCKWRITE_PARMS

- Value: 0x3000000F

The parameter of Block Write command is empty.

2.225 FAVSC_READER_ERR_NO_BLOCKERASE_PARMS

- Value: 0x30000010

The parameter of Block Erase command is empty.

2.226 FAVSC_READER_ERR_SET_ANTENNA_STATE

- Value: 0x30000011

Set the antenna port state failed.

2.227 FAVSC_READER_ERR_NO_FAV_READ_PARMS

- Value: 0x30000013

The parameter of FAV Read command is empty.

2.228 FAVSC_RFID_ERR_ALREADY_OPEN

- Value: 0xFFFFD8F1

Attempted to open a radio that is already open.

2.229 FAVSC_RFID_ERR_BUFFER_TOO_SMALL

- Value: 0xFFFFD8F2

Buffer supplied is too small.

2.230 FAVSC_RFID_ERR_FAILURE

- Value: 0xFFFFD8F3

General failures.

2.231 FAVSC_RFID_ERR_DRIVER_LOAD

- Value: 0xFFFFD8F4

The radio bus driver is failed to be loaded.

2.232 FAVSC_RFID_ERR_DRIVER_MISMATCH

- Value: 0xFFFFD8F5

Library cannot use version of radio bus driver present on system.

2.233 FAVSC_RFID_ERR_RESERVED_01

- Value: 0xFFFFD8F6

This error code is no longer used, but is retained for backward compatibility, in case any host applications are using hard coded error numbers.

2.234 FAVSC_RFID_ERR_INVALID_ANTENNA

- Value: 0xFFFFD8F7

Antenna number is invalid.

2.235 FAVSC_RFID_ERR_INVALID_HANDLE

- Value: 0xFFFFD8F8

Radio handle provided is invalid.

2.236 FAVSC_RFID_ERR_INVALID_PARAMETER

- Value: 0xFFFFD8F9

One of the parameters is invalid to the function.

2.237 FAVSC_RFID_ERR_NO_SUCH_RADIO

- Value: 0xFFFFD8FA

The radio turned on is not existed.

2.238 FAVSC_RFID_ERR_NOT_INITIALIZED

- Value: 0xFFFFD8FB

Library has not been successfully initialized.

2.239 FAVSC_RFID_ERR_NOT_SUPPORTED

- Value: 0xFFFFD8FC

Function is not supported.

2.240 FAVSC_RFID_ERR_OPERATION_CANCELLED

- Value: 0xFFFFD8FD

Operation was cancelled by cancelling operation, closing radio, or shutting down the library.

2.241 FAVSC_RFID_ERR_OUT_OF_MEMORY

- Value: 0xFFFFD8FE

Library encountered an error located in memory.

2.242 FAVSC_RFID_ERR_RADIO_BUSY

- Value: 0xFFFFD8FF

The operation cannot be performed because the radio is currently busy.

2.243 FAVSC_RFID_ERR_RADIO_FAILURE

- Value: 0xFFFFD900

The underlying radio module encountered an error.

2.244 FAVSC_RFID_ERR_RADIO_NOT_PRESENT

- Value: 0xFFFFD901

The radio has been detached from the system.

2.245 FAVSC_RFID_ERR_CURRENTLY_NOT_ALLOWED

- Value: 0xFFFFD902

The RFID library function is not allowed at this time.

2.246 FAVSC_RFID_ERR_RADIO_NOT_RESPONDING

- Value: 0xFFFFD903

The radio module's MAC firmware is not responding to requests.

2.247 FAVSC_RFID_ERR_NONVOLATILE_INIT_FAILED

- Value: 0xFFFFD904

The MAC firmware encountered an error while initiating the nonvolatile memory update. The MAC firmware will return to its normal idle state without resetting the radio module.

2.248 FAVSC_RFID_ERR_NONVOLATILE_OUT_OF_BOUNDS

- Value: 0xFFFFD905

An attempt was made to write data to an address that is not in the valid range of radio module nonvolatile memory addresses.

2.249 FAVSC_RFID_ERR_NONVOLATILE_WRITE_FAILED

- Value: 0xFFFFD906

The MAC firmware encountered an error while trying to write to the radio module's nonvolatile memory region.

2.250 FAVSC_RFID_ERR_RECEIVE_OVERFLOW

- Value: 0xFFFFD907

The underlying transport layer detected that there was an overflow error resulting in one or more bytes of the incoming data being dropped. The operation was aborted and all data in the pipeline was flushed.

2.251 FAVSC_RFID_ERR_UNEXPECTED_VALUE

- Value: 0xFFFFD908

An unexpected value was returned to this function by the MAC firmware

2.252 FAVSC_RFID_ERROR_NONVOLATILE_CRC_FAILED

- Value: 0xFFFFD909

The MAC firmware encountered CRC errors while trying to write to the radio module's nonvolatile memory region.

2.253 FAVSC_RFID_ERROR_NONVOLATILE_PACKET_HEADER

- Value: 0xFFFFD90A

The MAC firmware encountered unexpected values in the packet header.

2.254 FAVSC_RFID_ERROR_NONVOLATILE_MAX_PACKET_LENGTH

- Value: 0xFFFFD90B

The MAC firmware received more than the specified maximum packet size.