Huijie Pan

1210 Shaffer RD, Santa Cruz, CA, 95060

www.linkedin.com/in/huijie-pan1996 hpan996@gmail.com 831-251-7215

Graduate computer engineer completing the first year of a master's degree. Passionate about engineering, with good technical and interpersonal skills for working in a team and successfully completing a project. Currently looking for a software engineering part-time internship now and also open for full-time job opportunities starting from January 1st 2020.

EDUCATION

University of California, Santa Cruz **Santa Cruz,CA**
Master in Computer Science and Engineering (Ongoing) *Expected graduation in Dec 2019*

University of California, Santa Cruz **Santa Cruz,CA**
Bachelor of Science in Computer Engineering with a concentration in digital hardware *June 2018*

KEY SKILLS

- **Programming Languages:** C/C++, Java, Python, Verilog , Assembly, Bash, Matlab, TeX .
- **Software:** Qt, Atmel Studio, Visual Studio, Xilinx Vivado, Eclipse, Vim, PSoC Creator
- **Hardware:** PSoC, Raspberry Pi, BASYS FPGA, SAMD Chip Family
- **Operating Systems:** Linux, Windows
- **Relevant Coursework** Data Structures, Algorithms, Numerical Linear Algebra, Accelerator Microprocessor System, VLSI Design, Logic Design, Assembly Language, Physics, Digital Signal Processing, Circuits

ACADEMIC PROJECTS

Xilinx Rapidwright interface with Lgraph (Onging Master Project) *Fall 2018 - present*
- Developing a program to build a bridge between Lgraph and Vivado using a Xilinx program called RapidWright.

Seal Heart And Activity Tracker (Fall 2017 Undergraduate Senior Design) *Sep 2017 - June 2018*
- Became part of a team designing and developing a low cost, expandable, and open source wildlife activity tracker used for tracking behavior of Northern Elephant Seals.
- Developed the software library for accelerometer and magnetometer sensors on-board.
- Developed a front-end application to configure each sensor on the board for users, live stream data from the device and save the data to a file

Digital Oscilloscope and Logic Analyzer *Sep 2017 - Dec 2017*
- Used PSoC-5 microcontroller and a Raspberry Pi to design an 8-channel logic analyzer, This scope is capable of sampling analog signals up to 1000kHz and displaying both waveforms on a screen connected to the Pi. The trigger conditions are able to be set as well as x- and y-axis scales.

EXPERIENCE

UC Santa Cruz **Santa Cruz, CA**
Teaching Assistant for Computer Architecture, Computer Systems *Oct 1st 2018–present*
- Created new laboratory assignments, homework and exams using hardware and software concepts discussed in the courses.
- Assisted students in a laboratory environment with assembly language and logic design.
- Graded students' exams, homework, and lab work.
- Gave mini-lecture in lab sections

Small Group Tutor for Logic Design **Santa Cruz, CA**
- Prepared tutoring sessions to ensure students' academic development. *April 2018 – June 2018*
- Assessed the students' progress throughout tutoring sessions

Honors

- **Undergraduate Dean's Award** *2014 Fall–2018 Spring*