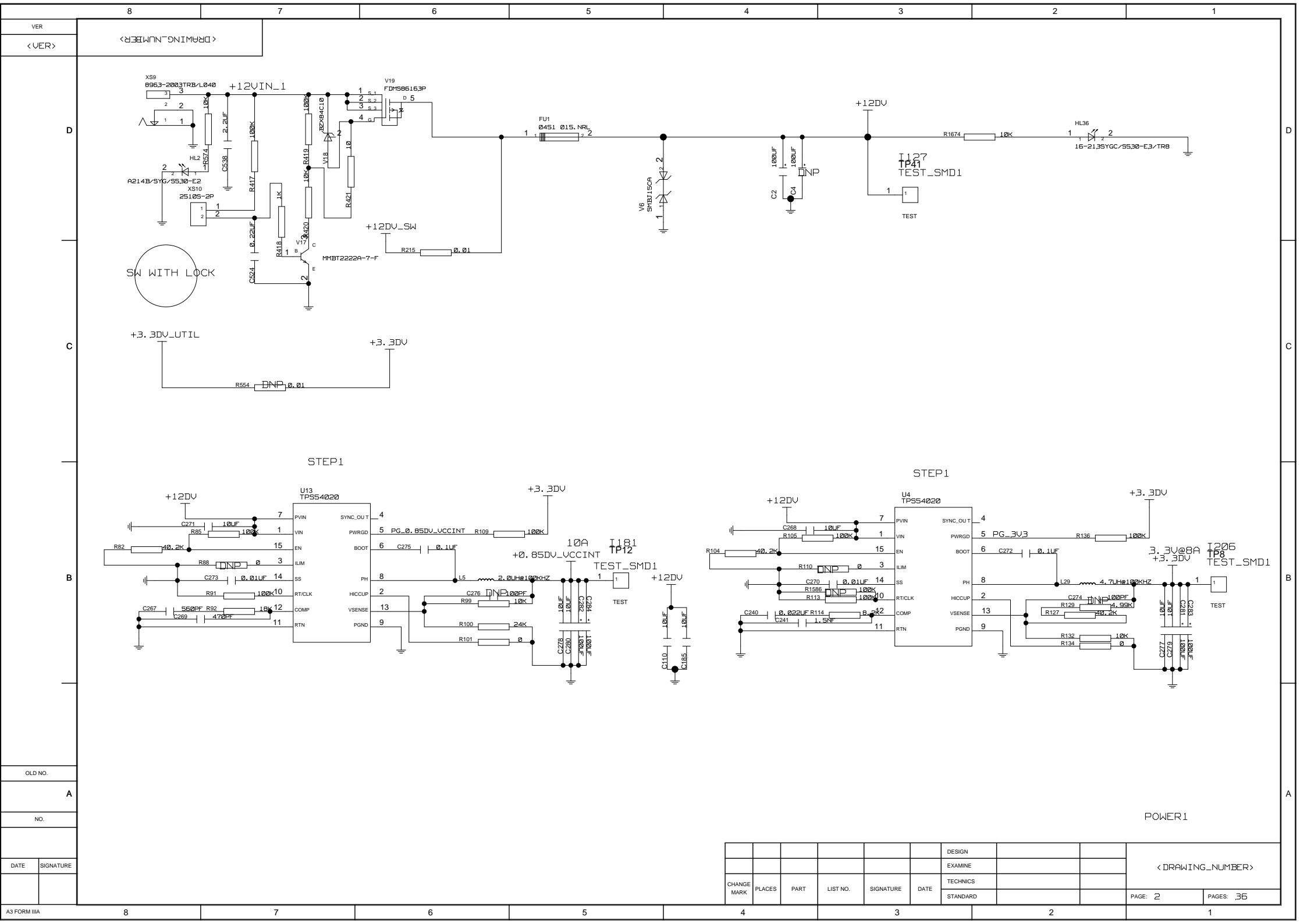
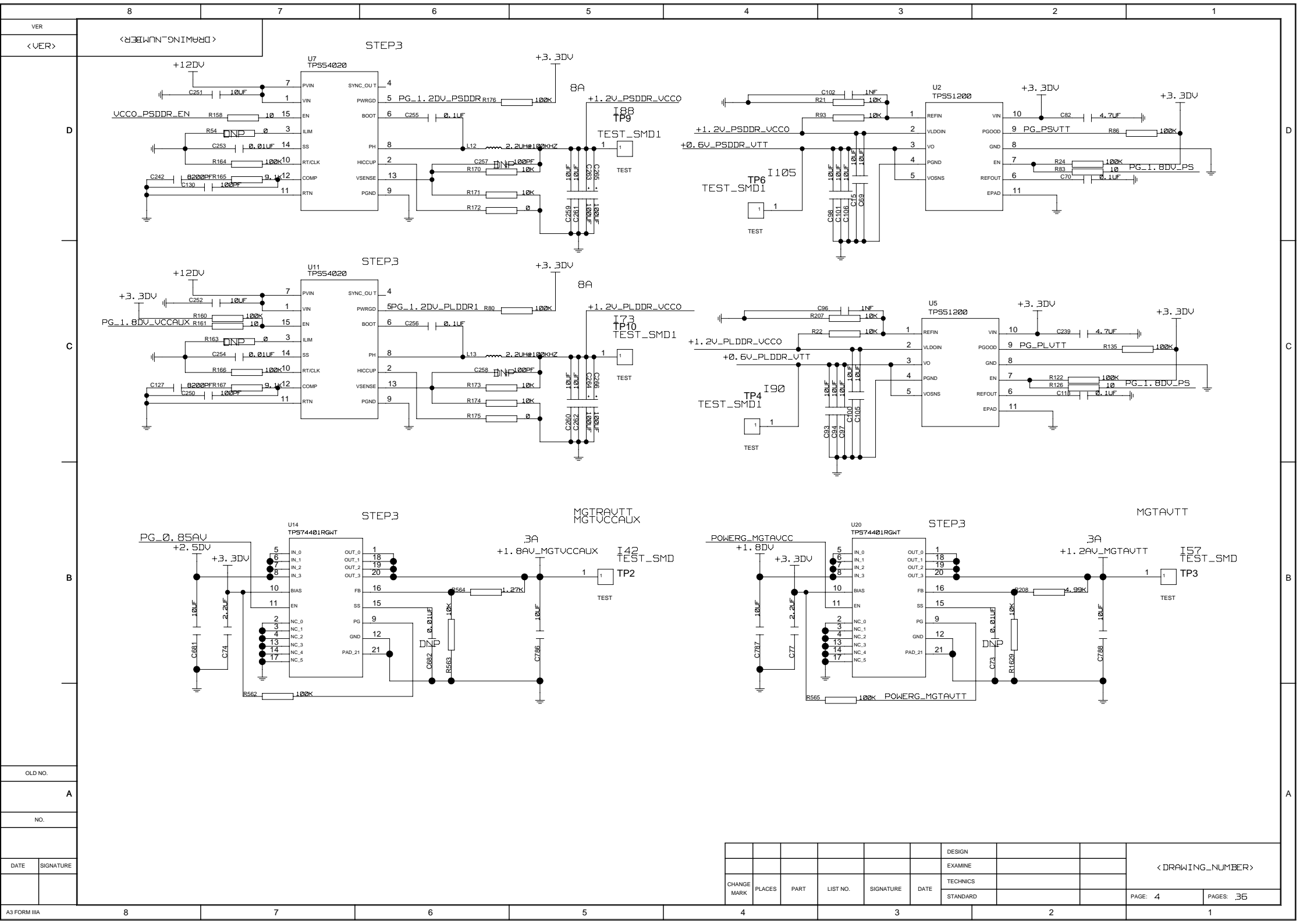
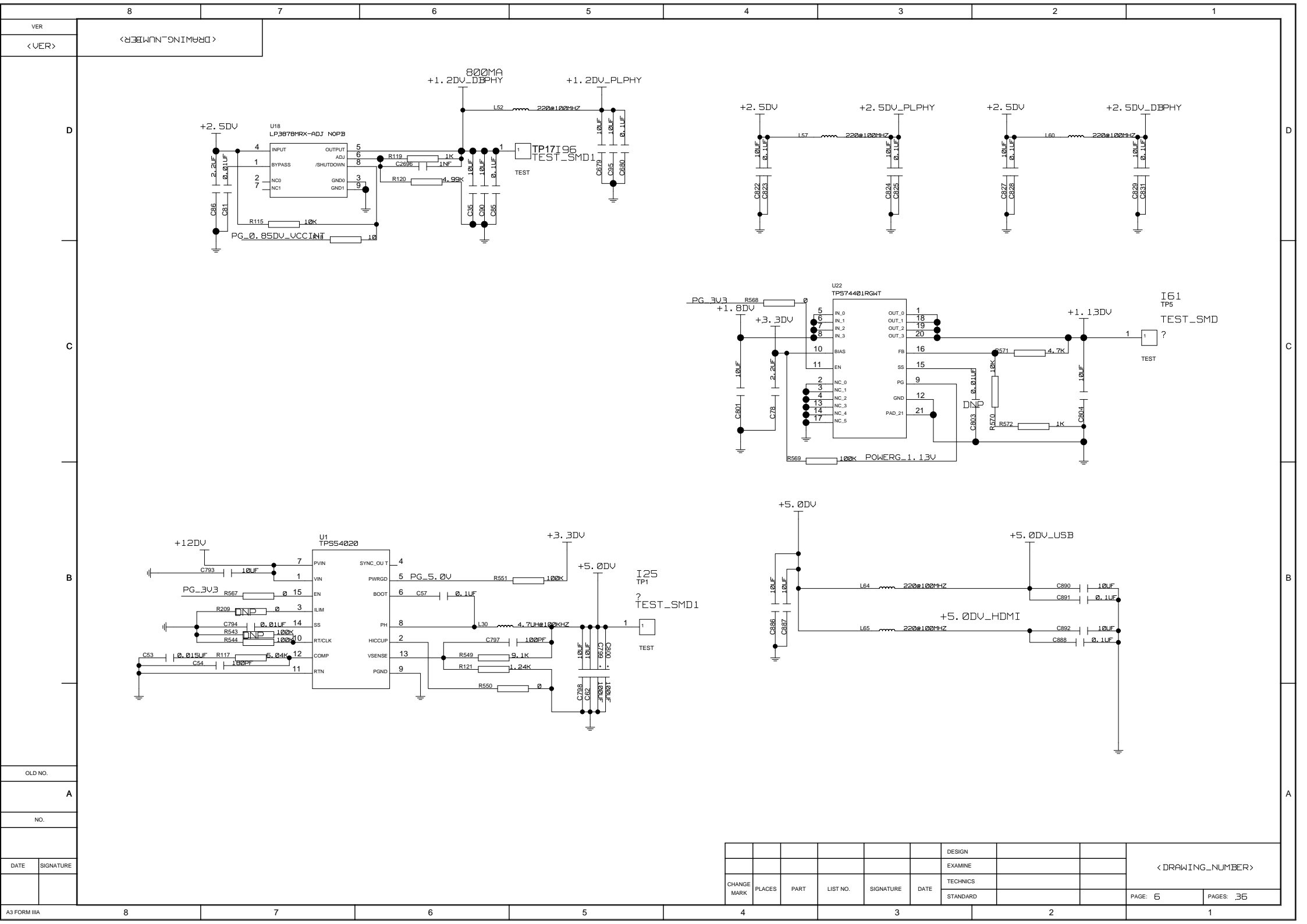


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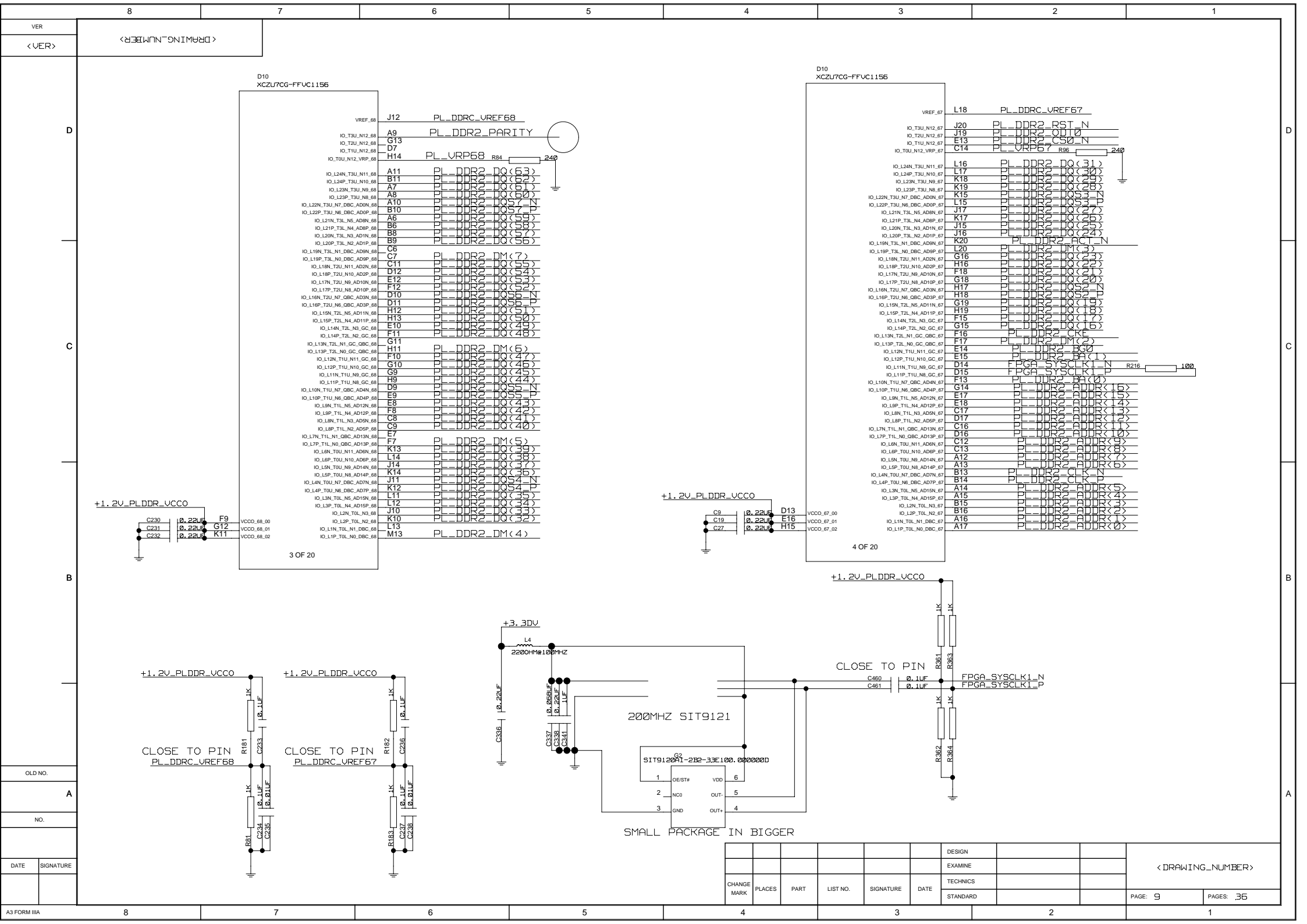
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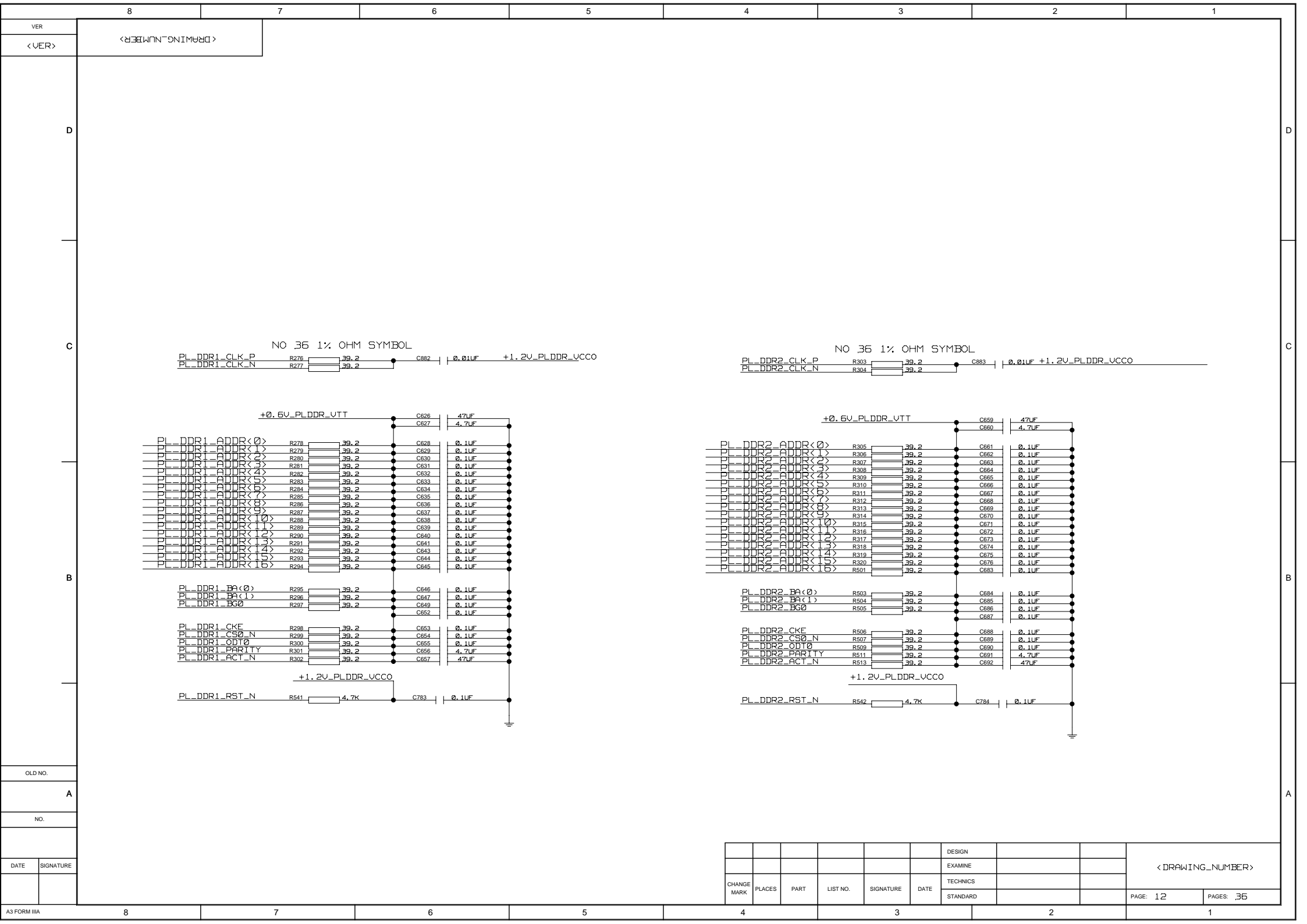
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PL_DDR1_RST_N

R541

4.7K

C783

0.1UF

+0.6V_PLDDR_VTT

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R305

39.2

C661

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R506

39.2

C688

0.1UF

PL_DDR2_CS0_N

R507

39.2

C689

0.1UF

PL_DDR2_ODT0

R509

39.2

C690

0.1UF

PL_DDR2_PARITY

R511

39.2

C691

4.7UF

PL_DDR2_ACT_N

R513

39.2

C692

47UF

+1.2V_PLDDR_VCCO

PL_DDR2_RST_N

R542

4.7K

C784

0.1UF

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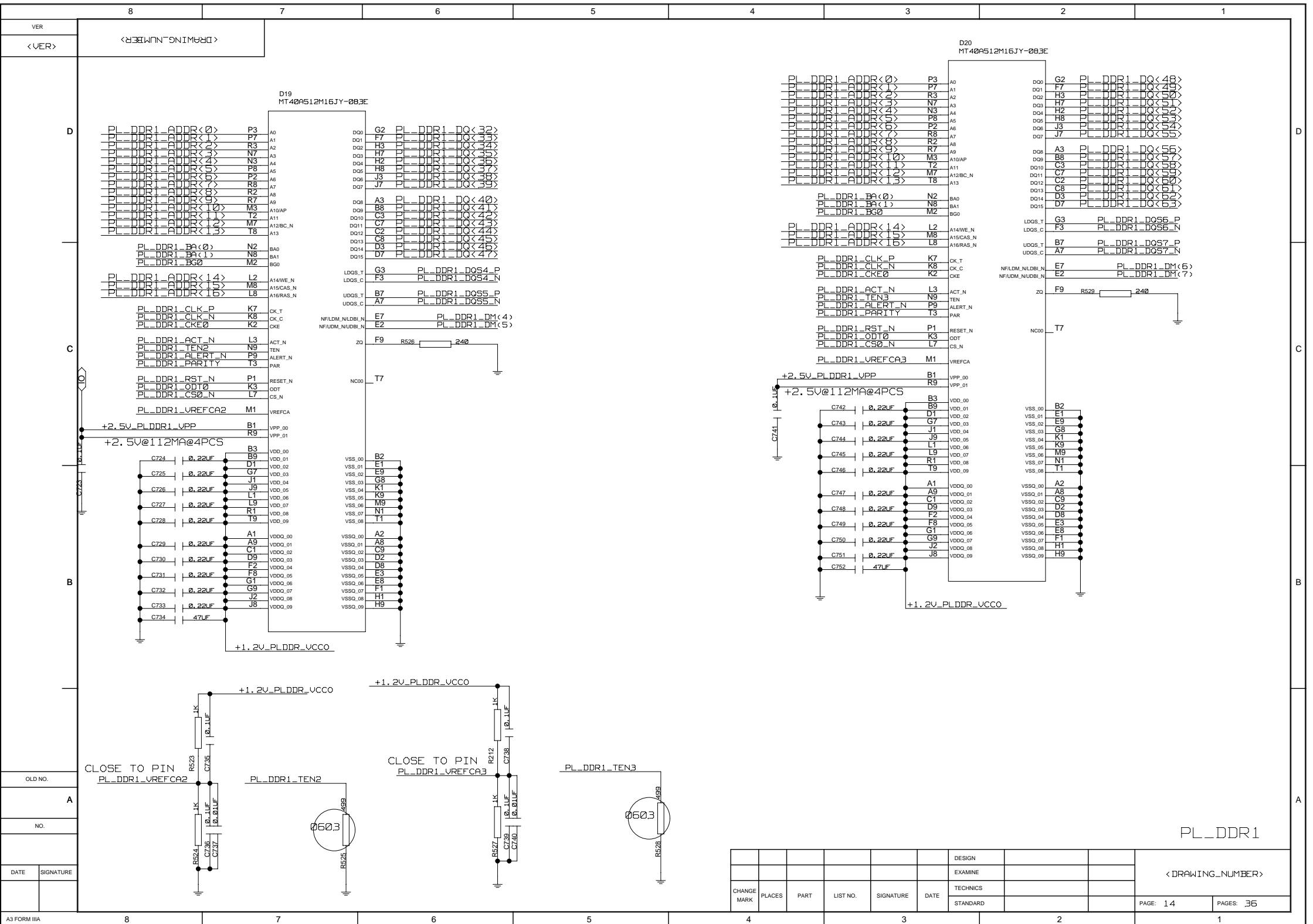
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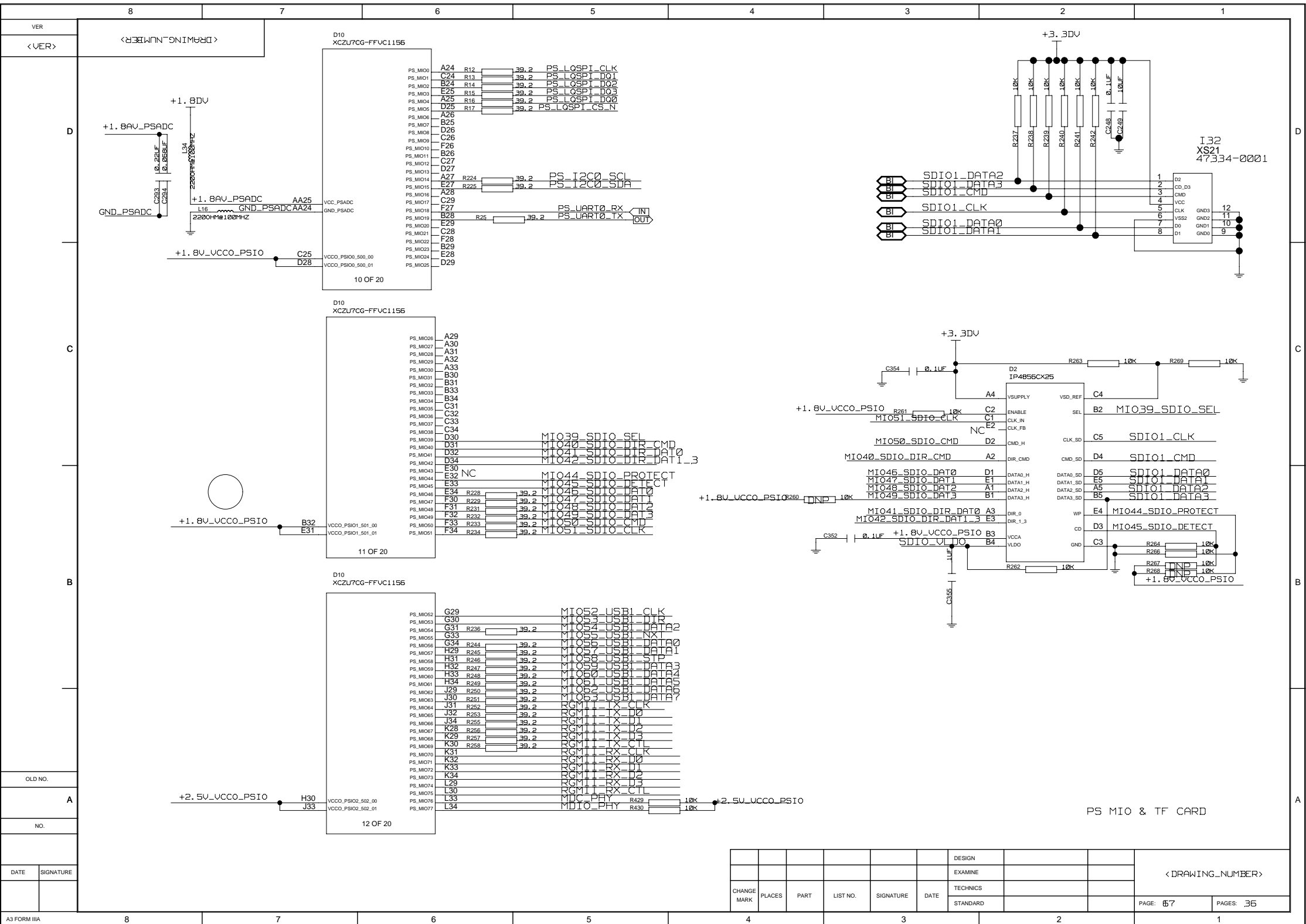
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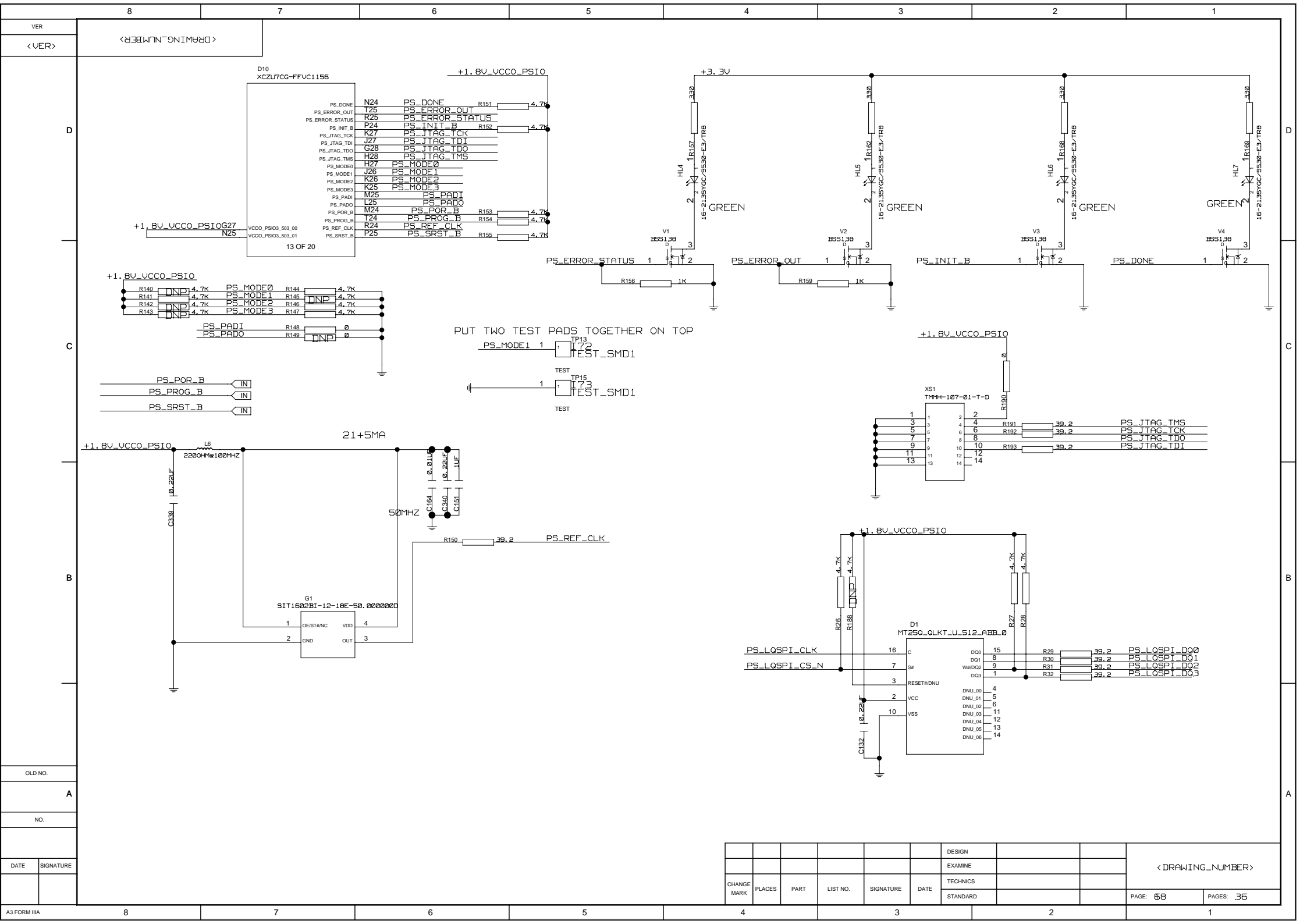
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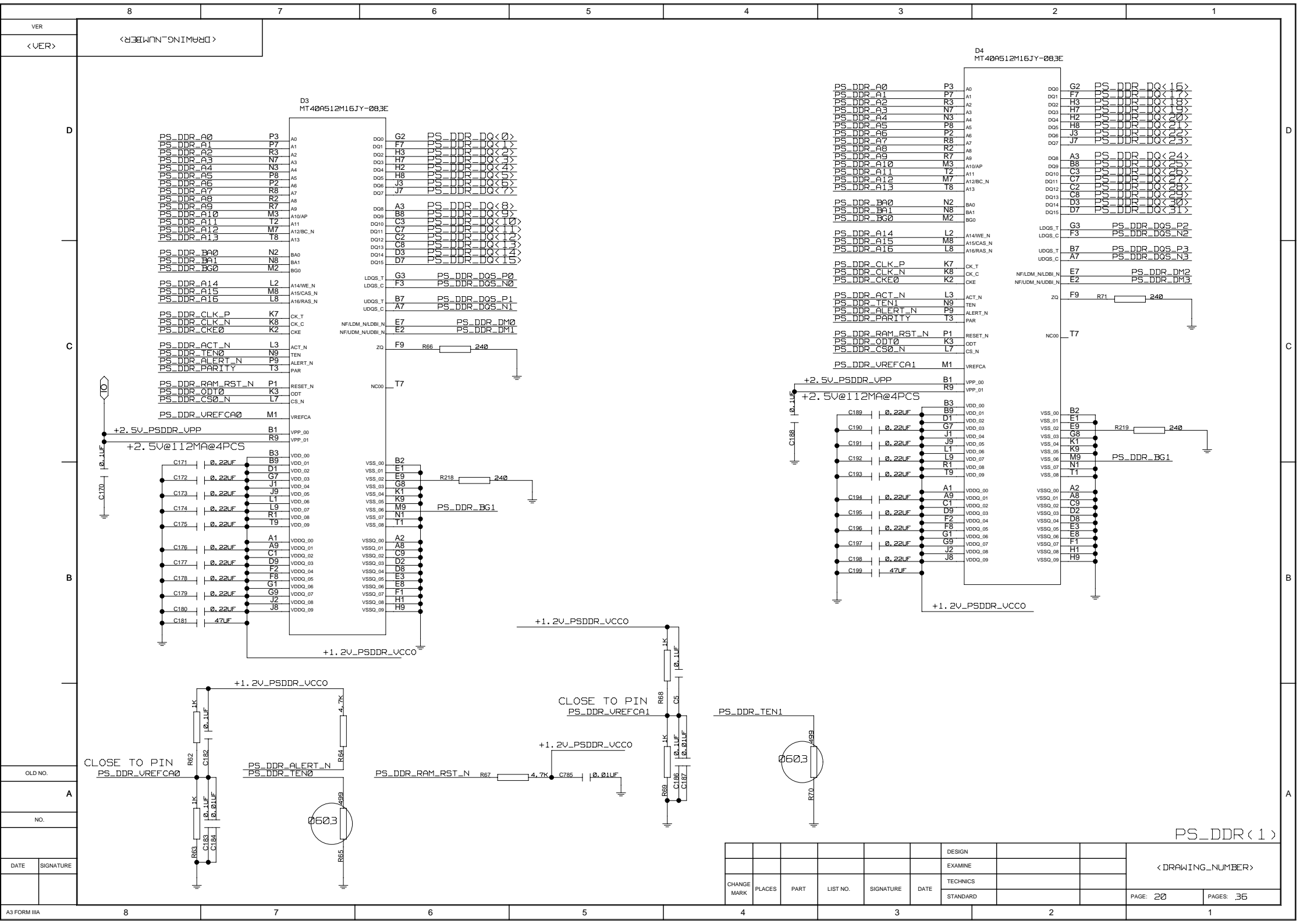
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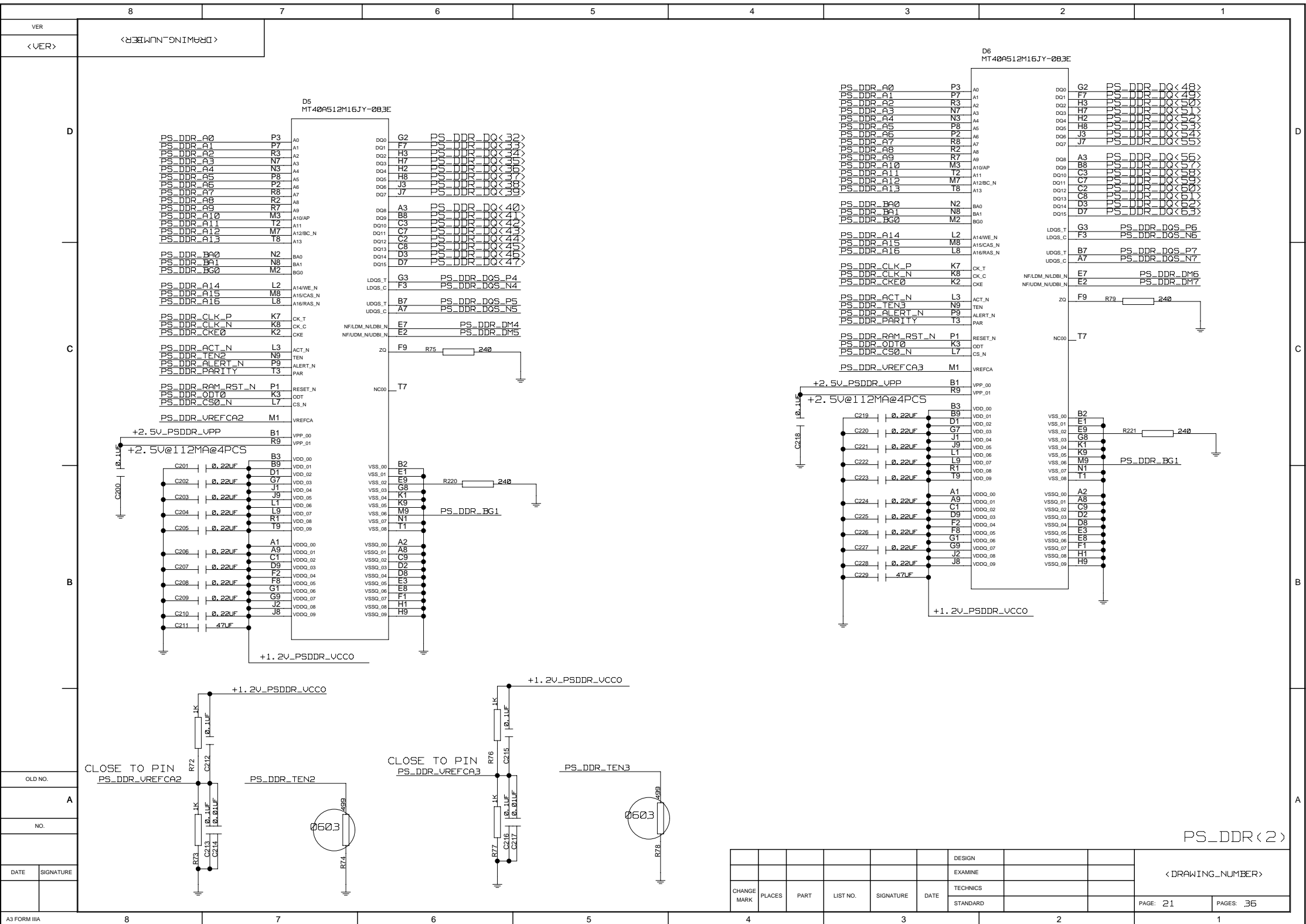


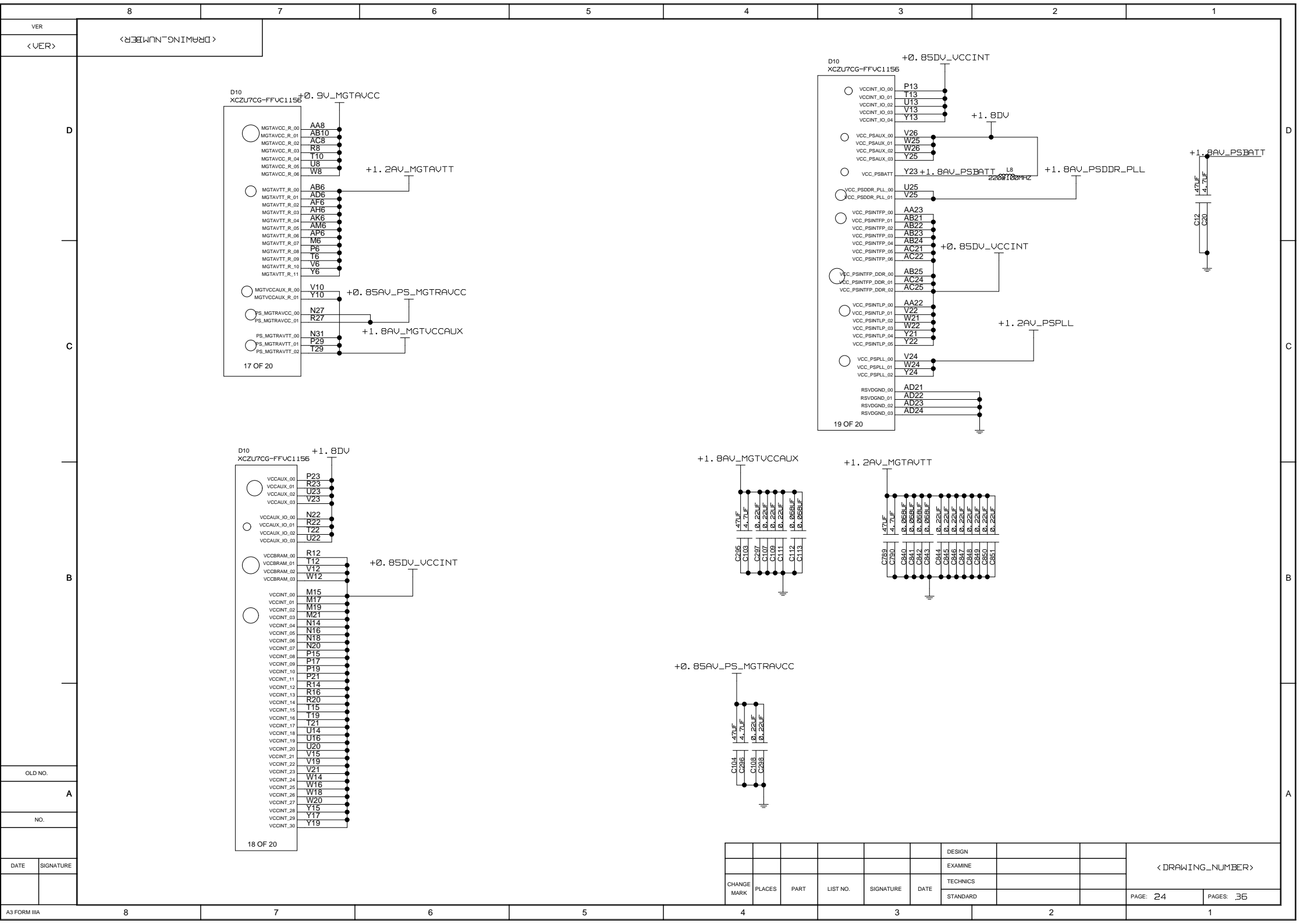












+1.8DV

+0.85DV_UCCINT

+0.85DV_UCCINT

+1.8DV

+1.8AV_PSBATT

+1.8AV_PSDDDR_PLL

+0.85DV_UCCINT

+1.2AV_PSPLL

+1.8AV_MGTVCCAUX

+1.2AV_MGTAUVT

+0.85AV_PS_MGTRAVCC

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