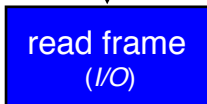


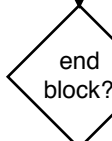
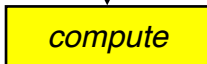
$t_{\text{opening_trajectory}}$



$t_{\text{I/O}}$



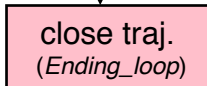
t_{comp}



no

yes

$t_{\text{end_loop}}$



t_{comm}

