

Table 8-9 Instruction cache data RAM sizes, no parity or ECC

| Cache size | Data RAMs |
|-------------------|---|
| 4KB, 4 1KB ways | 4 banks 64 bits 128 lines or 8 banks 32 bits 128 lines |
| 8KB, 4 2KB ways | 4 banks 64 bits 256 lines or 8 banks 32 bits 256 lines |
| 16KB, 4 4KB ways | 4 banks 64 bits 512 lines or 8 banks 32 bits 512 lines |
| 32KB, 4 8KB ways | 4 banks 64 bits 1024 lines or 8 banks 32 bits 1024 lines |
| 64KB, 4 16KB ways | 4 banks 64 bits 2048 lines or 8 banks 32 bits 2048 lines |

Table 8-10 Data cache data RAM sizes, no parity or ECC

| Cache size | Data RAMs |
|-------------------|----------------------------|
| 4KB, 4 1KB ways | 8 banks 32 bits 128 lines |
| 8KB, 4 2KB ways | 8 banks 32 bits 256 lines |
| 16KB, 4 4KB ways | 8 banks 32 bits 512 lines |
| 32KB, 4 8KB ways | 8 banks 32 bits 1024 lines |
| 64KB, 4 16KB ways | 8 banks 32 bits 2048 lines |