



Memory Clock

Data

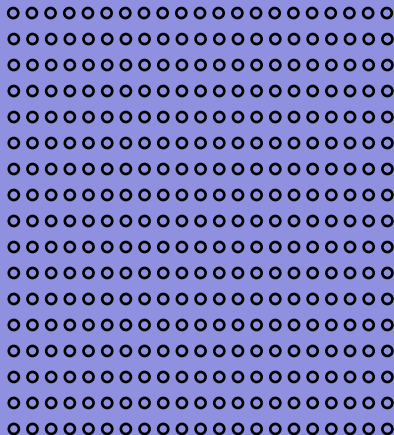


Address



IMPACT Memory

Multi row decoder



Bitcell + logic operation

CTRL

Parallel ALU

IN/OUT DFF/LATCH