

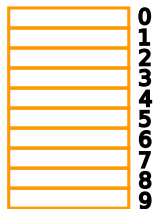
Memory



Instruction encoding



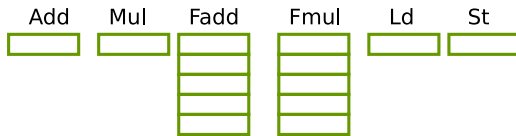
Register Bank



Instruction examples

add r1, r2, r9
ld r3, [r3, r5]

Functional units



Latency
Delay