

System block diagram 先楫半导体

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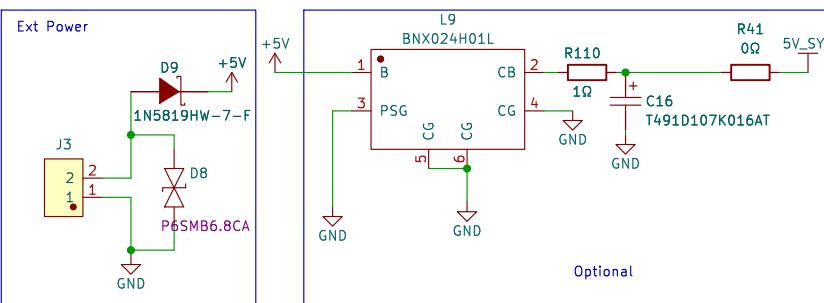
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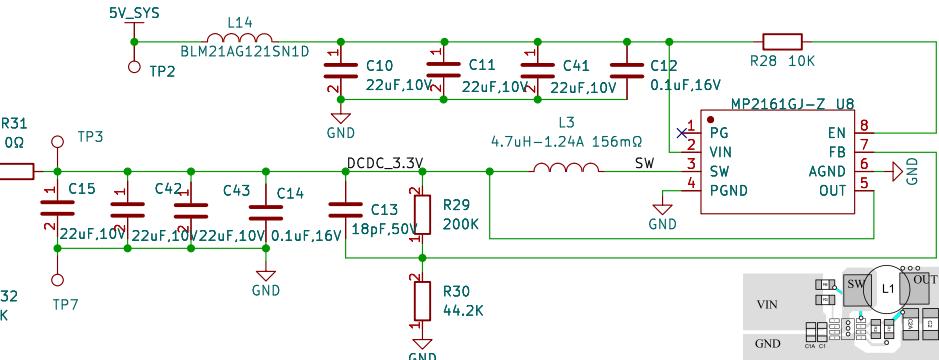
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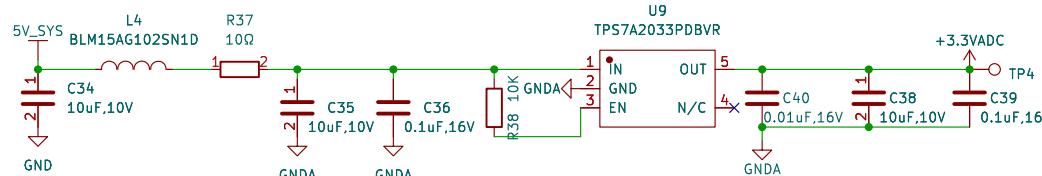
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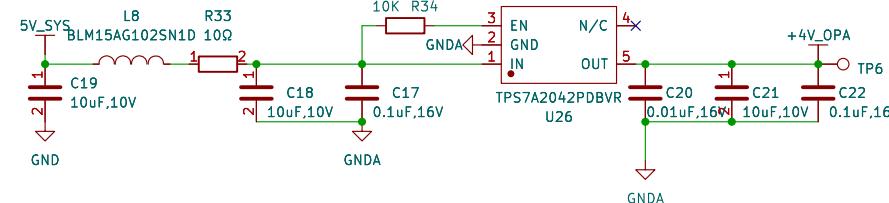
DCDC 5V-3.3V



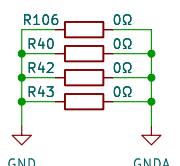
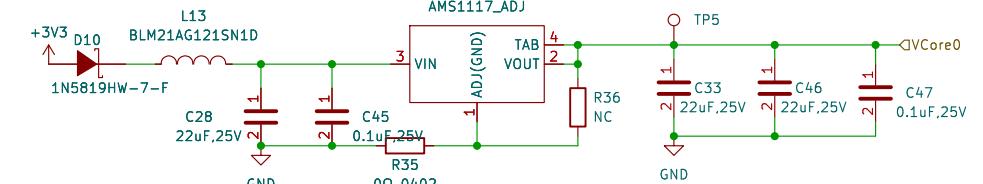
LDO 5V-3.3V(ADC)



OPA VDD



LDO 5V-1.2V(VCore)



01-Power



Sheet File: Power

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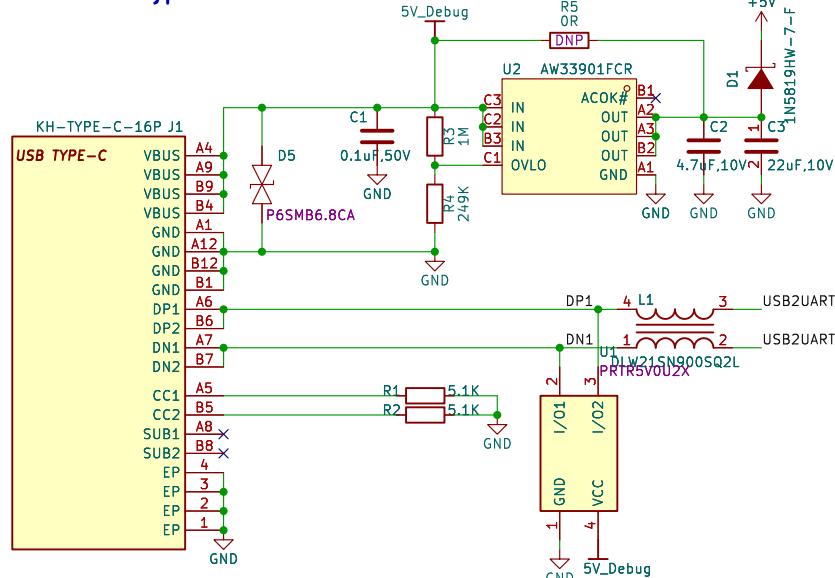
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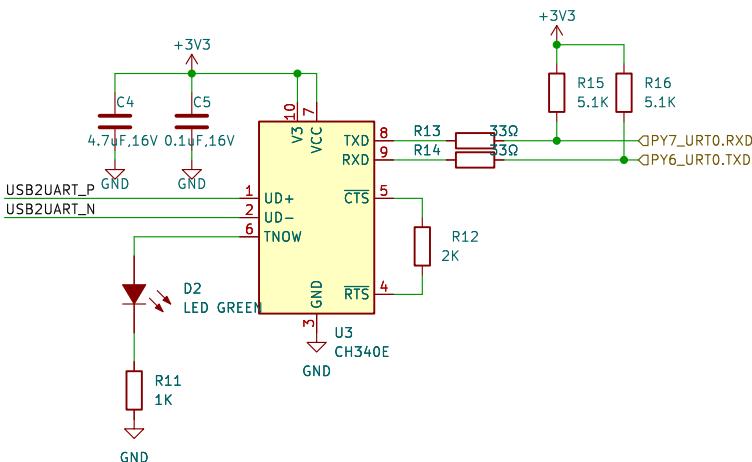
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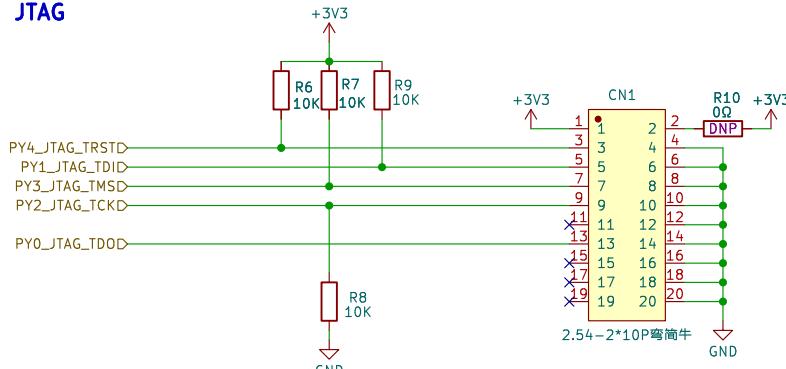
USB2UART Type-C



USB2UART



JTAG



02-Debug



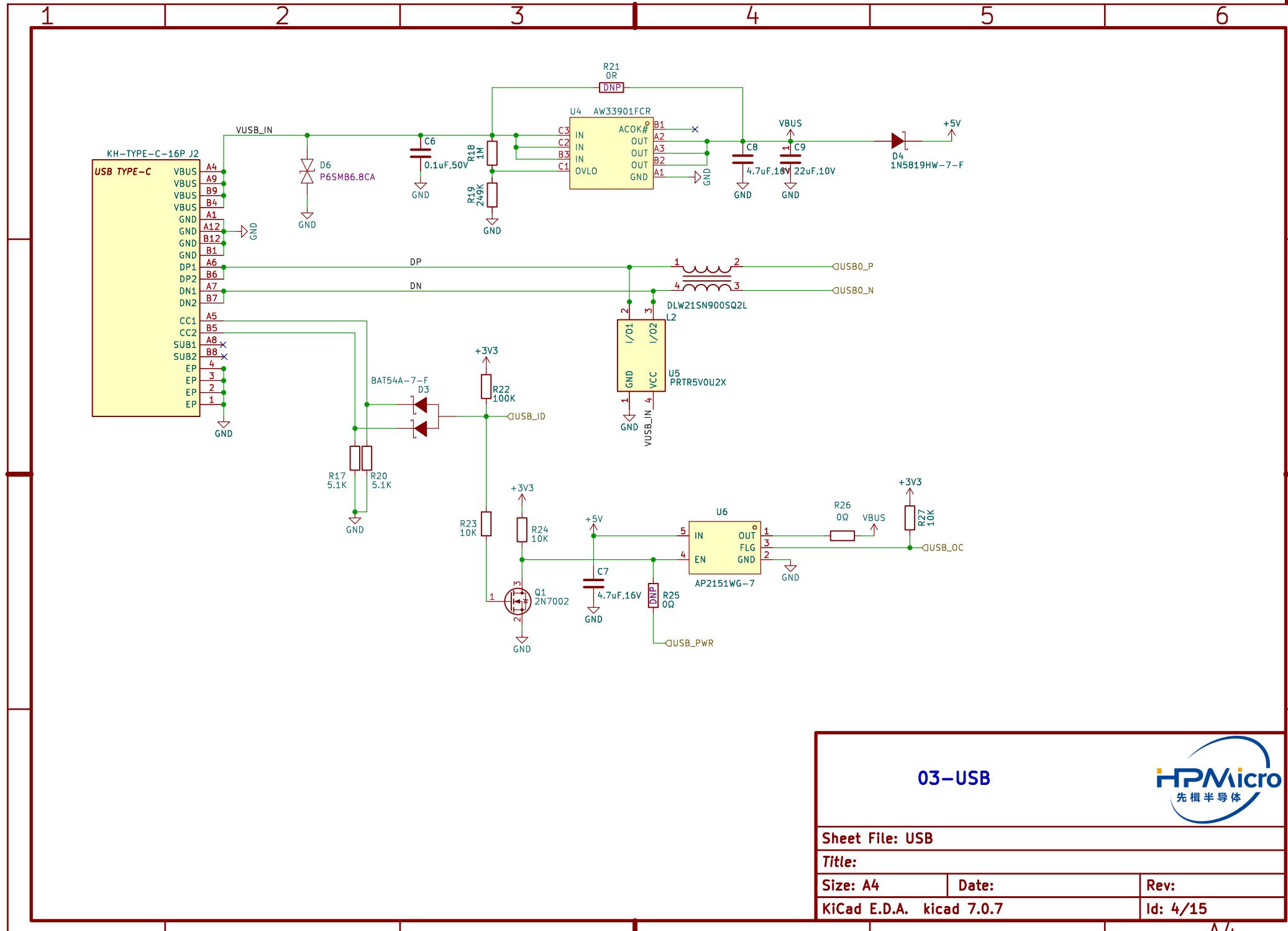
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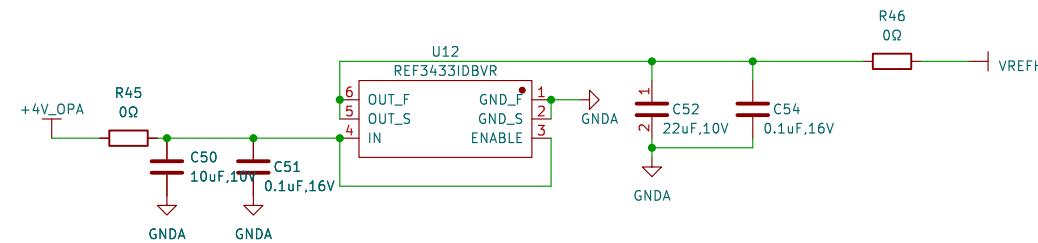
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04-VREF



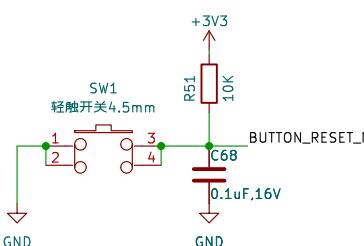
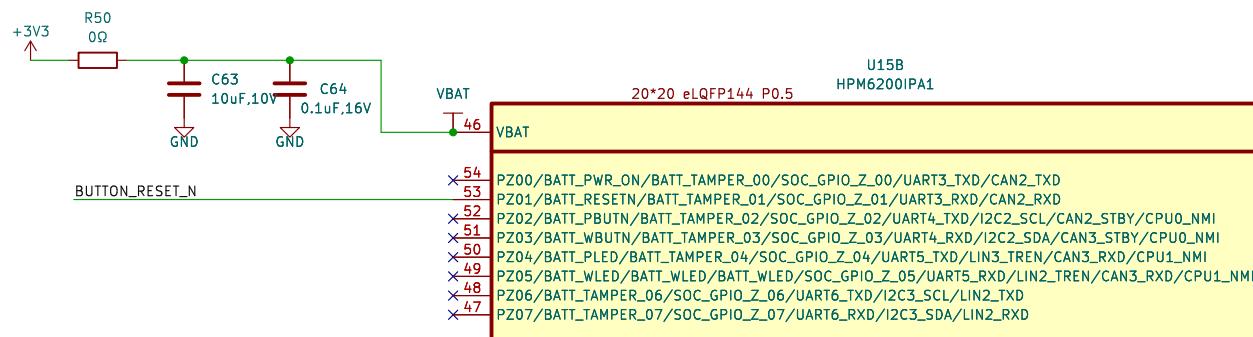
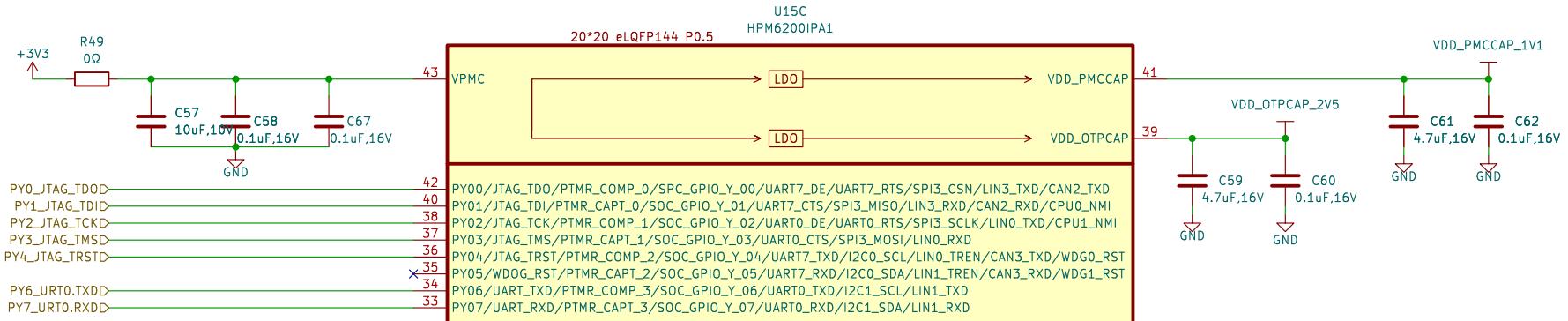
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05-HPM6200-VPMC-VBAT



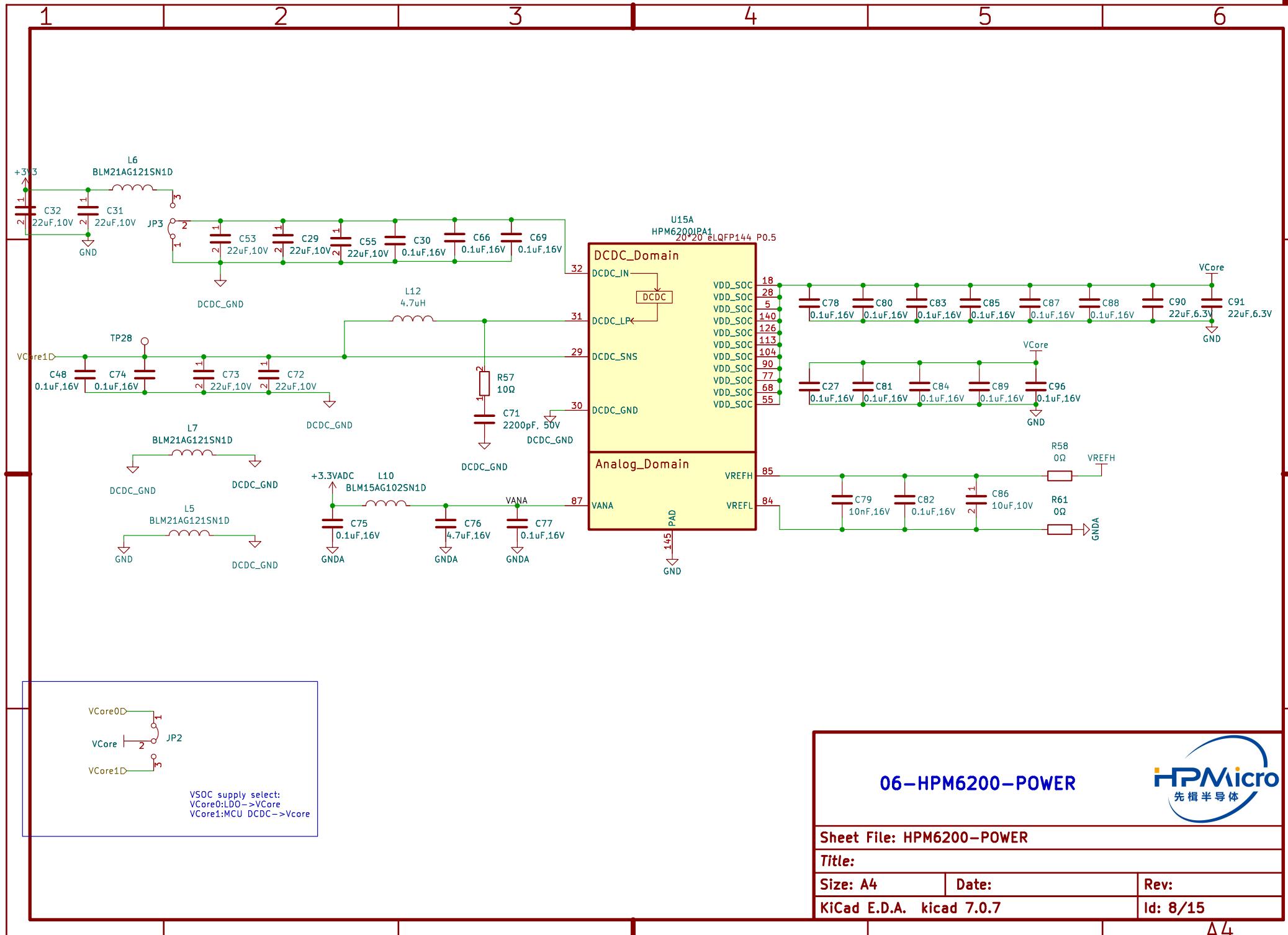
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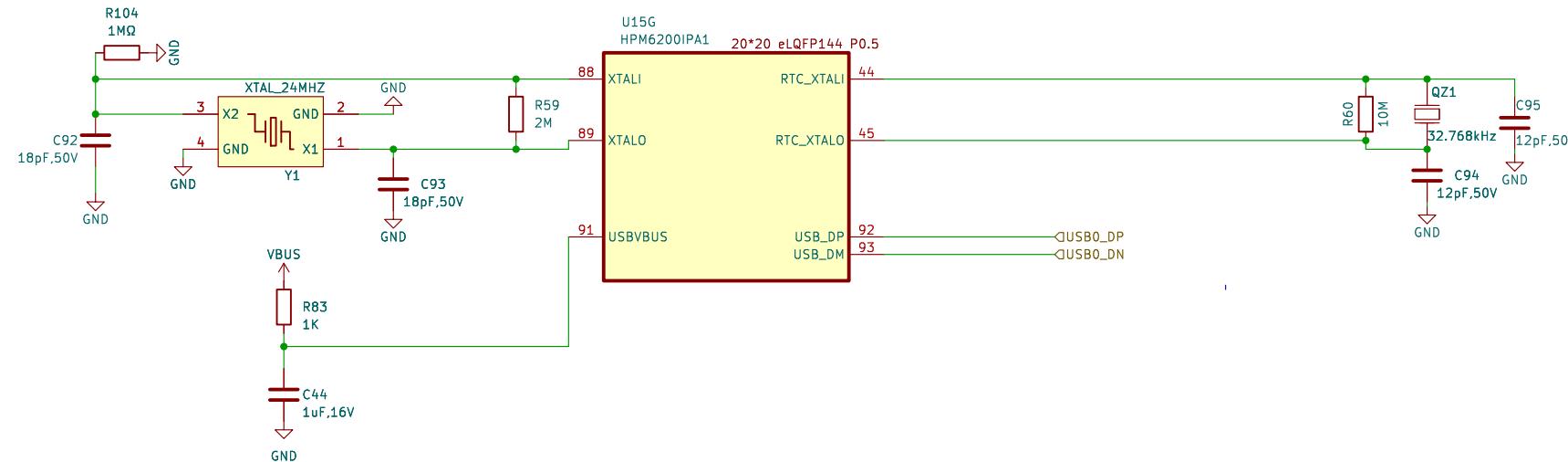
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07-HPM6200-OSC-USB

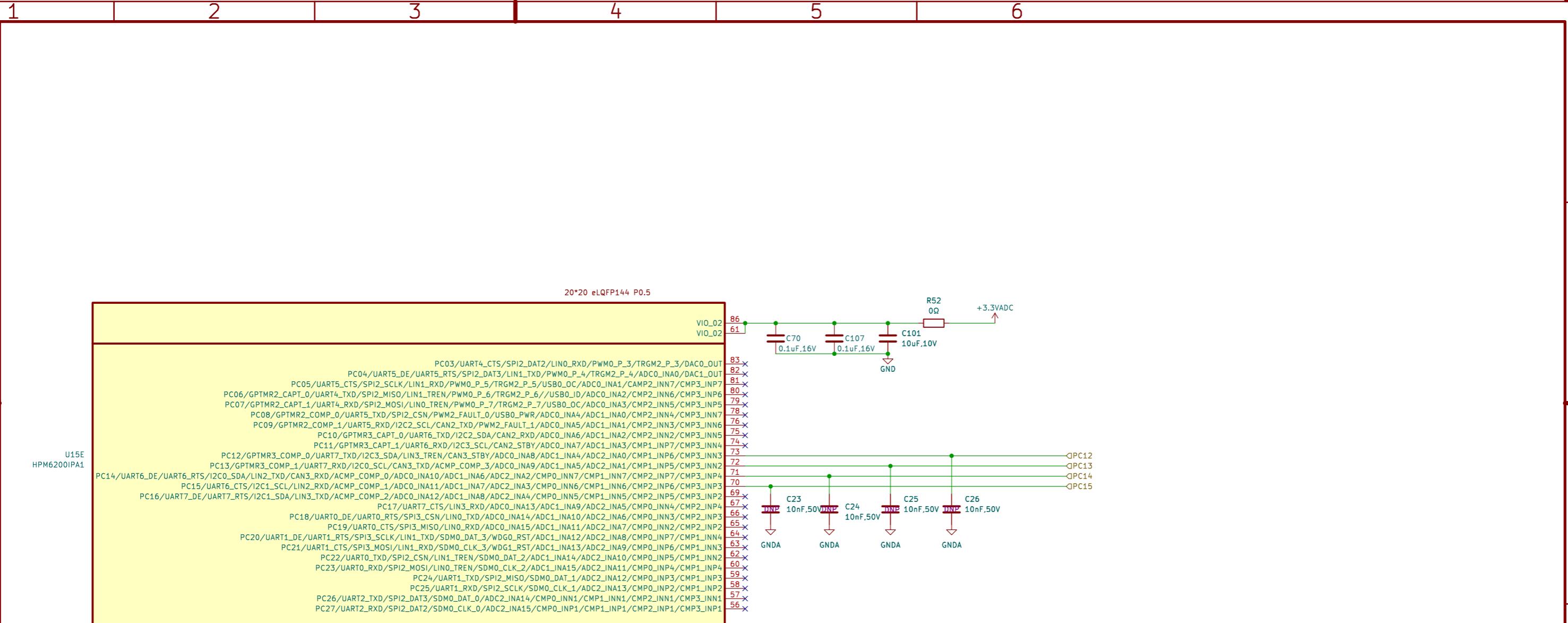


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08-HPM6200-I01



Sheet File: HPM6200-I01

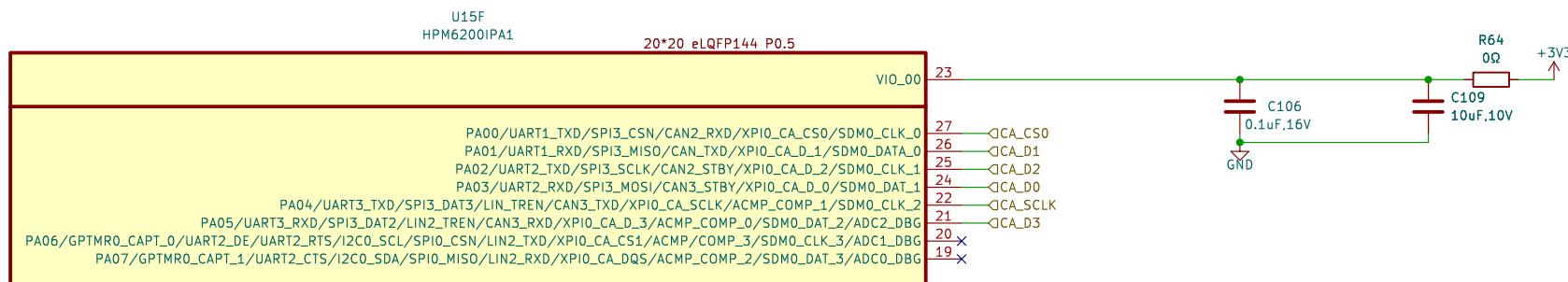
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9-HPM6200-I02



Sheet File: HPM6200-I02

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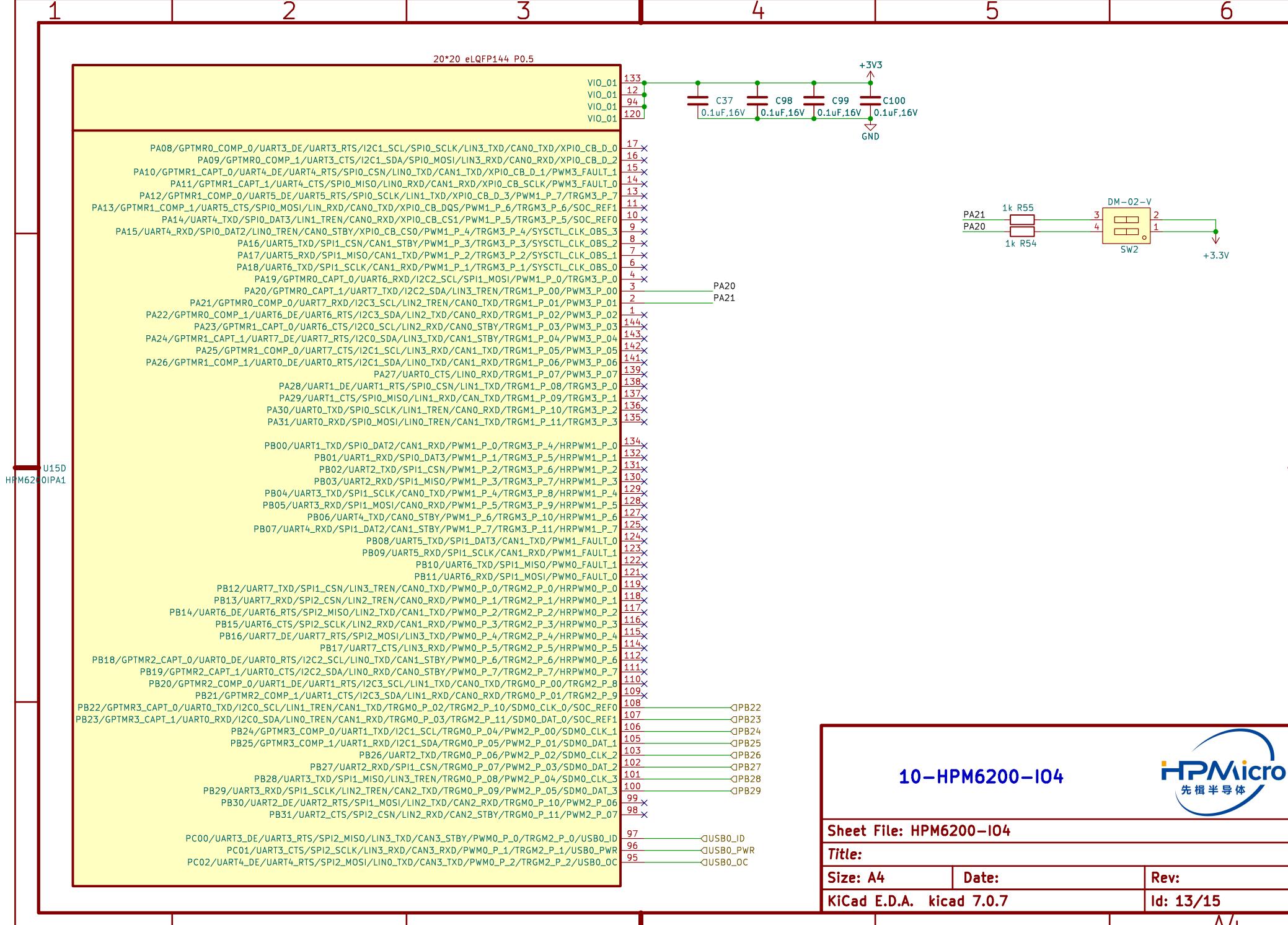
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10-HPM6200-I04



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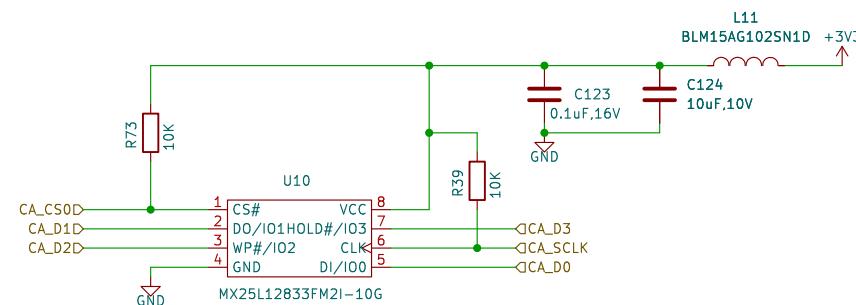
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**11-Norflash**

Sheet File: NorFlash

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Size: A4 Date: Rev:

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12-Assembly



Sheet File: Assembly

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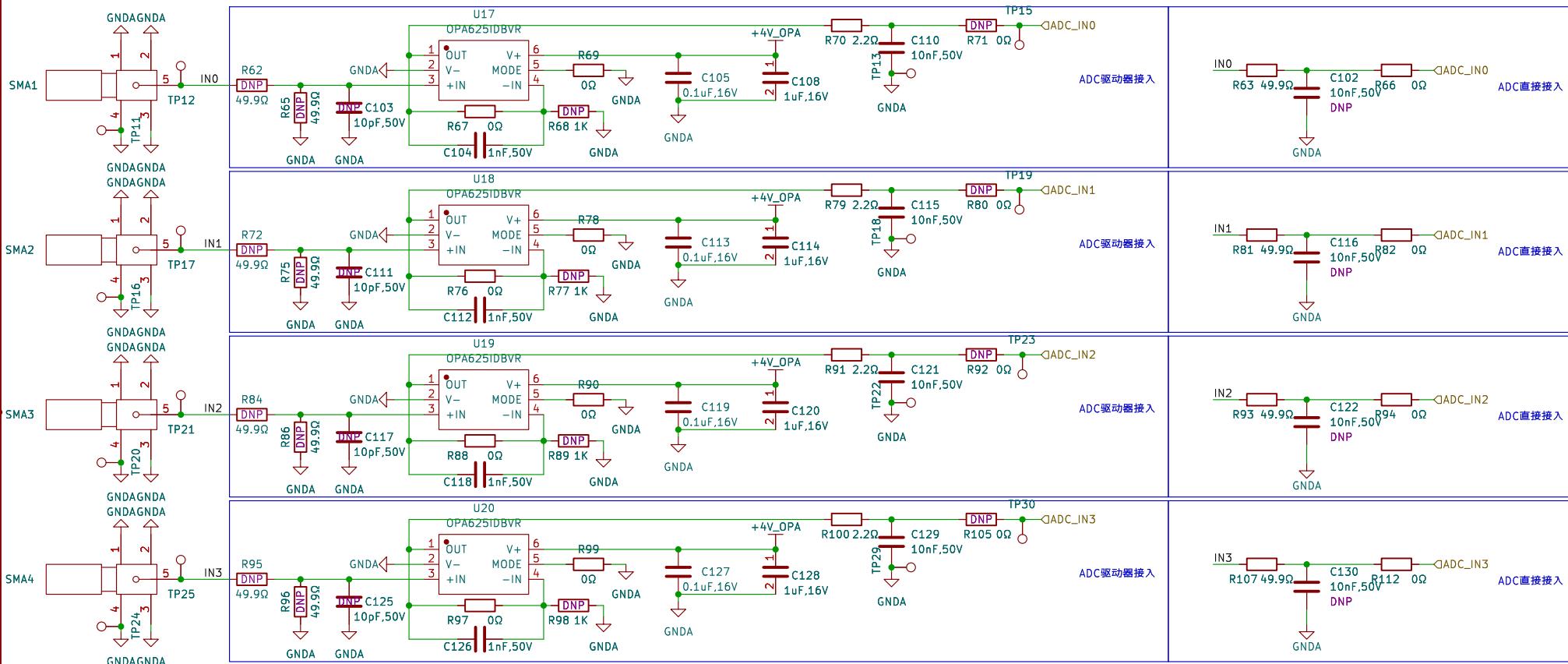
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13-ADC



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