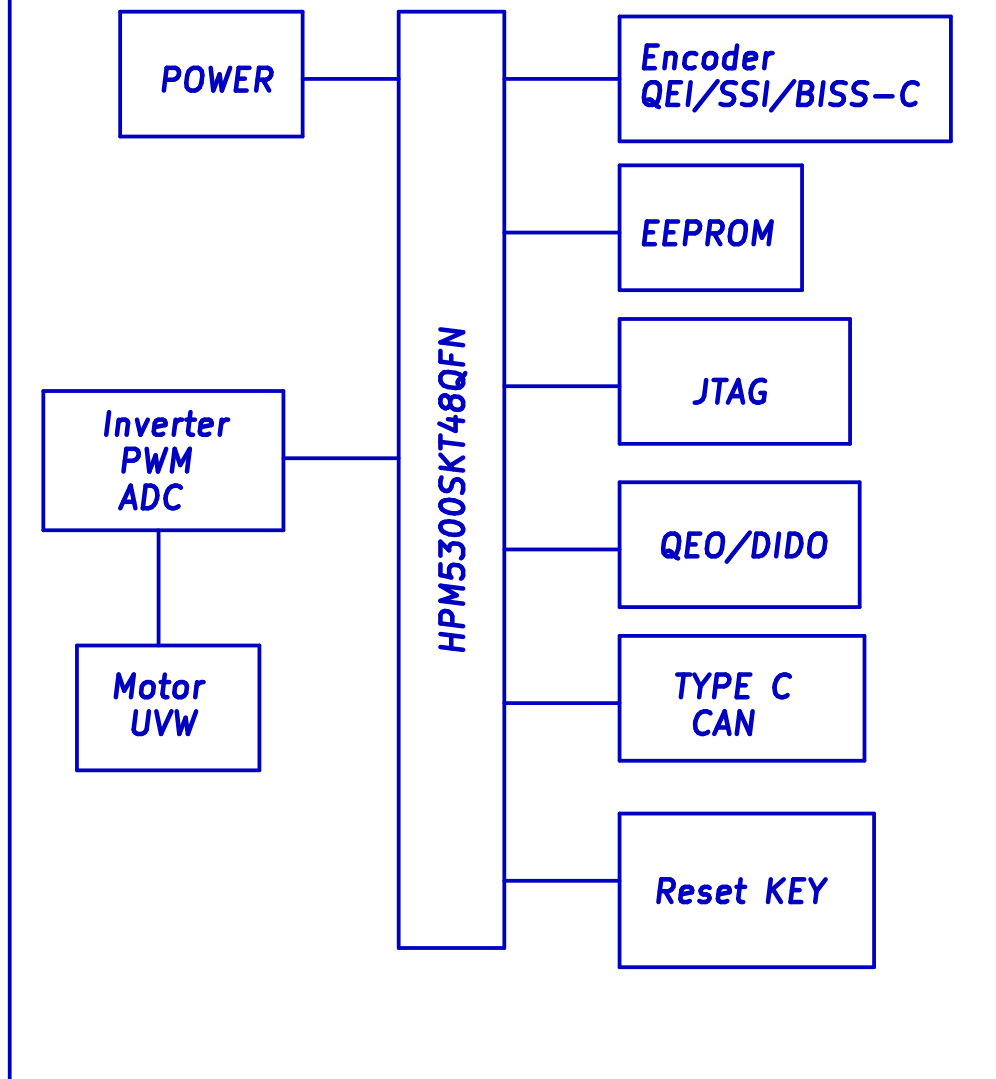


MAIN

System block diagram



修订历史

内容

时间

HPM5300_DCServo_CTL_REVB

20240301

System block diagram



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Date:

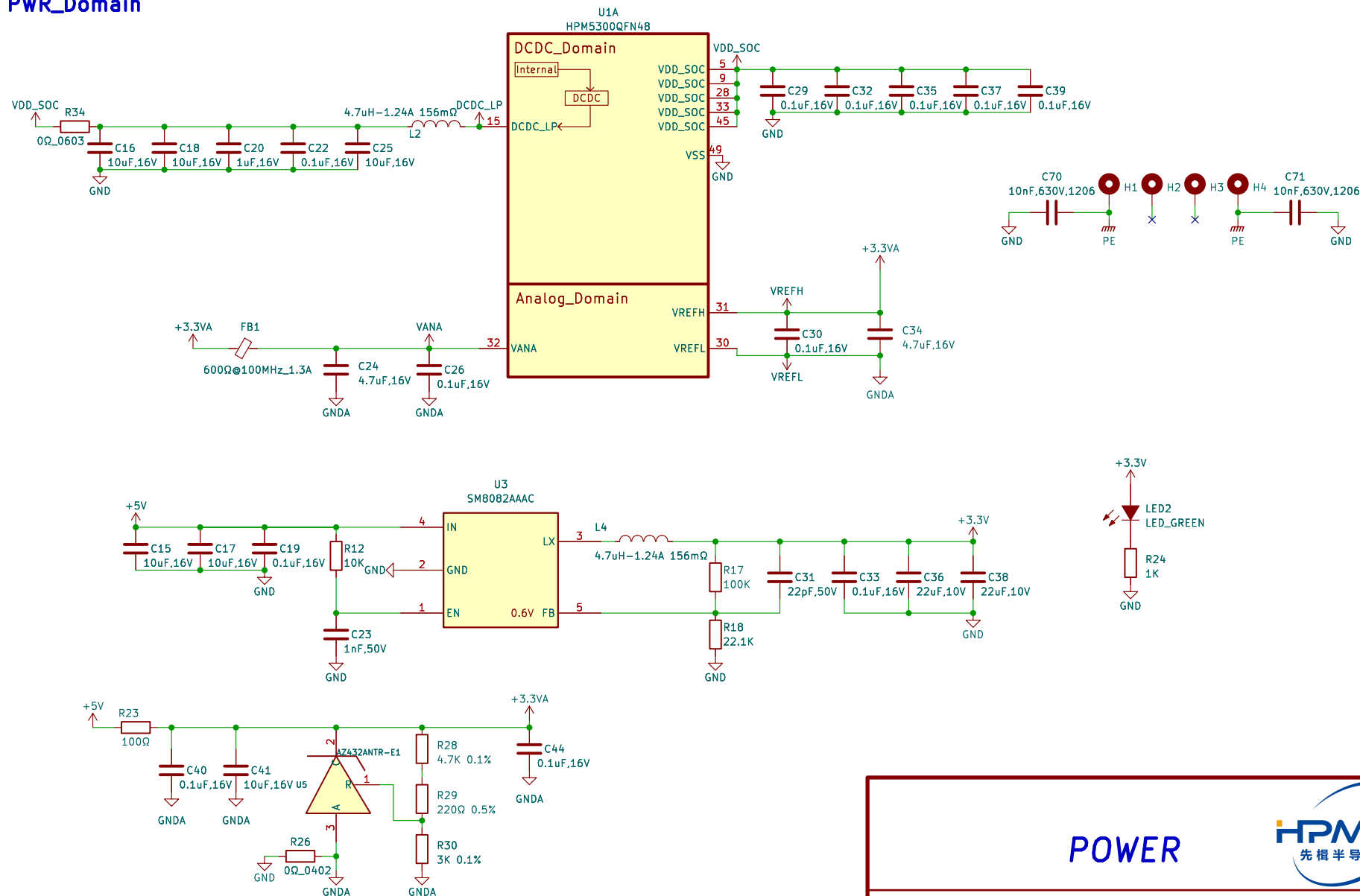
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KiCad E.D.A. kicad 7.0.2

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PWR_Domain



POWER



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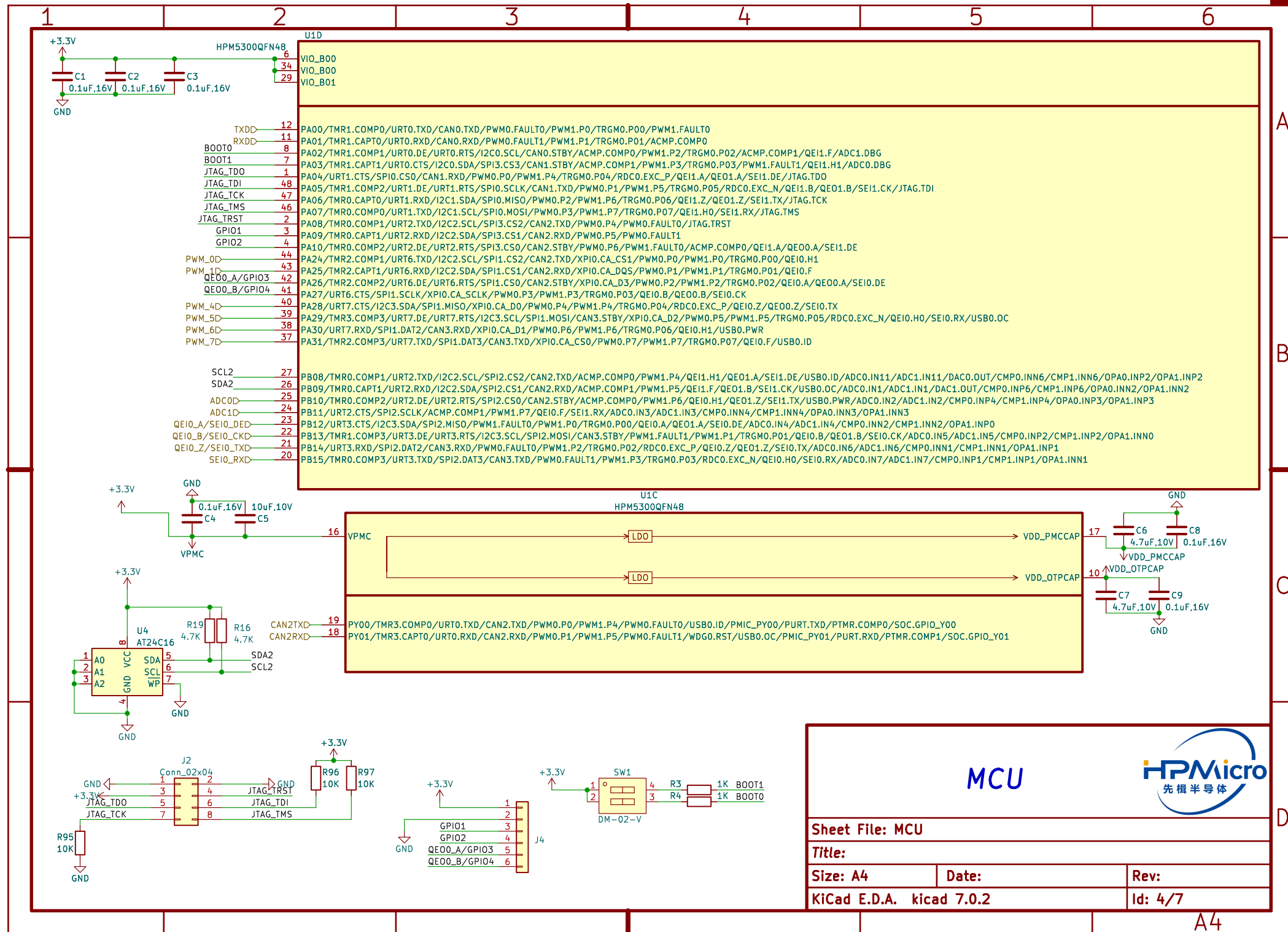
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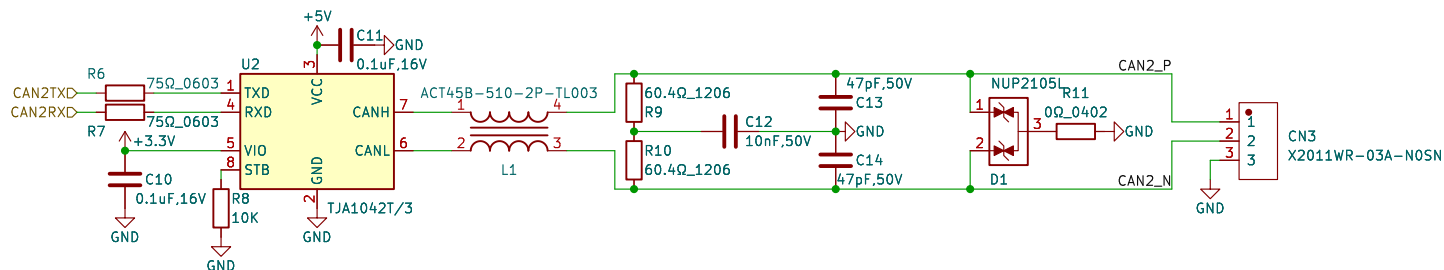
KiCad E.D.A. kiCad 7.0.2

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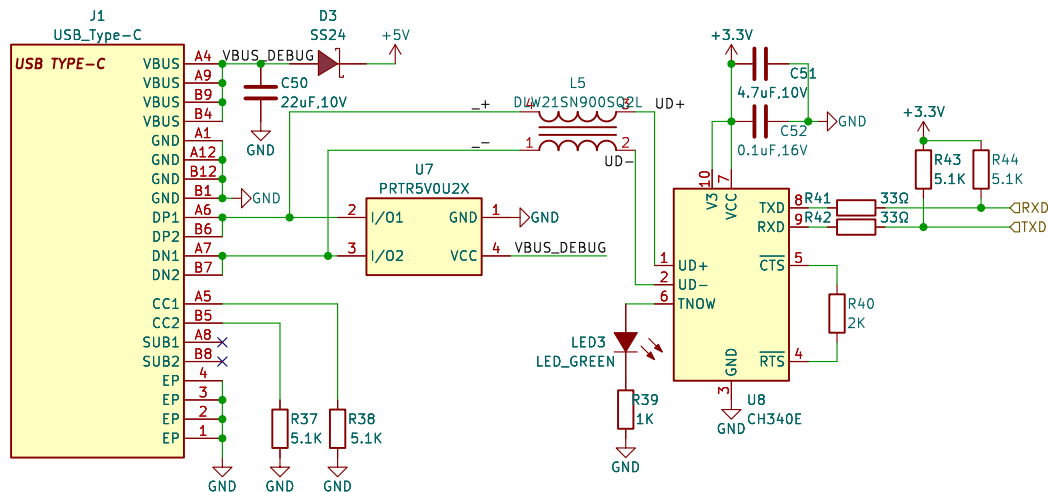
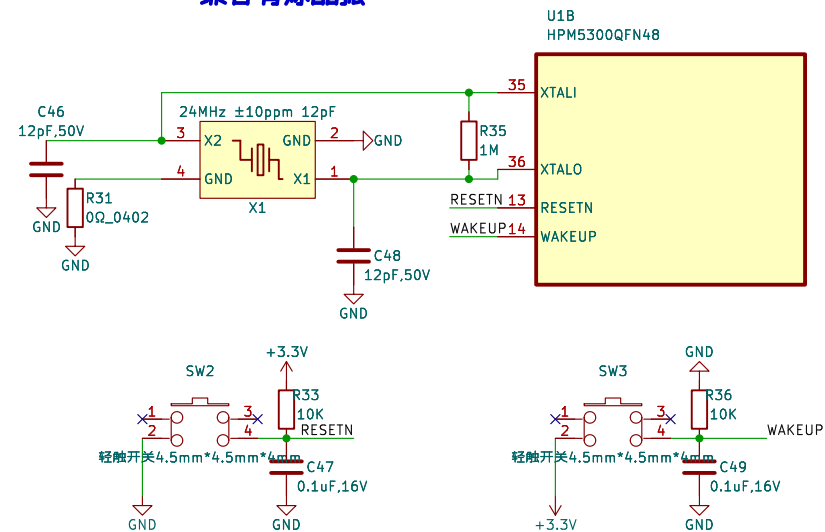
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CAN



USB_CH340

OSC_Domain 默认焊接无源晶振
兼容有源晶振

CAN/CH340



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Date:

Rev:

KiCad E.D.A. kicad 7.0.2

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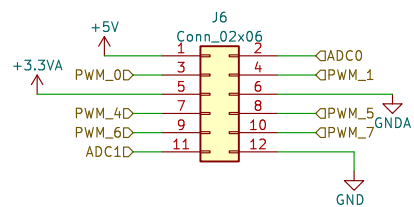
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C

D



DRV



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Id: 6/7

A4

