
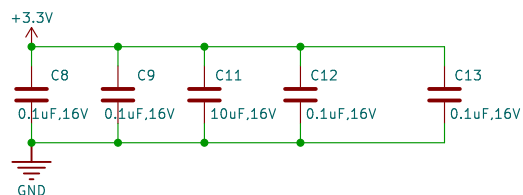
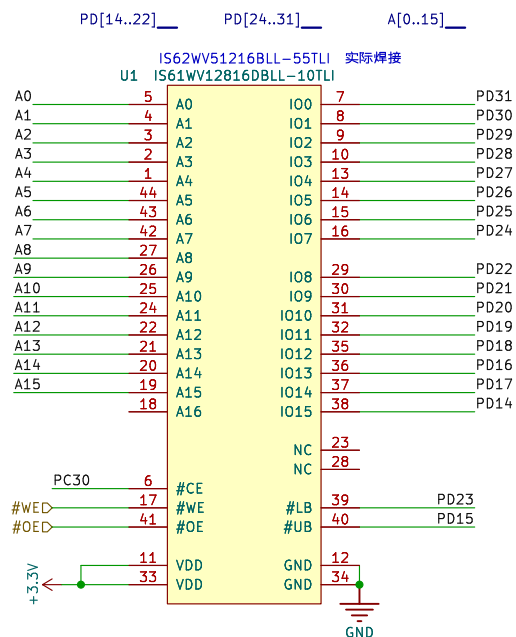


sdr am 高位数据, 地址不变
sram 低位数据
sram 地址使用锁存器

01_Main

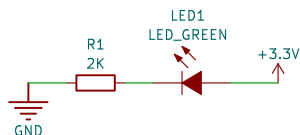
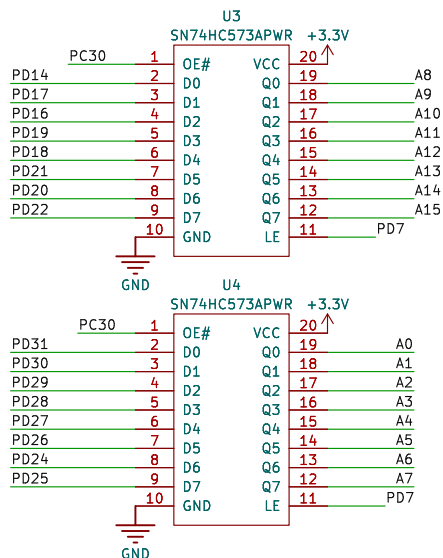


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Size: A4	Date: 2024/10/22	Rev:A
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PC[0..31]

PD[0..31]



PC00	VIO_B05	GPIO	ESC0.B.GPI[63]	DRAM.AB.DQ[24]	PPIO.A.DQ[24]
PC01	VIO_B05	GPIO	ESC0.B.GPI[62]	DRAM.AB.DQ[25]	PPIO.A.DQ[25]
PC02	VIO_B05	GPIO	ESC0.B.GPI[61]	DRAM.AB.DQ[22]	PPIO.A.DQ[22]
PC03	VIO_B05	GPIO	ESC0.B.GPI[60]	DRAM.AB.DQ[26]	PPIO.A.DQ[26]
PC04	VIO_B05	GPIO	ESC0.B.GPI[59]	DRAM.AB.DQ[27]	PPIO.A.DQ[27]
PC05	VIO_B05	GPIO	ESC0.B.GPI[58]	DRAM.AB.DQ[28]	PPIO.A.DQ[28]
PC06	VIO_B05	GPIO	ESC0.B.GPI[57]	DRAM.AB.DQ[29]	PPIO.A.DQ[29]
PC07	VIO_B05	GPIO	ESC0.B.GPI[56]	DRAM.AB.DQ[30]	PPIO.A.DQ[30]
PC08	VIO_B05	GPIO	ESC0.B.GPI[55]	DRAM.AB.DQ[31]	PPIO.A.DQ[31]
PC09	VIO_B05	GPIO	ESC0.B.GPI[54]	DRAM.AB.DQ[23]	PPIO.A.DQ[23]
PC10	VIO_B05	GPIO	ESC0.B.GPI[53]	DRAM.AB.DQ[20]	PPIO.A.DQ[20]
PC11	VIO_B05	GPIO	ESC0.B.GPI[52]	DRAM.AB.DQ[21]	PPIO.A.DQ[21]
PC12	VIO_B05	GPIO	ESC0.B.GPI[51]	DRAM.AB.DQ[2]	PPIO.A.DM[2]
PC13	VIO_B05	GPIO	ESC0.B.GPI[50]	DRAM.AB.DQ[18]	PPIO.A.DQ[18]
PC14	VIO_B05	GPIO	ESC0.B.GPI[49]	DRAM.AB.DQ[19]	PPIO.A.DQ[19]
PC15	VIO_B05	GPIO	ESC0.B.GPI[48]	DRAM.AB.DQ[3]	PPIO.A.DM[3]
PC16	VIO_B05	GPIO	ESC0.B.GPI[47]	DRAM.AB.DQ[16]	PPIO.A.DQ[16]
PC17	VIO_B05	GPIO	ESC0.B.GPI[46]	DRAM.AB.DQ[17]	PPIO.A.DQ[17]

PD14	VIO_B07	GPIO	ESC0.B.GPI[17]	DRAM.AB.DQ[08]	PPIO.A.DQ[08]
PD15	VIO_B07	GPIO	ESC0.B.GPI[16]	DRAM.AB.DM[1]	PPIO.A.DM[1]
PD16	VIO_B07	GPIO	ESC0.B.GPI[15]	DRAM.AB.DQ[10]	PPIO.A.DQ[10]
PD17	VIO_B07	GPIO	ESC0.B.GPI[14]	DRAM.AB.DQ[09]	PPIO.A.DQ[09]
PD18	VIO_B07	GPIO	ESC0.B.GPI[13]	DRAM.AB.DQ[12]	PPIO.A.DQ[12]
PD19	VIO_B07	GPIO	ESC0.B.GPI[12]	DRAM.AB.DQ[11]	PPIO.A.DQ[11]
PD20	VIO_B07	GPIO	ESC0.B.GPI[11]	DRAM.AB.DQ[14]	PPIO.A.DQ[14]
PD21	VIO_B07	GPIO	ESC0.B.GPI[10]	DRAM.AB.DQ[13]	PPIO.A.DQ[13]
PD22	VIO_B07	GPIO	ESC0.B.GPI[09]	DRAM.AB.DQ[15]	PPIO.A.DQ[15]
PD23	VIO_B07	GPIO	ESC0.B.GPI[08]	DRAM.AB.DM[0]	PPIO.A.DM[0]
PD24	VIO_B07	GPIO	ESC0.B.GPI[07]	DRAM.AB.DQ[06]	PPIO.A.DQ[06]
PD25	VIO_B07	GPIO	ESC0.B.GPI[06]	DRAM.AB.DQ[07]	PPIO.A.DQ[07]
PD26	VIO_B07	GPIO	ESC0.B.GPI[05]	DRAM.AB.DQ[05]	PPIO.A.DQ[05]
PD27	VIO_B07	GPIO	ESC0.B.GPI[04]	DRAM.AB.DQ[04]	PPIO.A.DQ[04]
PD28	VIO_B07	GPIO	ESC0.B.GPI[03]	DRAM.AB.DQ[03]	PPIO.A.DQ[03]
PD29	VIO_B07	GPIO	ESC0.B.GPI[02]	DRAM.AB.DQ[02]	PPIO.A.DQ[02]
PD30	VIO_B07	GPIO	ESC0.B.GPI[01]	DRAM.AB.DQ[01]	PPIO.A.DQ[01]
PD31	VIO_B07	GPIO	ESC0.B.GPI[00]	DRAM.AB.DQ[00]	PPIO.A.DQ[00]

PD07	VIO_B06	GPIO	ESC0.B.GPI[24]	DRAM.AB.SCLK[1]	PPIO.AB.CTR[3]
PC22	VIO_B06	GPIO	ESC0.B.GPI[41]	DRAM.AB.SRDY	PPIO.AB.CTR[2]

PE00	VIO_B08	GPIO	TSW0.A.P1_MDC	PPIO.AB.CTR[4]	
PE01	VIO_B08	GPIO	TSW0.A.P1_MDIO	PPIO.AB.CTR[5]	

02_SRAM



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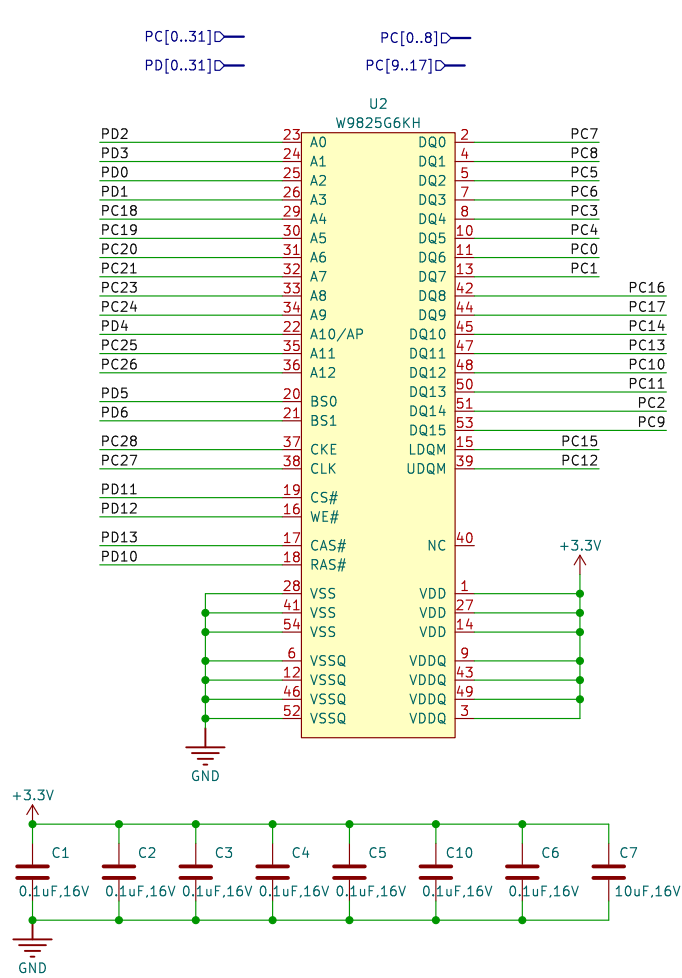
Date: 2024/10/22

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PC02	VIO_B05	GPIO	ESC0.B.GPI[61]	DRAM.AB.DQ[22]	PPI0.A.DQ[22]
PC03	VIO_B05	GPIO	ESC0.B.GPI[60]	DRAM.AB.DQ[26]	PPI0.A.DQ[26]
PC04	VIO_B05	GPIO	ESC0.B.GPI[59]	DRAM.AB.DQ[27]	PPI0.A.DQ[27]
PC05	VIO_B05	GPIO	ESC0.B.GPI[58]	DRAM.AB.DQ[28]	PPI0.A.DQ[28]
PC06	VIO_B05	GPIO	ESC0.B.GPI[57]	DRAM.AB.DQ[29]	PPI0.A.DQ[29]
PC07	VIO_B05	GPIO	ESC0.B.GPI[56]	DRAM.AB.DQ[30]	PPI0.A.DQ[30]
PC08	VIO_B05	GPIO	ESC0.B.GPI[55]	DRAM.AB.DQ[31]	PPI0.A.DQ[31]
PC09	VIO_B05	GPIO	ESC0.B.GPI[54]	DRAM.AB.DQ[23]	PPI0.A.DQ[23]
PC10	VIO_B05	GPIO	ESC0.B.GPI[53]	DRAM.AB.DQ[20]	PPI0.A.DQ[20]
PC11	VIO_B05	GPIO	ESC0.B.GPI[52]	DRAM.AB.DQ[21]	PPI0.A.DQ[21]
PC12	VIO_B05	GPIO	ESC0.B.GPI[51]	DRAM.AB.DM[2]	PPI0.A.DM[2]
PC13	VIO_B05	GPIO	ESC0.B.GPI[50]	DRAM.AB.DQ[18]	PPI0.A.DQ[18]
PC14	VIO_B05	GPIO	ESC0.B.GPI[49]	DRAM.AB.DQ[19]	PPI0.A.DQ[19]
PC15	VIO_B05	GPIO	ESC0.B.GPI[48]	DRAM.AB.DM[3]	PPI0.A.DM[3]
PC16	VIO_B05	GPIO	ESC0.B.GPI[47]	DRAM.AB.DQ[16]	PPI0.A.DQ[16]
PC17	VIO_B05	GPIO	ESC0.B.GPI[46]	DRAM.AB.DQ[17]	PPI0.A.DQ[17]

PC18	VIO_B06	GPIO	ESC0.B.GPI[45]	DRAM.AB.A[04]	PPI0.B.DQ[04]
PC19	VIO_B06	GPIO	ESC0.B.GPI[44]	DRAM.AB.A[05]	PPI0.B.DQ[05]
PC20	VIO_B06	GPIO	ESC0.B.GPI[43]	DRAM.AB.A[06]	PPI0.B.DQ[06]
PC21	VIO_B06	GPIO	ESC0.B.GPI[42]	DRAM.AB.A[07]	PPI0.B.DQ[07]
PC22	VIO_B06	GPIO	ESC0.B.GPI[41]	DRAM.AB.SRDY	PPI0.AB.CTR[2]
PC23	VIO_B06	GPIO	ESC0.B.GPI[40]	DRAM.AB.A[08]	PPI0.B.DQ[08]
PC24	VIO_B06	GPIO	ESC0.B.GPI[39]	DRAM.AB.A[09]	PPI0.B.DQ[09]
PC25	VIO_B06	GPIO	ESC0.B.GPI[38]	DRAM.AB.A[11]	PPI0.B.DQ[11]
PC26	VIO_B06	GPIO	ESC0.B.GPI[37]	DRAM.AB.A[12]	PPI0.B.DQ[12]
PC27	VIO_B06	GPIO	ESC0.B.GPI[36]	DRAM.AB.CLK[0]	PPI0.AB.CTR[0]
PC28	VIO_B06	GPIO	ESC0.B.GPI[35]	DRAM.AB.CKE	PPI0.B.DM[0]
PC29	VIO_B06	GPIO	ESC0.B.GPI[34]	DRAM.AB.SCLK[0]	PPI0.AB.CLK
PC30	VIO_B06	GPIO	ESC0.B.GPI[33]	DRAM.AB.SCS[0]	PPI0.AB.CS[0]
PC31	VIO_B06	GPIO	ESC0.B.GPI[32]	DRAM.AB.SCS[1]	PPI0.AB.CS[1]
PD00	VIO_B06	GPIO	ESC0.B.GPI[31]	DRAM.AB.A[02]	PPI0.B.DQ[02]
PD01	VIO_B06	GPIO	ESC0.B.GPI[30]	DRAM.AB.A[03]	PPI0.B.DQ[03]
PD02	VIO_B06	GPIO	ESC0.B.GPI[29]	DRAM.AB.A[00]	PPI0.B.DQ[00]
PD03	VIO_B06	GPIO	ESC0.B.GPI[28]	DRAM.AB.A[01]	PPI0.B.DQ[01]
PD04	VIO_B06	GPIO	ESC0.B.GPI[27]	DRAM.AB.A[10]	PPI0.B.DQ[10]
PD05	VIO_B06	GPIO	ESC0.B.GPI[26]	DRAM.AB.BA0	PPI0.B.DQ[13]
PD06	VIO_B06	GPIO	ESC0.B.GPI[25]	DRAM.AB.BA1	PPI0.B.DQ[14]
PD07	VIO_B06	GPIO	ESC0.B.GPI[24]	DRAM.AB.SCLK[1]	PPI0.AB.CTR[3]
PD08	VIO_B06	GPIO	ESC0.B.GPI[23]	DRAM.AB.CS[1]	PPI0.AB.CS[3]
PD09	VIO_B06	GPIO	ESC0.B.GPI[22]	DRAM.A.DQS	
PD10	VIO_B06	GPIO	ESC0.B.GPI[21]	DRAM.AB.RAS	PPI0.AB.CTR[1]
PD11	VIO_B06	GPIO	ESC0.B.GPI[20]	DRAM.AB.CS[0]	PPI0.AB.CS[2]
PD12	VIO_B06	GPIO	ESC0.B.GPI[19]	DRAM.AB.WE	PPI0.B.DQ[15]
PD13	VIO_B06	GPIO	ESC0.B.GPI[18]	DRAM.AB.CAS	PPI0.B.DM[1]

03_SDRAM



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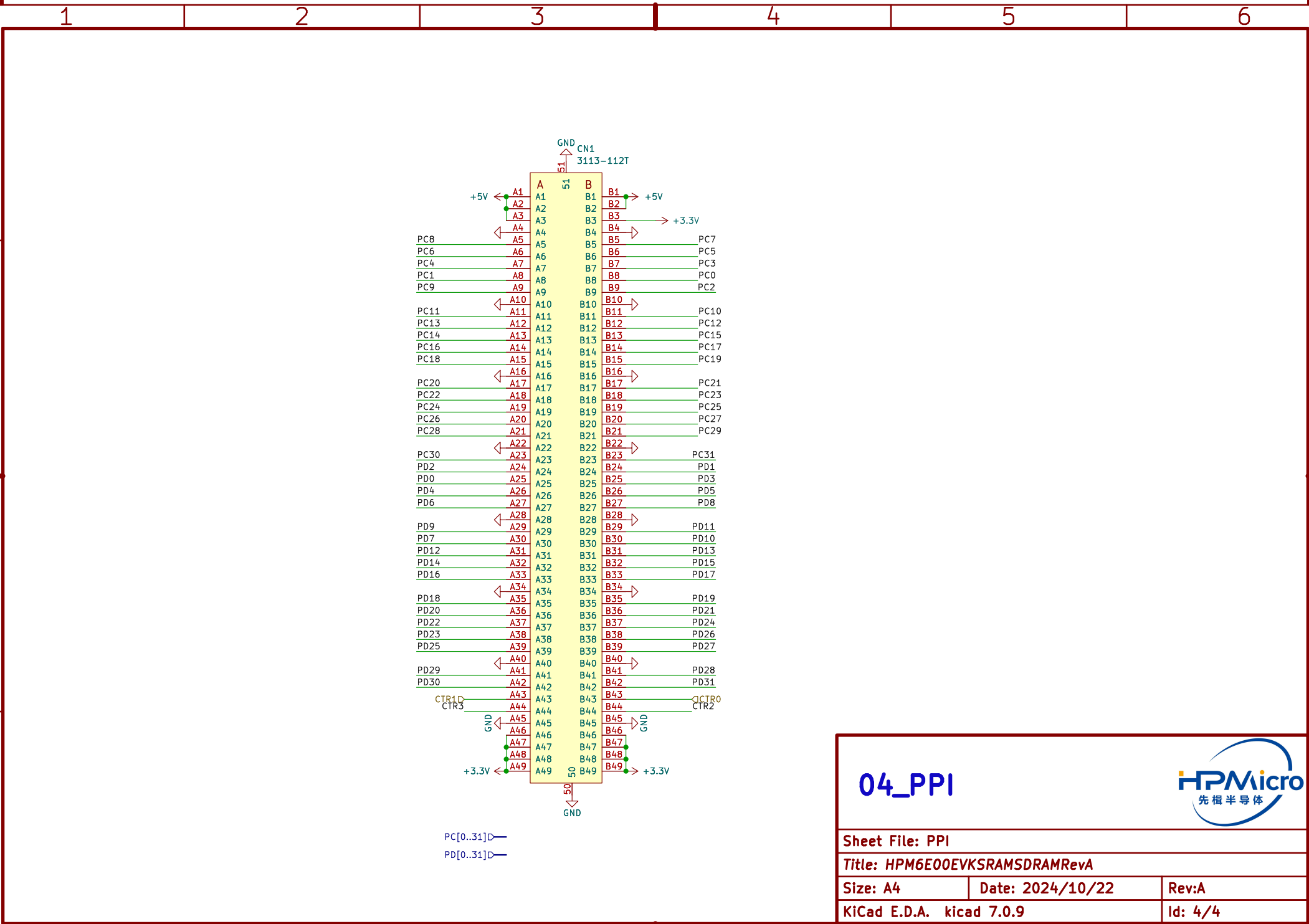
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04_PPI



Sheet File: PPI

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Size: A4

Date: 2024/10/22

Rev:A

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