nPM2100

Engineering B

Errata

v1.0

NORDIC® SEMICONDUCTOR

Contents

1 nPM2100 Engineering B Errata

This Errata document contains anomalies and configurations for the nPM2100 PMIC, Engineering B (CAAA-BA0, QEAA-BA0).

2 Revision history

See the following list for an overview of changes from previous versions of this document.

Version	Date	Change
nPM2100 Engineering B v1.0	28.03.2025	• Added: No. 1. “Ripple voltage is increased on VOUT”

3 New and inherited anomalies

The following anomalies are present in Engineering B of the nPM2100 PMIC.

ID	Module	Description	New in Engineering B
1	BOOST	Ripple voltage is increased on VOUT	X

Table 1: New and inherited anomalies

3.1 [1] BOOST: Ripple voltage is increased on VOUT

This anomaly applies to Engineering B, build codes CAAA-BA0, QEAA-BA0.

Symptoms

Ripple voltage is increased on BOOST VOUT in High Power mode.

Conditions

The voltage on the VBAT pin is less than 200 mV typical below the target VOUT voltage. BOOST is set either to Forced High Power mode at any load current or to Auto mode while the load current is high enough to set the BOOST into High Power mode.

Consequences

VOUT ripple voltage is increased to the following values:

- 20 mVp-p typical at VOUT=1.8 V
- 60 mVp-p typical at VOUT=3.0 V

VBAT ripple voltage might increase.

Workaround

Avoid using Forced High Power mode when VBAT voltage is close to the target VOUT voltage. If possible, program a higher VOUT voltage level.