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CMPE 100L

Lab Section 3

3 June 2016

Lab 7 Report

Description: In this lab, we were to make a game with the VGA port of the BASYS 2 board. The game we created is a slug (a blue rectangular bar) that moves left and right within the active region of a computer screen. It toggles off the walls of the borders and moves the other direction. The way to control the slug is using the BTN2 push button on the BASYS 2 board. When the game is in its Idle state, you will see the blue slug moving left and right, bouncing off the walls. We create a yellow border around the screen that is 8 pixels wide to show that the slug bounces a pixel off from the border, and to see the meteors fall into and off the screen. There are two meteors that we must create, a red colored one with a pattern and a green one. In Idle state, you will see the border, slug moving and a red meteor in the center of the screen. The meteor is shaped like a diamond. It is 128 pixels wide (64 pixels wide from the center to the corners of the diamond). Once we start the game, we must dodge the meteors. Every meteor dodges increments our score by one. When we dodge 10 meteors, we increase in level. The way you know you went up a level correctly is by seeing the game stop, the slug flash magenta, then flash blue and get bigger and the level score increments one. We pick how many levels by using the switches. When the level matches the switches, the slug and meteor flash saying you won the game (Victory Dance). You lose when you hit a meteor. When you lose, your slug flashes as the game is stopped. The way to return to Idle from winning or losing is by pressing BTN1 to restart the

game, or by pressing BTN3 which is a hard reset and resets the entire game, then goes to Idle, which takes more time. From this information, you can play the game correctly.

Methods: The way I went on with this lab was by first making my yellow border. I set the bounds of the active region and HS and VS according to the lab manual. After that, I tried to create my slug and have it bounce which was really hard. After two days of working on that, and from getting help from some friends, I figured that I must use a toggle flip flop and edge detect my push button. I used a counter to move the slug left and right (1 frame per second), and to toggle the slug, I used the toggle flip flop and I set the bounds at which the slug is suppose to bounce at. After that, I started working on the meteors. In piazza, the professor uploaded this equation to make the diamond shape. The equation was: $| U - X | + | V - Y | < 64$. I remember her saying that it sometimes gets messed up if you subtract in verilog so I went threw the arithmetic symbols and found an adder and subtracter. The way I did this was use my horizontal counter (U) and subtracting that with a constant. Same with the vertical counter (V). Those outputs went into my sign changer which made their values positive. Then I added their values. To get the design, I used the COMPM16 comparator and set bounds with the less than symbol and made it start in the center bright and it gets lighter as it goes out. From this, I got my red and green diamond and their pattern, now I have to get them to fall and in random places. I made an LFSR (same as lab 5) and had that go into the register. I took off the constants from the subtractor symbols and plugged in the output of the register into (X) to get the $(U - X)$ part. I used a counter to have it fall down and that output was used as the (Y) in the $(V - Y)$ part. In order to keep the meteors in the screen, I had to create a verilog module that keeps it in bounds, and I had another verilog

module that makes the meteor fall at 2 frames per second. The last verilog module just tells the counter when the diamond when it falls off the screen so it can reset. The only difference between my red and green meteor is that my red meteor has a 2 to 1 mux connected to the counter. When the game is in Idle, the counter puts the position of the red meteor in the center of the screen, other than that, it falls from -64 (FFC0). Then I started my Finite State Machine. I had to make 3 of them to get the right one. I used LEDs to find what state and outputs were working. I made the time counter similar to older labs, same with the hex7seg, sign changer, full adder, LFSR, Ring Counter, Edge detector, and Synchronizer. The score counter was new to me because we used the CD counter but I got it down after a while. The thing I didn't know was what my CE would be but I found out it was the AND of my four second timer and when my FSM is at it's level up state. I made my collision logic with and gates so when they were both 1, the FSM would know there was a collision. I had a schematic called Color Logic which dealt with all the logic for the solid and flashing colors. All that stuff was outputted into my TOP TOP and ibuf/obufed their.

Results: My design had a lot of errors which delayed me from finishing. One thing I kept doing was misspelling things so when I thought something was connected, it wasn't because of the difference in spelling. This messed me up a lot because I thought it was connected so I thought my logic was wrong. That was really frustrating. Other than that, my design was nice. I connected all the things needed to where they go and compiled and it worked. The way I tested my FSM was with LEDs. I tested my symbols by just playing the game even though the score or anything wasn't connected. The only errors I got from testing was FSM errors. My LEDs weren't

lighting when I wanted them to so I tried to debug but it was hard to find the error, so I thought it was my state diagram so I redrew that. The second one had similar problems because the diagrams were really similar, so then I just remade it one more time and that one worked well. The major components in this lab in my opinion were the borders verilog, slug logic, and meteor logic. I feel like that was the meat of this assignment. They all interested though the color logic schematic. The VGA signals played the role in connecting all these components together. The pins I used in this lab were:

Input	Pins
PB0	G12
PB1	C11
PB2	M4
CA	L14
CB	H12
CC	N14
CD	N11
CE	P12
CF	L13
CG	M12
SW0	P11
SW1	L3
SW2	K3
SW3	B4
HS	J14
VS	K13
RED0	C14
RED1	D13
RED2	F13

GREEN0	F14
GREEN1	G13
GREEN2	G14
BLUE0	H13
BLUE1	J13
DP	N13
AN0	F12
AN1	J12
AN2	M13
AN3	K14
SW7	N3
PB3	A7
LED7	G1

Conclusion: From this lab, I learned how a lot about the VGA outputs and how to use logic to control pixels on a connected computer through BASYS 2 VGA port. The logic and verilog was basically the same, the only thing that was different was accounting for the 3 red, 3 green, and 2 blue colors, as well as the HSync and VSync. Other than that, I felt like everything was the same. I had a lot of difficulties in this lab. Some that I explained earlier like to get the slug to toggle, or to making the meteors fall within the borders randomly was truly a pain. Another error I had that was very strange was with my selector. I tried showing the TAs/tutors on Tuesday this week in lab but nobody knew how to solve this error. I had a 4 bit bus of an M4_1E mux. I fed it the inputs and everything and when I compiled, it would show no errors. When I tried to generate the bit file, it would pass synthesize and Implement design, but once it got to trying to generate the bit file, the error would pop up. I posted the error on piazza on Tuesday when the tutors told me

too cause they didn't know what was wrong. I also sent a message to the Professor because I was in lab the whole day and couldn't get help from anybody on how to fix it. The next day I met with the professor and we fixed the problem and it worked. From asking the professor, the error was that an input was grounded and that would mess the m4_1E mux up. She made me create the 4 to 1 mux by using three 2 to 1 muxes and then it worked. The error didn't explain well what was wrong and that is why I think me or the tutors couldn't find the error. That took me like 2 days to solve. Plus the toggle of the slug and making the meteors fall randomly in the borders was the problems I dealt with the longest. If I could do this lab over again, I would make sure to name stuff easier so I know what output is going to what input, and to make sure the names were correct. A problem I had was having the slug to go every level. I checked my logic and verilog and I was sure it was right. After some hours of looking and trying to find why it wasn't working, I found that the inputs name was misspelled from the output I needed. Once I fixed this, it worked so that was a pain in the butt. I now know to make sure all my names are correct and spelled correctly, and to sometimes fix warning because they may lead to future errors. I believe my components are optimized but I am not fully sure about that. I didn't ask students, TAs, tutors, or the teacher so I am not sure. In the end, this lab was great and I am happy I was able to create it, but it was extremely stressful and the most time consuming thing I have ever done. I am so happy it is over with and I don't have to stress about this anymore, but I am happy we did this lab because it is really cool knowing that we made a game like this.

Appendix:

BTNO - G12

BTN1 - C11

BTN2 - M4

10 VGA objects

CA - L14

CB - K12

CC - N14

CD - N11

CE - P12

CF - L13

CG - M12

SU0 - P11

SU1 - L3

SU2 - K3

SU3 - B4

L9b 7

*DP - N13

ANO - F12

ANI - J12

*AN2 - M13

AN3 - K14

Idle - Moves back and forth / Meteor Not in motion

BTNO - Start game

BTN2 - Change direction

Every meteor dodgeL is 1 point

Every 10 pointr AN3 goes up 1 (level), Slug gets

bigger by 16 pixels. Flasher Magenta then goes back
to blue.

Slug moves 1 pixel per frame

Meteor moves 2 pixels per frame.

When Meteor hits slug, metors freeze and slug flashes
until BTN1 is pressed, then back to Idle.

When win game, flash meteor and slug, until
BTN1 pressed, then back to Idle.

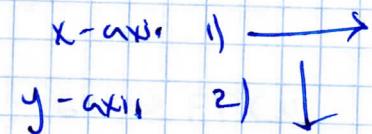
Switches correspond to levels, if 2 switches up,
2 levels then game is over.

Once half meter gone, other meter falls.

8 bit LFSR determines horizontal coordinates of meteor.

Increment when meter is all off from bottom.

(R₂, R₁, R₀, G₂, G₁, G₀, B₁, B₀)



HS → 0 to 655, 750 - 799

VS → 0 to 489, 490 - 524

Red-green = Yellow

Red-Blue = Magenta

799 = 0000001100001111 → H-high

524 = 0000001000001100 → V-high

479 = 000000011101111 → AR-V

16x lev AN2

639 = 000000100111111 → AR-H

Slug initial = 16 x 64

H-high = frame = 1 p/s

320 → 0140

15 - 9' ↓

L0 L(7) high

240 → F0

→ 16

772 → 0000000100010000

L(7) high L

Map

How to move?

~~Sign change
for meteor?~~

How to move slug and meteor? ✓

How to make meteor? ✓

How to bounce slug? ✓

64 frame ≈ 1 second

-64 → FF(0)

How to make meteor move? ✓

Alternate from Red to Green ✓

(17), high¹⁶

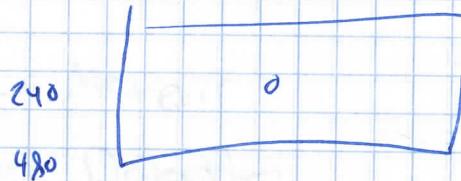
Fall, reset, change color, fall (meteor falls after last in half ms)

Center pixel (x, y)

$$|U-x| + |V-y| < 64$$

Counter +2

111 → 004 (16) (1)
 110 → (32) (11)
 101 → (48) (2)
 010 → (64) (3)



525

512 → (9)

Text

Branch

Frame = 2
if 1 or

640 - - - - - - - - - -

-64
608

Alternate Red & green ✓

1 2 3

Meteor movement ✓

3 2 1

Random Meteors

302

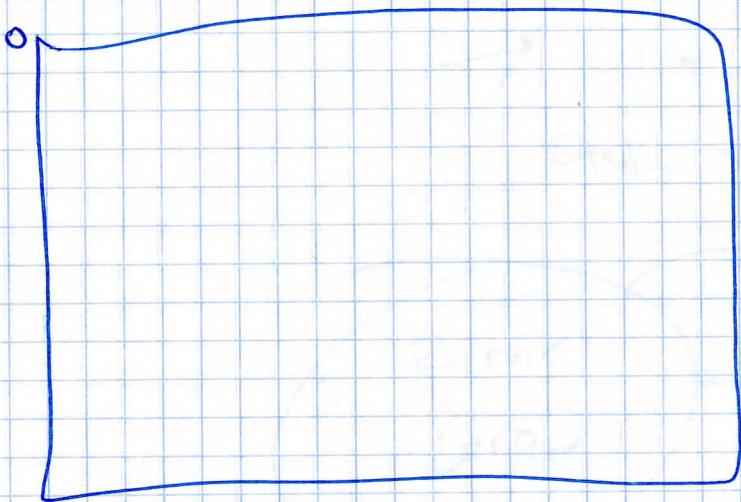
Collision detection

✓

-64

1 2 3
3 2 1 0

240



fix - cont

Random Stat

Random Meteor

Collision Detection

Green Met

Slug Flash

Mct + speel

Meteor Flash

Test Branch

Not growing.

1066

Toggle Red Green → Center

Stay in Bower ✓

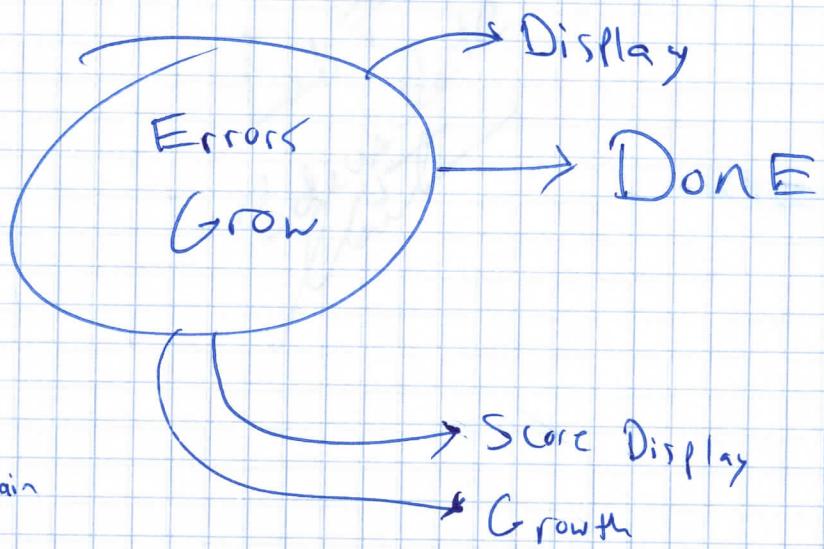
LFSR ✓

Error
→ Grow

640 → 320

480 ↓ 240

Static start ✓



Slug length → check again

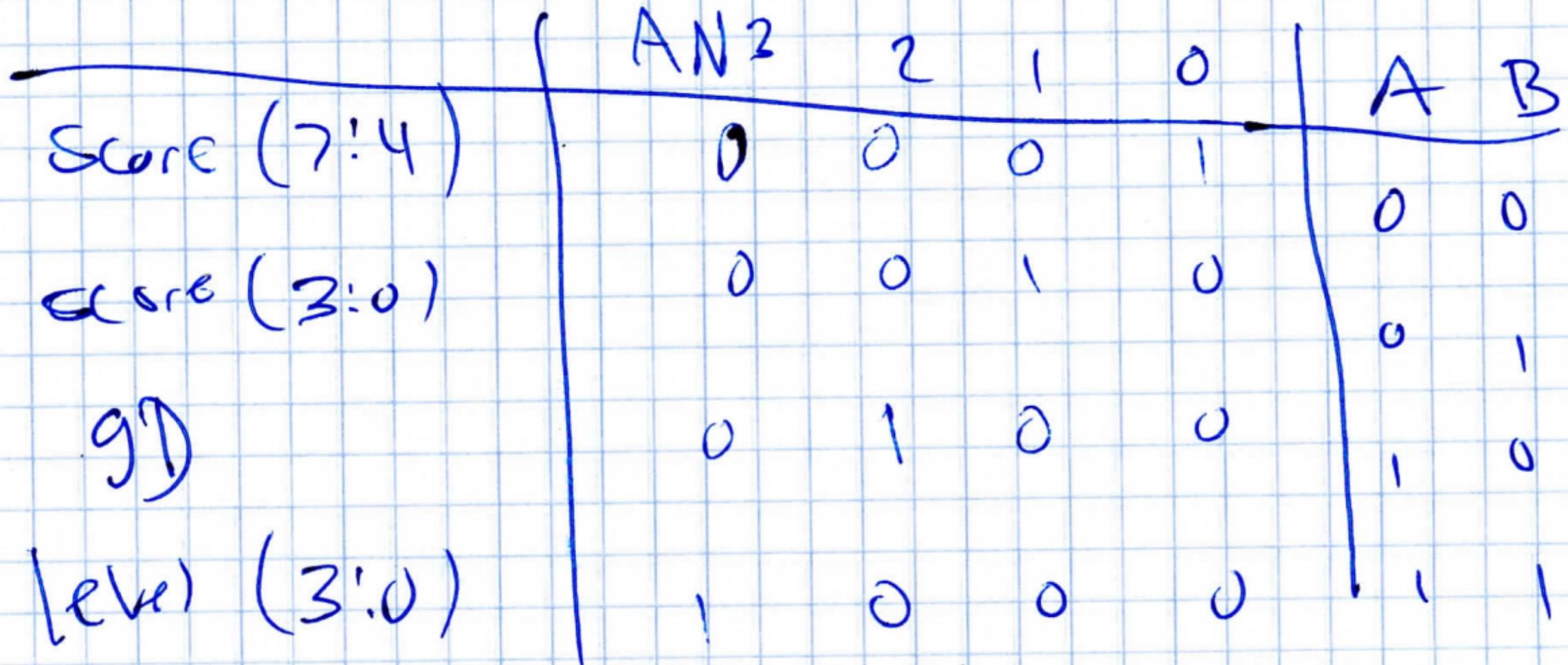
T_Count → check again

Selector

Score Display

flash

Error fixed



Time
Score

1 00

Inc

level starts off at 1
ever in Inc

PB0 - time

Lab 7 stuff

Inputs:

PBO (start game)

FourSecs

PB1 (return to idle after lose)

Collision + (meteor & slug)

SW Level (1 - F)

Diamond_Gone

Level_UP

Match (su & level)

Start

Restart

Foursec

Collision

Dir_In

Meteor_TEN, Match

Outputs:

Locat (Random Meteors)

Level_Inc (start at 1) (ever

Flash_Slug (every 10 magenta)

Increase_Slug (blue plus 16 pix)

Score_Inc

Win (Flash slug and Meteor (Vict

Lose (Flash slug)

Reset Time

Reset

Victory_Dance

Flash

RED_M

Magenta

Score_Fail

Slay_Stop

level starts at 1

every meteor gone off is Inc

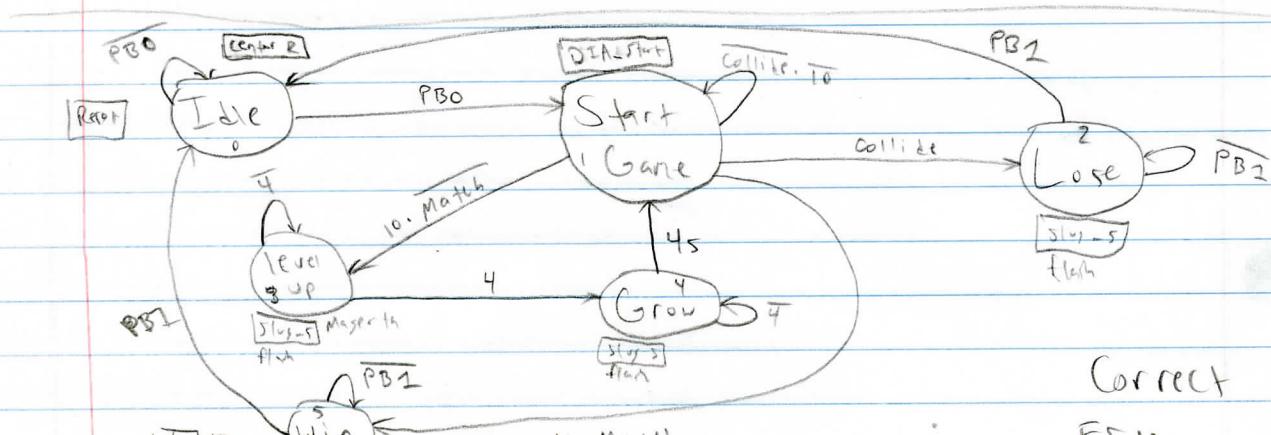
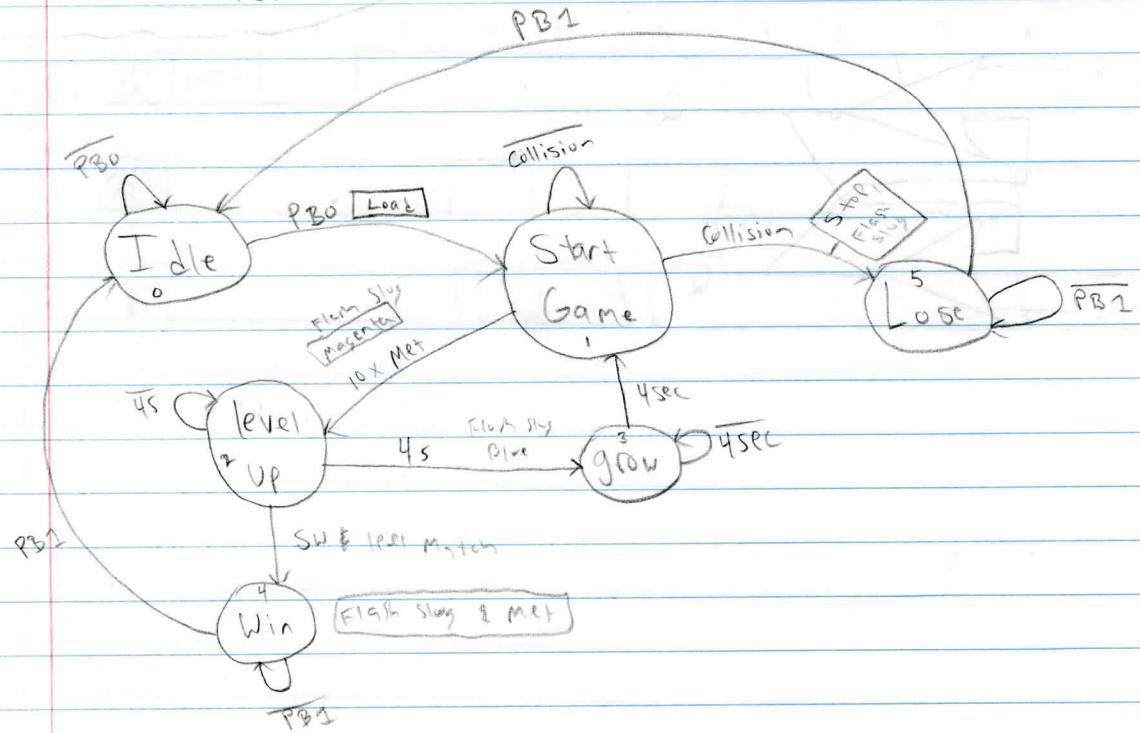
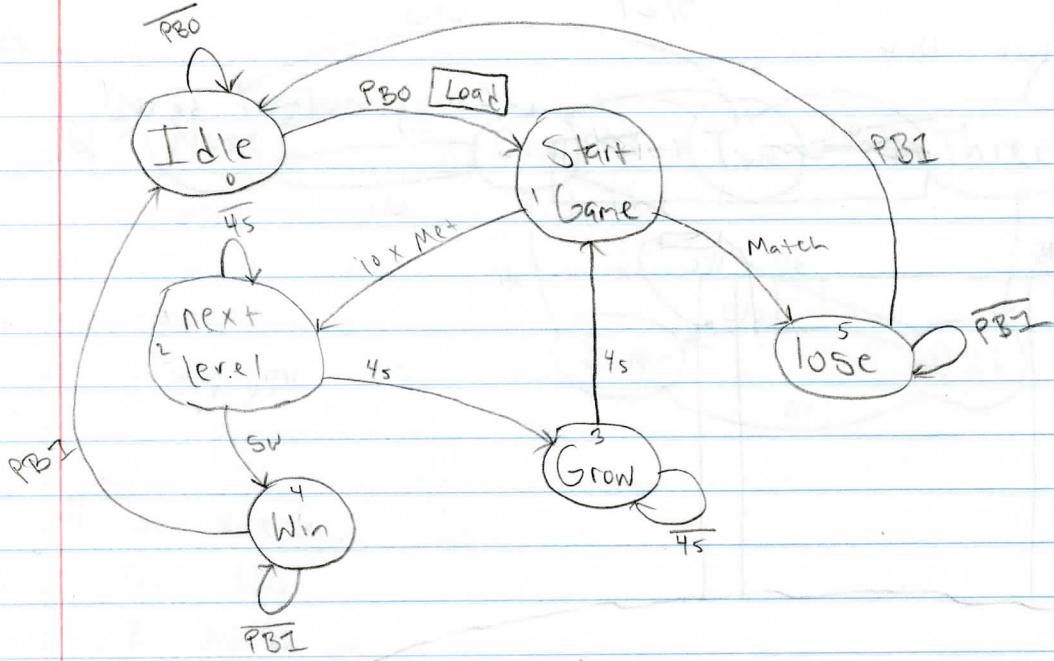
every 10 is Inc level, flap magenta, grow & Inc level & flap sl

key playing after that pause.

When lose, meteor stop and slug (earlier, PB2 to return to TLE)

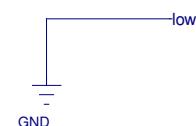
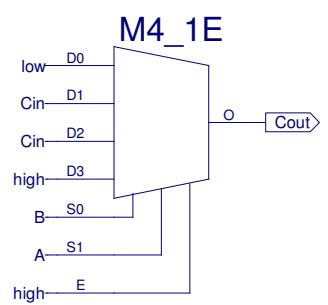
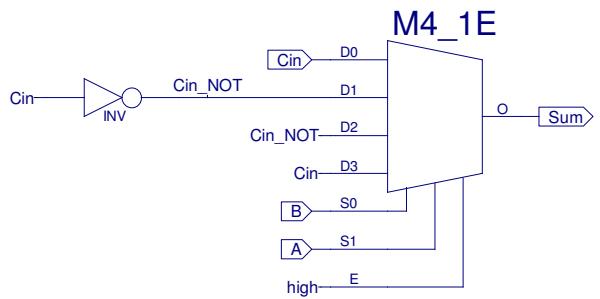
When switch match (inc), flash meteor/Slug, PB2 to return to Idle.

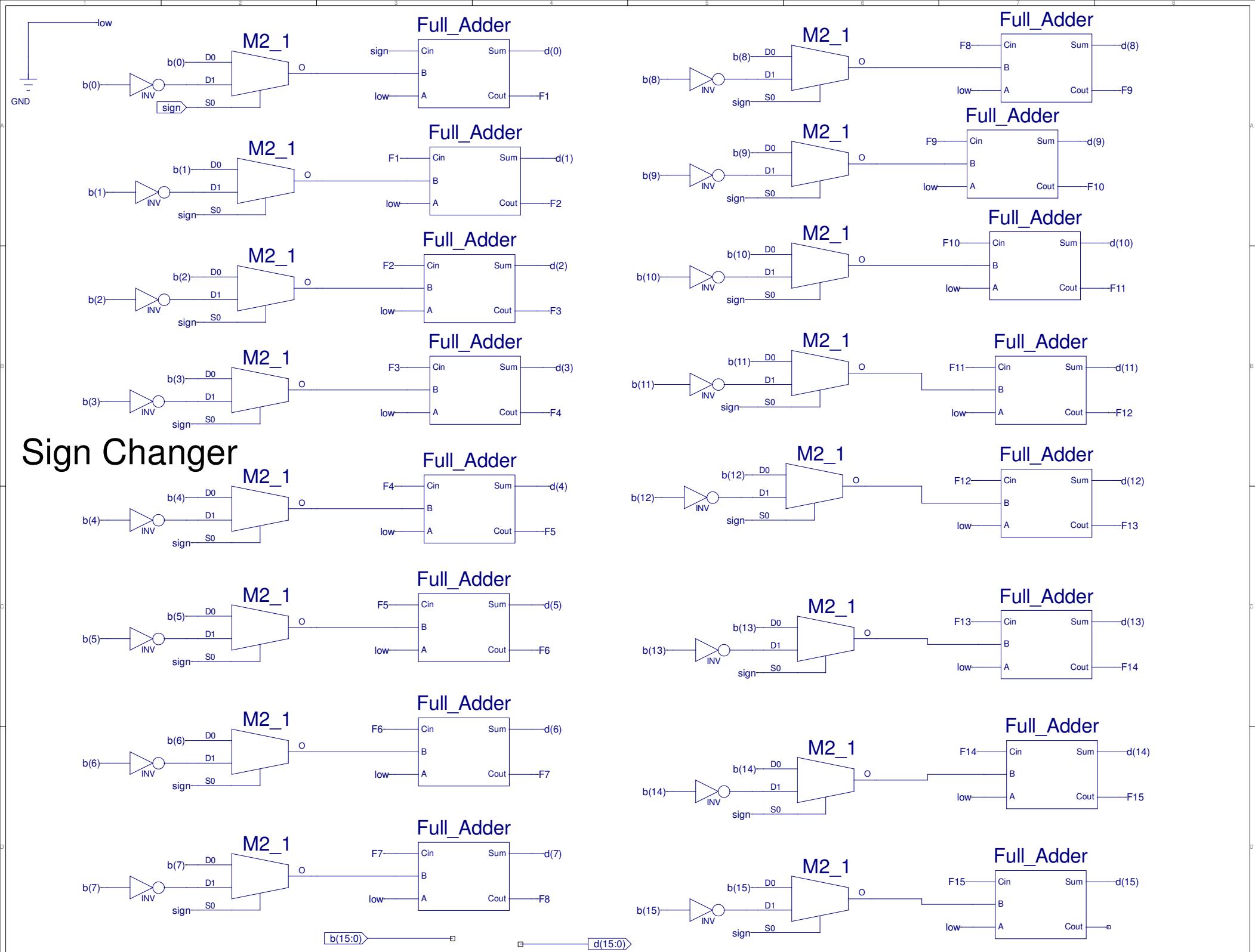
grow → 16 pixels



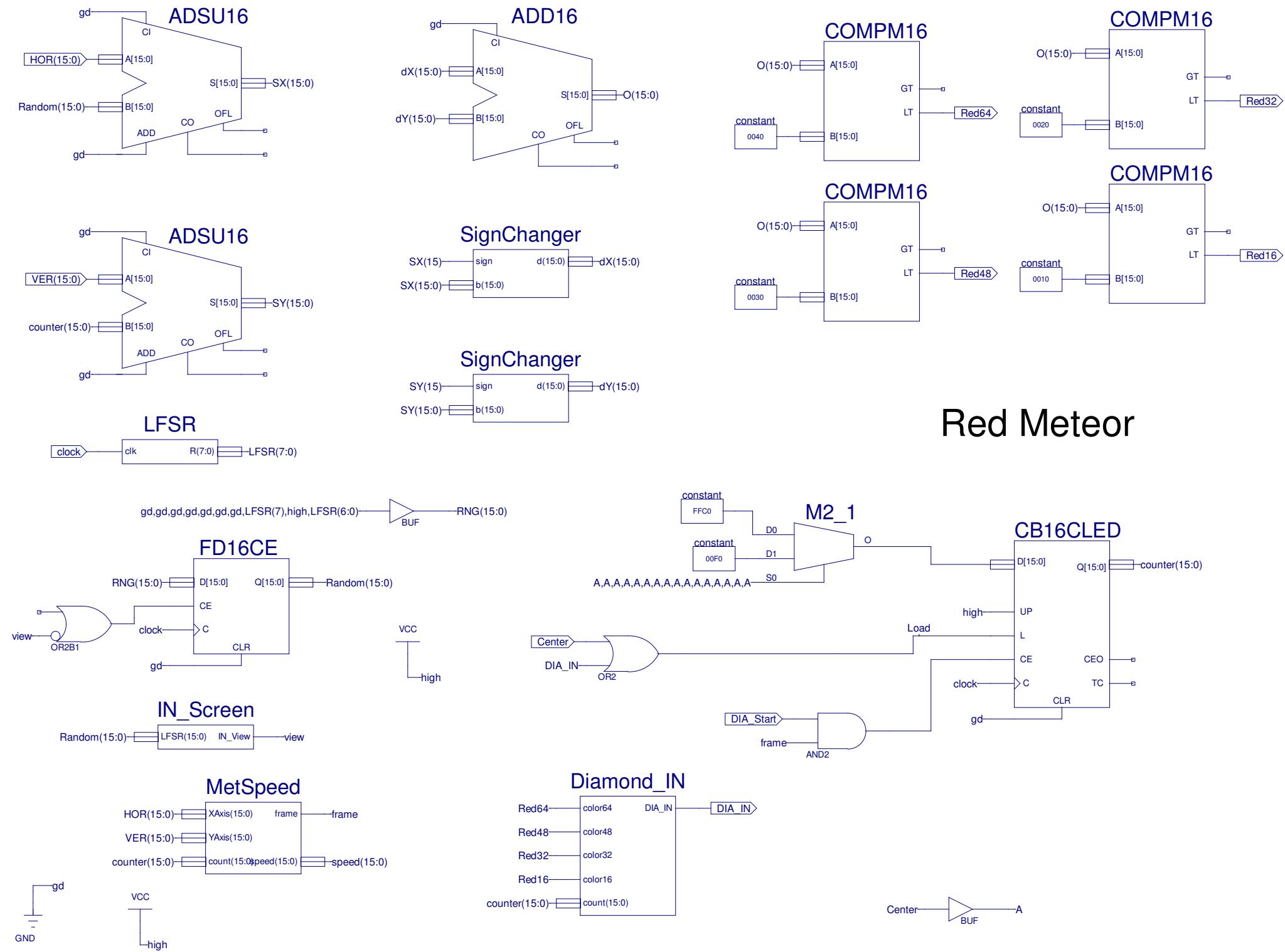
Correct

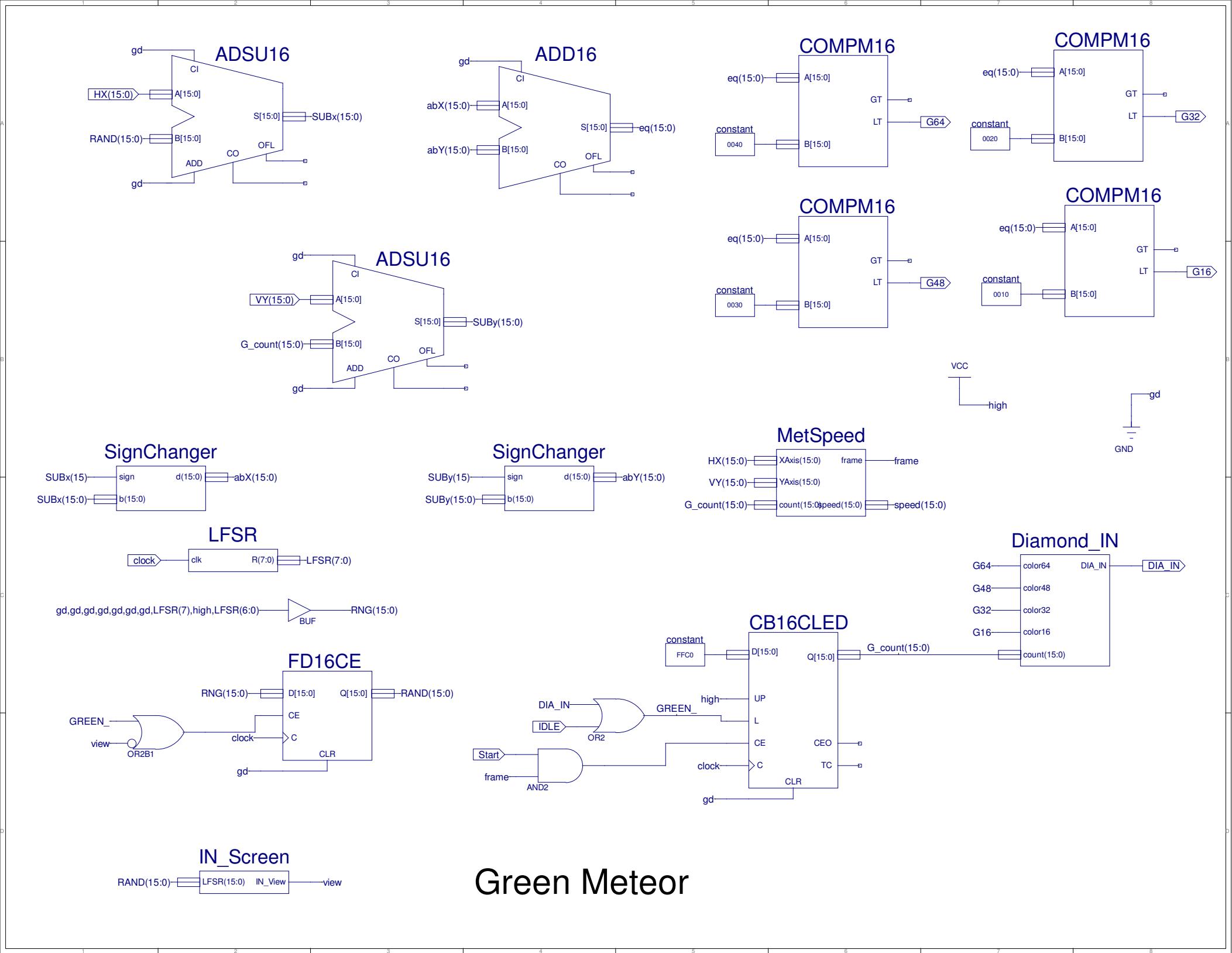
Full Adder





Red Meteor



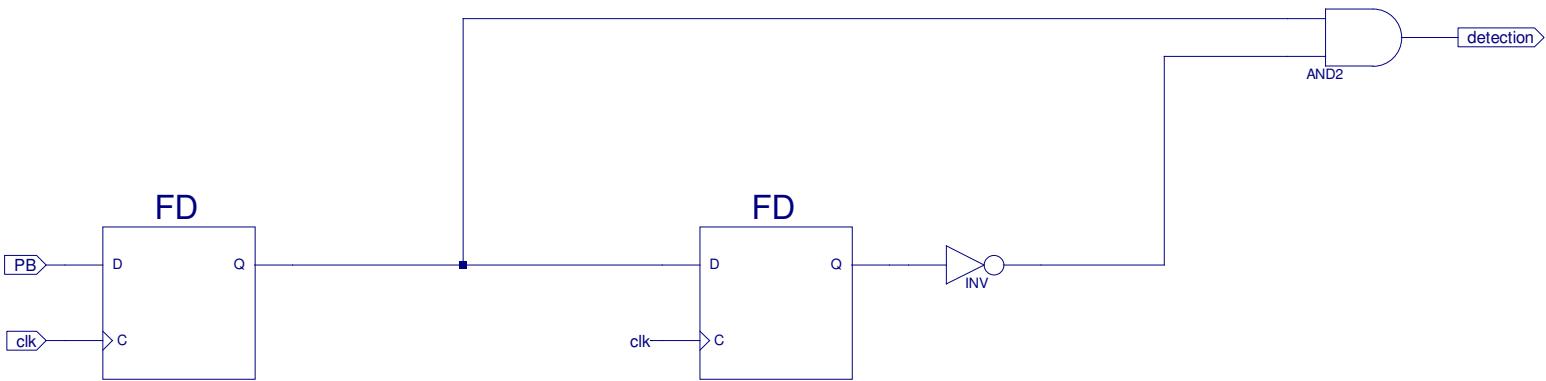


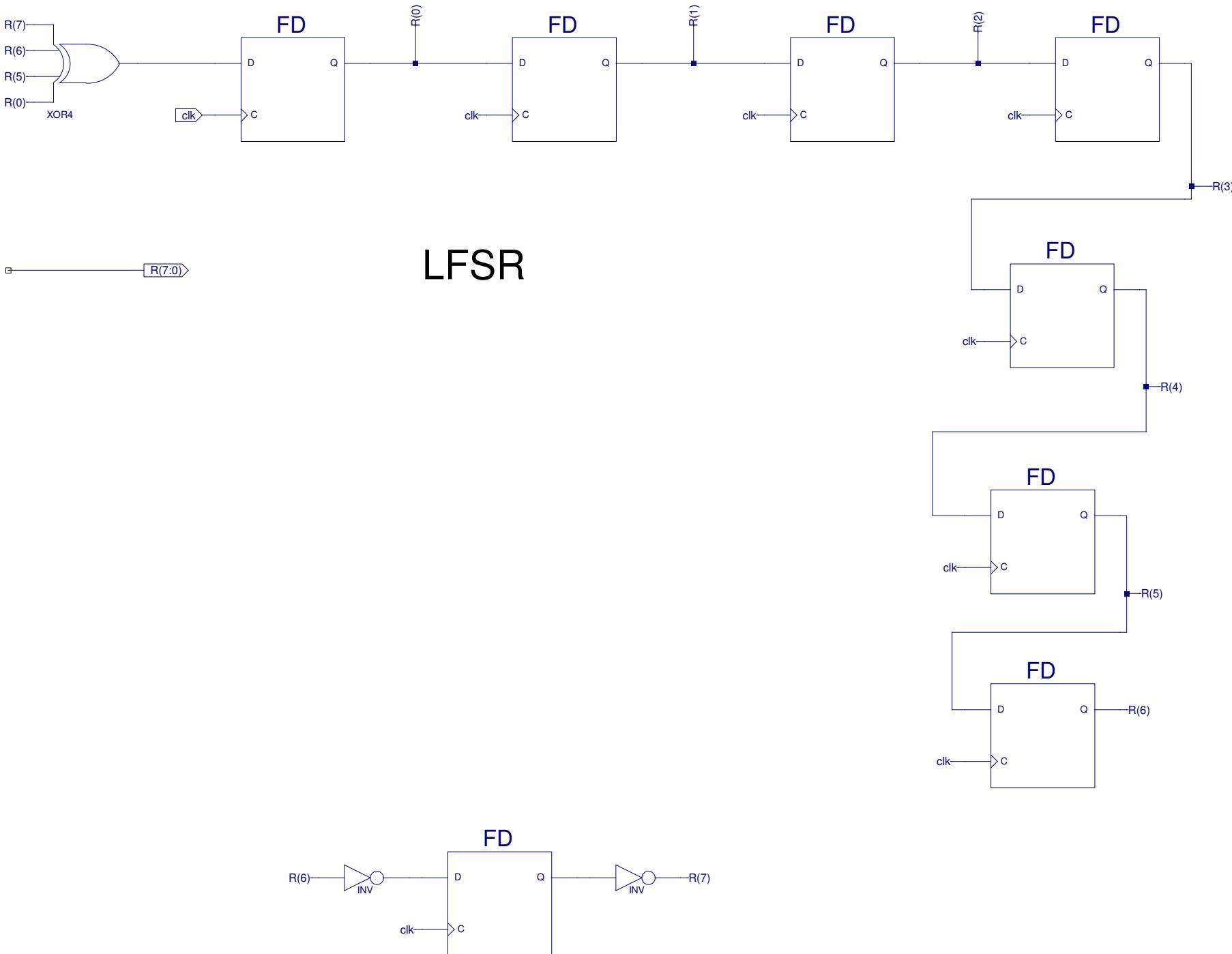
```
1  `timescale 1ns / 1ps
2  ///////////////////////////////////////////////////////////////////
3  // Company:
4  // Engineer:
5  //
6  // Create Date:    19:38:42 05/30/2016
7  // Design Name:
8  // Module Name:   IN_Screen
9  // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 module IN_Screen(LFSR, IN_View);
22
23 input [15:0] LFSR;
24
25 output IN_View;
26
27 //assign IN_View = (520 >= LFSR >= 150);
28 assign IN_View = ((LFSR <= 520) & (LFSR >= 150));
29
30 endmodule
31
```

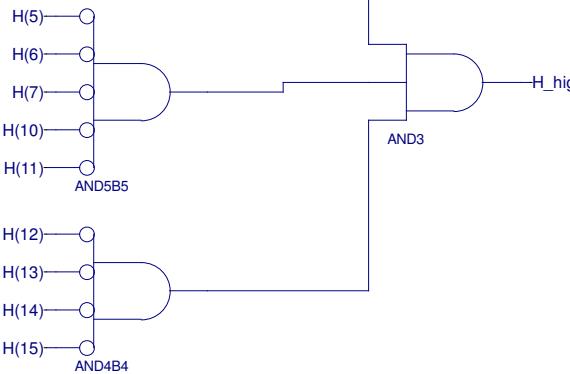
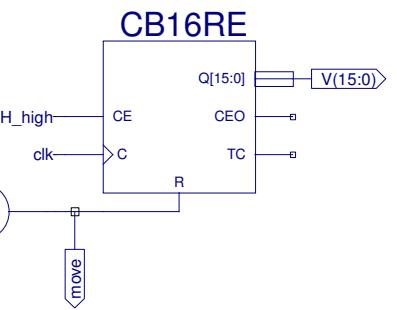
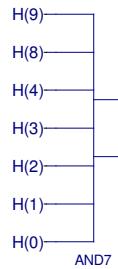
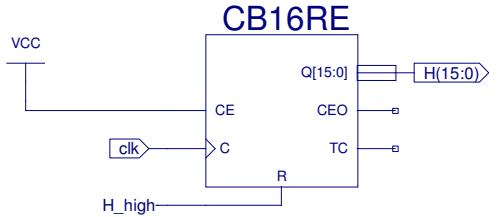
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1  `timescale 1ns / 1ps
2  ///////////////////////////////////////////////////////////////////
3  // Company:
4  // Engineer:
5  //
6  // Create Date:    19:32:05 05/30/2016
7  // Design Name:
8  // Module Name:   Diamond_IN
9  // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 ///////////////////////////////////////////////////////////////////
21 module Diamond_IN(count, color64, color48, color32, color16,
DIA_IN);
22
23 input [15:0] count;
24 input color64;
25 input color48;
26 input color32;
27 input color16;
28
29 output DIA_IN;
30
31 assign DIA_IN = (color64 & color48 & color32 & color16 & (count
==530));
32
33
34 endmodule
35
```

```
1  `timescale 1ns / 1ps
2  ///////////////////////////////////////////////////////////////////
3  // Company:
4  // Engineer:
5  //
6  // Create Date:    19:16:56 05/28/2016
7  // Design Name:
8  // Module Name:   MetSpeed
9  // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 module MetSpeed(XAxis, YAxis, count, frame, speed);
22
23     input [15:0] XAxis;
24     input [15:0] YAxis;
25     input [15:0] count;
26
27     output frame;
28     output [15:0] speed;
29
30     assign frame = ((XAxis==480 & YAxis==480) | (XAxis==480 &
31     YAxis==481));
32     assign speed = (count + count);
33
34 endmodule
```

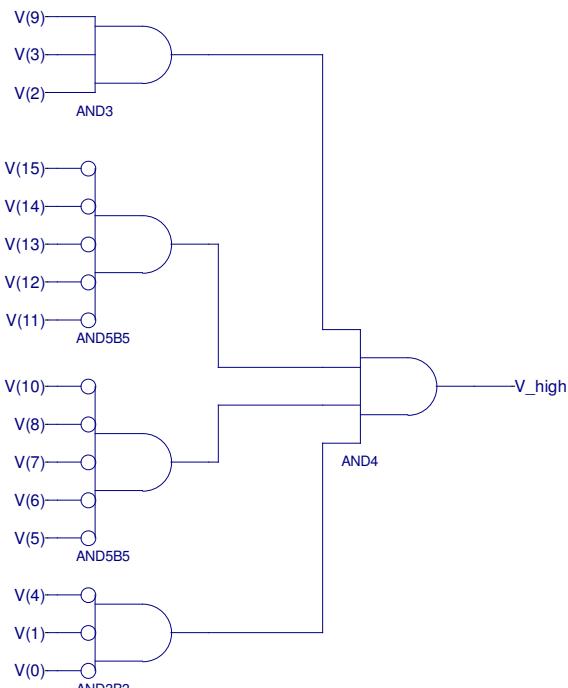
Edge Detector

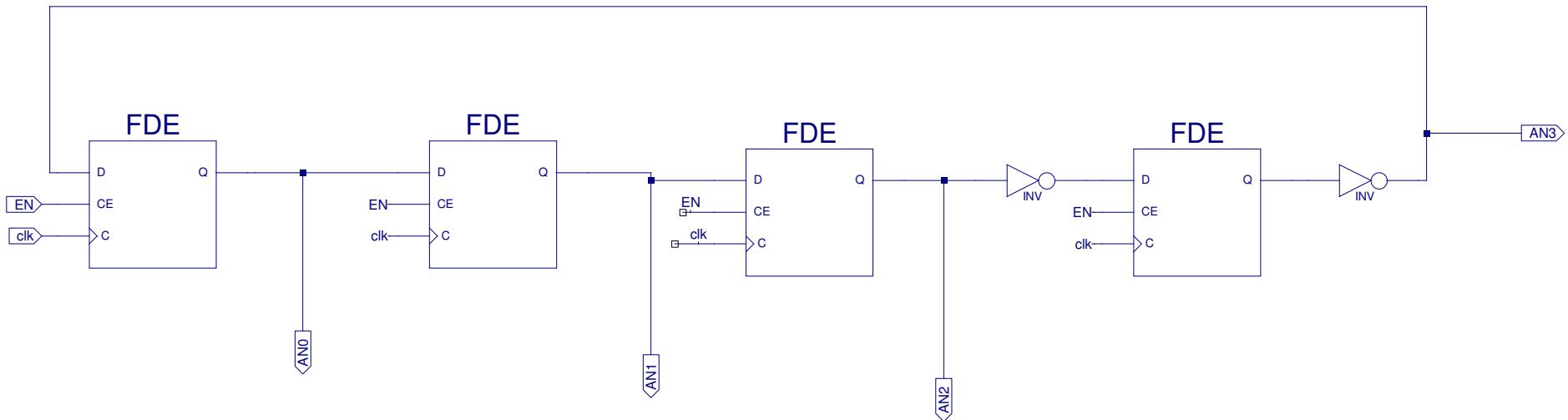






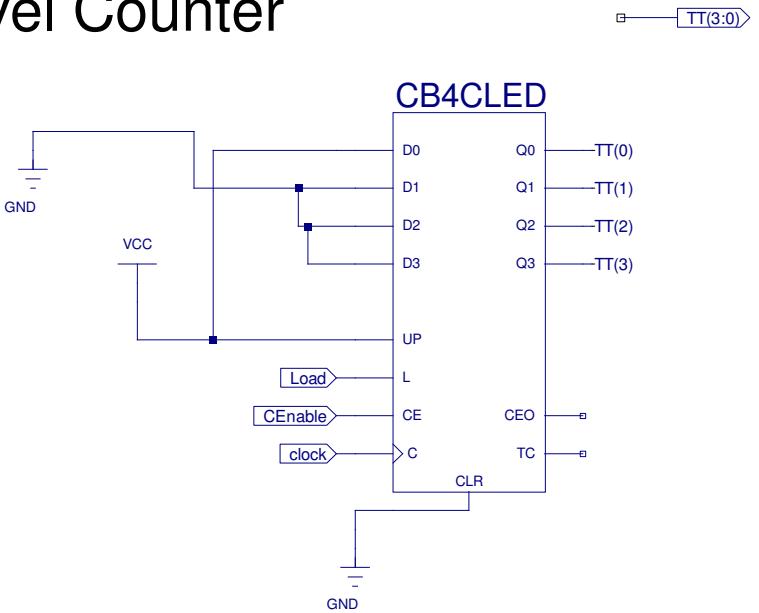
Pixel Counter





Ring Counter

Level Counter

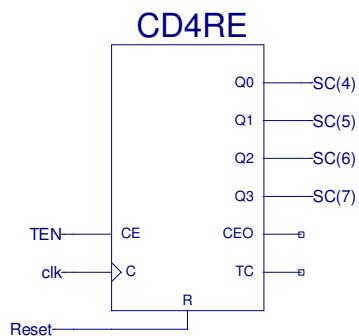
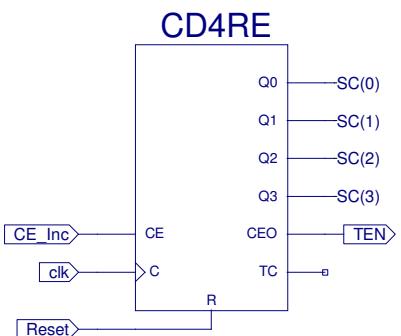


□ TT(3:0)

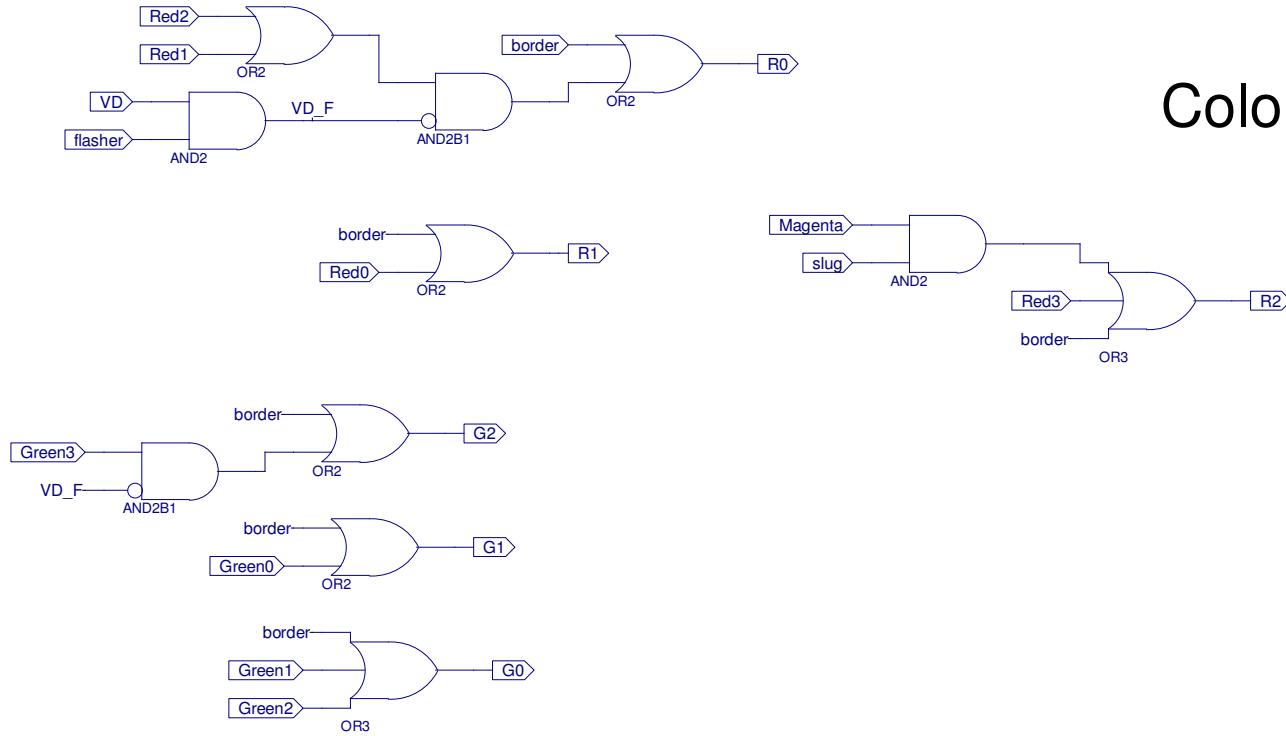
```
1  `timescale 1ns / 1ps
2  ///////////////////////////////////////////////////////////////////
3  // Company:
4  // Engineer:
5  //
6  // Create Date:    12:41:30 05/19/2016
7  // Design Name:
8  // Module Name:   hex7seg
9  // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 module hex7seg(s3, s2, s1, s0, CA, CB, CC, CD, CE, CF, CG);
22   input s3, s2, s1, s0;
23   output CA, CB, CC, CD, CE, CF, CG;
24
25   assign CA = (s3|s2|s1|~s0) & (s3|~s2|s1|s0) & (~s3|s2|~s1|~s0) & (~s3|~s2|s1|~s0);
26   assign CB = (s3|~s2|s1|~s0) & (s3|~s2|~s1|s0) & (~s3|s2|~s1|~s0) & (~s3|~s2|s1|s0) & (~s3|~s2|~s1|~s0);
27   assign CC = (s3|s2|~s1|s0) & (~s3|~s2|s1|s0) & (~s3|~s2|~s1|s0) & (~s3|~s2|~s1|~s0);
28   assign CD = (s3|s2|s1|~s0) & (s3|~s2|s1|s0) & (s3|~s2|~s1|~s0) & (~s3|s2|~s1|s0) & (~s3|~s2|~s1|~s0);
29   assign CE = (s3|s2|s1|~s0) & (s3|s2|~s1|~s0) & (s3|~s2|s1|s0) & (s3|~s2|~s1|~s0) & (s3|~s2|~s1|~s0);
30   assign CF = (s3|s2|s1|~s0) & (s3|s2|~s1|s0) & (s3|s2|~s1|~s0) & (s3|~s2|~s1|~s0) & (~s3|~s2|~s1|~s0);
31   assign CG = (s3|s2|s1|s0) & (s3|s2|s1|~s0) & (s3|~s2|~s1|~s0) & (~s3|~s2|s1|~s0);
32
33 endmodule
```

SC(7:0)

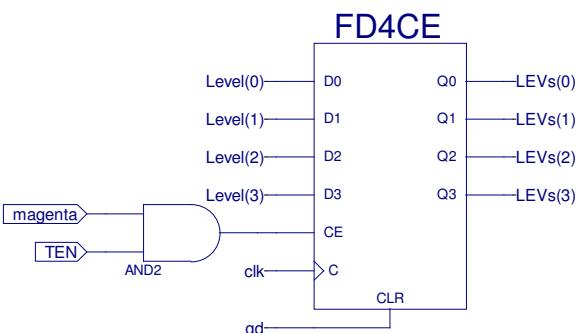
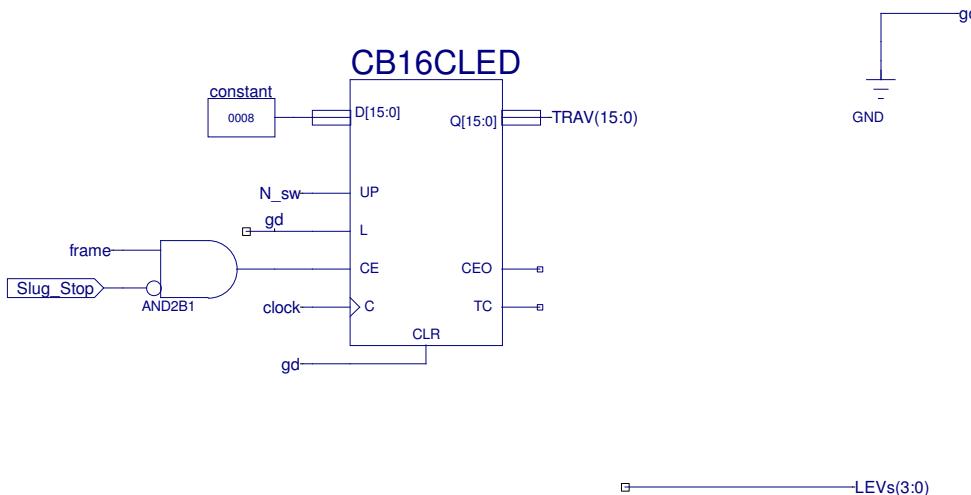
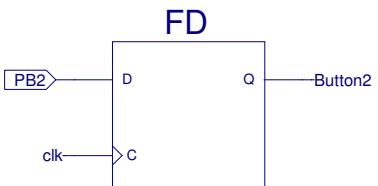
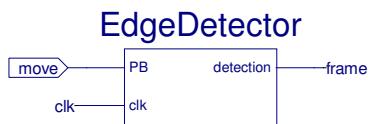
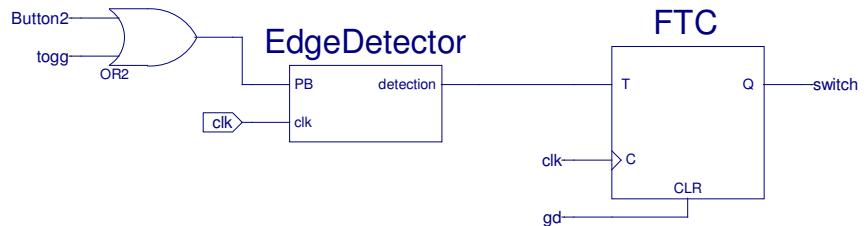
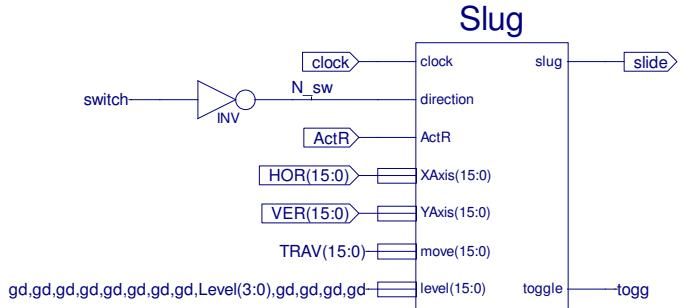
Score Counter



Color Logic



Slug Logic



Level(3:0) → □

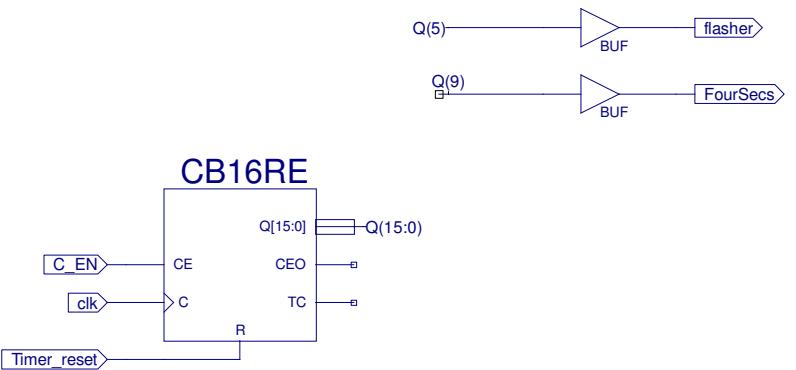
```
1 `timescale 1ns / 1ps
2 ///////////////////////////////////////////////////////////////////
3 // Company:
4 // Engineer:
5 //
6 // Create Date:    09:45:51 05/19/2016
7 // Design Name:
8 // Module Name:   Borders
9 // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 ///////////////////////////////////////////////////////////////////
22 module Borders(XAxis, YAxis, Clock, HS, VS, border, ActR);
23
24     input [15:0]XAxis;
25     input [15:0]YAxis;
26     input Clock;
27
28     output HS;
29     output VS;
30     output border;
31     output ActR;
32
33     assign HS = (XAxis < 655) | (XAxis > 750);
34     assign VS = (YAxis < 489) | (YAxis > 490);
35     assign ActR = (XAxis >= 0 & XAxis <= 639) & (YAxis >= 0 & YAxis
36     <= 479);
37     //assign border = ((XAxis >=0 & XAxis <= 7) | (YAxis >= 0 &
38     YAxis <= 7) | (XAxis >= 632 & XAxis <= 639) | (YAxis >= 472 &
39     YAxis <= 479));
40     assign topborder = (YAxis >= 0 & YAxis <= 7);
41     assign bottomborder = (YAxis >= 472 & YAxis <= 479);
42     assign leftborder = (XAxis >=0 & XAxis <= 7);
43     assign rightborder = (XAxis >= 632 & XAxis <= 639);
44     assign border = ActR & (leftborder | rightborder | topborder |
```

```
    bottomborder); // & ActR;  
42  
43  
44 endmodule  
45
```

```
1 `timescale 1ns / 1ps
2 ///////////////////////////////////////////////////////////////////
3 // Company:
4 // Engineer:
5 //
6 // Create Date:    22:40:08 05/23/2016
7 // Design Name:
8 // Module Name:   Slug
9 // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 module Slug(clock, XAxis, YAxis, move, level, direction, ActR,
slug, toggle);
22
23     input clock;
24     input [15:0]XAxis;
25     input [15:0]YAxis;
26     input [15:0]move;
27     input [15:0]level;
28     input direction;
29     input ActR;
30
31     output slug;
32     output toggle;
33 //output slug_leftbound;
34 //output slug_rightbound;
35
36 //wire slug_leftbound;
37 //wire slug_rightbound;
38
39 //assign XAxis_MAX = 799;
40 //assign XAxis_MIN = 0;
41 //assign slug_LE = (XAxis >= (7 + move));
42 //assign slug_RE = (XAxis <= (71 + move));
43     assign slug = (ActR & (((XAxis >= (8 + move ) & XAxis <= (56
```

```
+ move + level) & (YAxis >= 404 & YAxis <= 420))));  
44 //assign slug = (slug_LE & slug_RE & (420 >= YAxis >= 404));  
45 //assign travel = (slug > XAxis + 7) & (slug + 568 < XAxis);  
46 //assign toggle = (slug == border);  
47 //assign slug_leftbound = (move + 7);  
48 //assign slug_rightbound = (move + 71);  
49 //assign toggle = (slug_leftbound == 8 | slug_rightbound == 631);  
50 //assign toggle = (slug == 7 | slug == 632);  
51     assign toggle = ((56 + move + level >= 631) & direction) | (  
move<=0 & ~direction);  
52 //assign toggle = (slug_LE == slug_leftbound) | (slug_RE ==  
slug_rightbound);  
53  
54  
55 endmodule  
56
```

Time Counter



```
1 `timescale 1ns / 1ps
2 ///////////////////////////////////////////////////////////////////
3 // Company:
4 // Engineer:
5 //
6 // Create Date: 20:04:35 05/28/2016
7 // Design Name:
8 // Module Name: FSM
9 // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 ///////////////////////////////////////////////////////////////////
21 module FSM(Start, FourSecs, Restart, Collision, Meteor_TEN,
22 Match, Diamond_Gone, PS, NS, Reset, Timer_RE, Score_INC,
23 Slug_Stop, F_Slug_Magenta, Flash, Victory_Dance, Center_Red,
24 Diamond_Start);
25     input Start;
26     input FourSecs;
27     input Restart;
28     input Collision;
29     input Meteor_TEN;
30     input Match;
31     input Diamond_Gone;
32     input [5:0] PS;
33
34     output [5:0] NS;
35     output Reset;
36     output Timer_RE;
37     output Score_INC;
38     output Slug_Stop;
39     output F_Slug_Magenta;
40     output Flash;
41     output Victory_Dance;
42     output Center_Red;
43     output Diamond_Start;
```

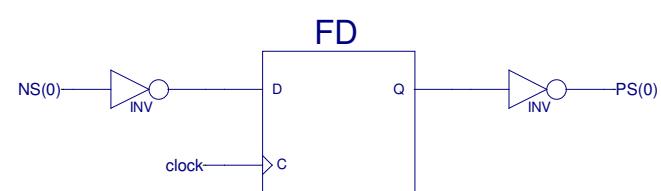
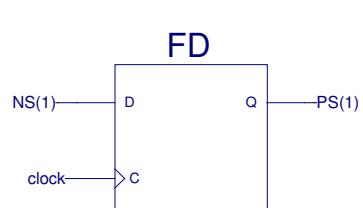
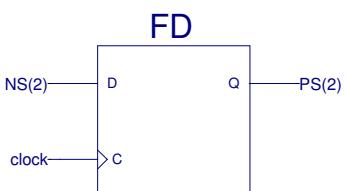
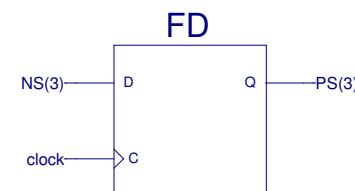
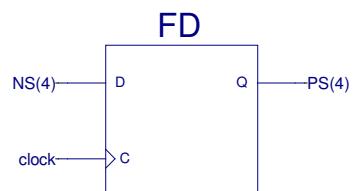
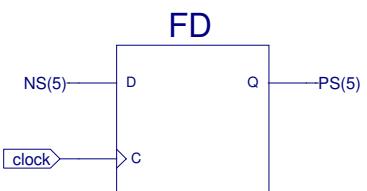
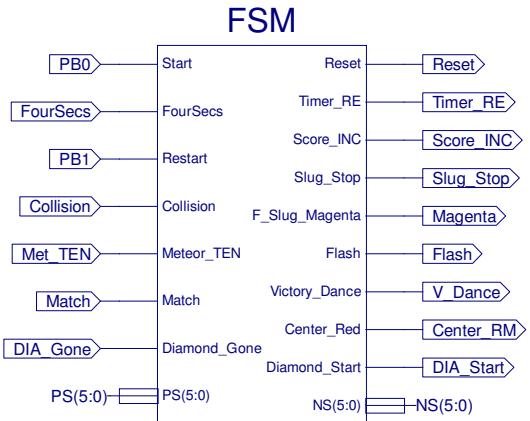
```
42
43
44     wire Idle, Start_Game, Lose, Level_Up, Grow, Win;
45     wire Next_Idle, Next_Start_Game, Next_Lose, Next_Level_Up,
Next_Grow, Next_Win;
46
47     assign Idle = PS[0];
48     assign Start_Game = PS[1];
49     assign Lose = PS[2];
50     assign Level_Up = PS[3];
51     assign Grow = PS[4];
52     assign Win = PS[5];
53
54     assign NS[0] = Next_Idle;
55     assign NS[1] = Next_Start_Game;
56     assign NS[2] = Next_Lose;
57     assign NS[3] = Next_Level_Up;
58     assign NS[4] = Next_Grow;
59     assign NS[5] = Next_Win;
60
61
62     assign Next_Idle = (Idle & ~Start) | (Win & Restart) | (Lose
& Restart);
63     assign Next_Start_Game = (Start_Game & ~Collision & ~
Meteor_TEN) | (Idle & Start) | (Grow & FourSecs);
64     assign Next_Lose = (Lose & ~Restart) | (Start_Game &
Collision);
65     assign Next_Level_Up = (Level_Up & ~FourSecs) | (Start_Game &
Meteor_TEN & ~Match);
66     assign Next_Grow = (Grow & ~FourSecs) | (Level_Up & FourSecs);
67     assign Next_Win = (Win & ~Restart) | (Start_Game & Match &
Meteor_TEN);
68
69 // assign Meteor_TEN = (MET_reset + MET_reset + MET_reset +
MET_reset + MET_reset + MET_reset + MET_reset + MET_reset +
MET_reset + MET_reset);
70
71 // assign Load = (Idle & Start) | (Grow & FourSecs);
72     assign Reset = Idle;
73     assign Timer_RE = ((Grow & FourSecs) | (Level_Up & FourSecs)
| (Start_Game & Meteor_TEN));
74     assign Score_INC = (Start_Game & Diamond_Gone);
75     assign Slug_Stop = (Lose | Level_Up | Grow | Win);
76 // assign Level_Inc = Meteor_TEN;
77     assign F_Slug_Magenta = Level_Up;
78     assign Flash = (Lose | Level_Up | Grow);

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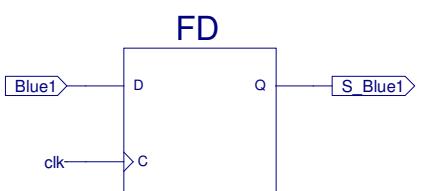
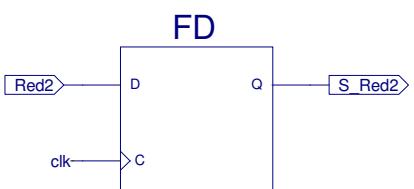
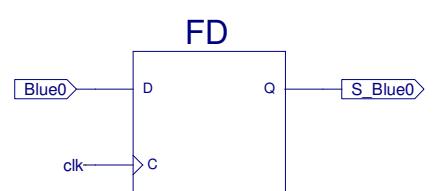
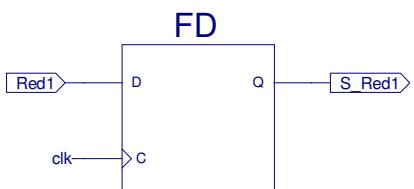
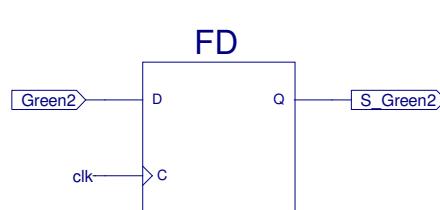
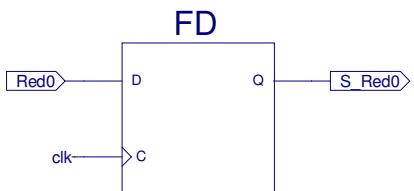
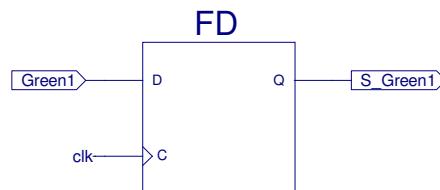
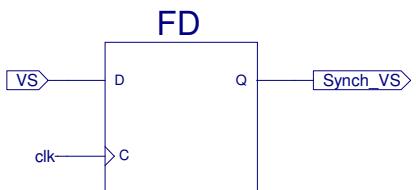
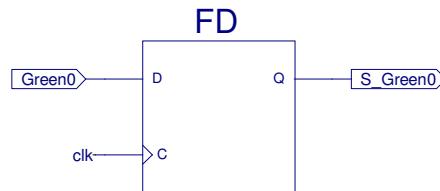
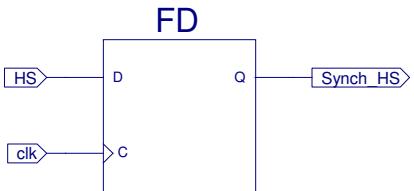

```

```
79      assign Victory_Dance = Win;
80      assign Center_Red = Idle;
81      assign Diamond_Start = Start_Game;
82
83
84
85  endmodule
86
```

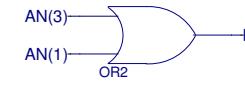
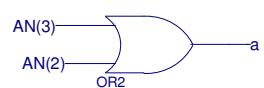
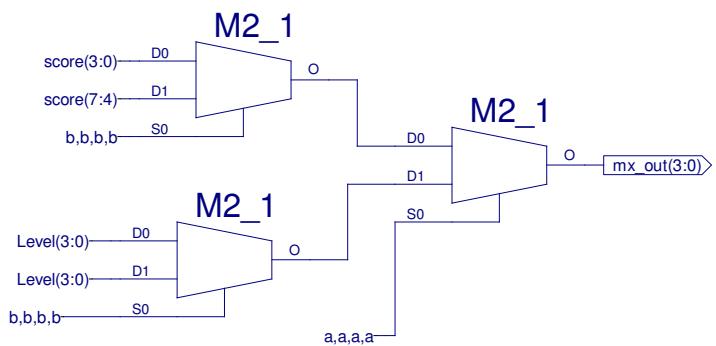
FSM Flip Flops

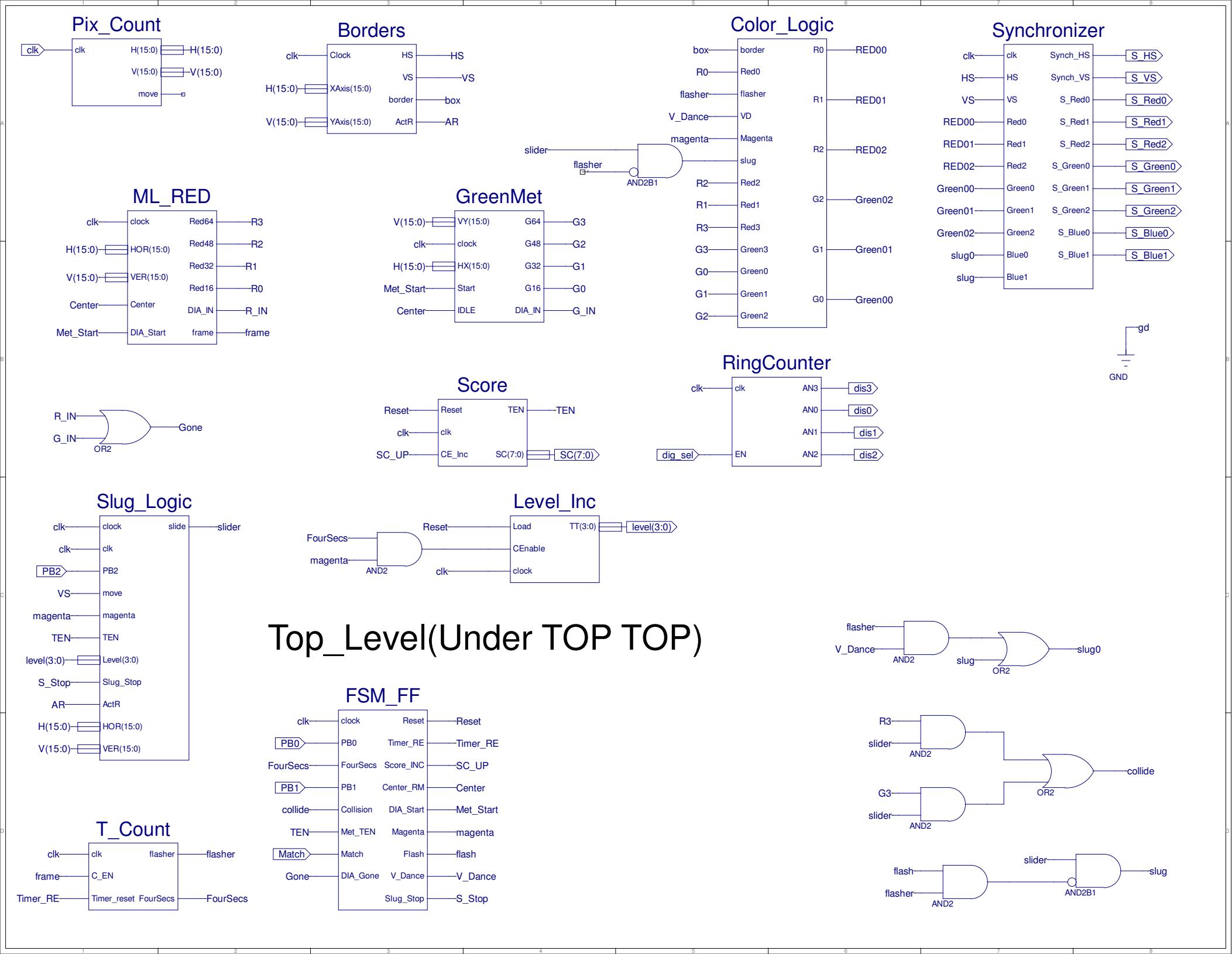


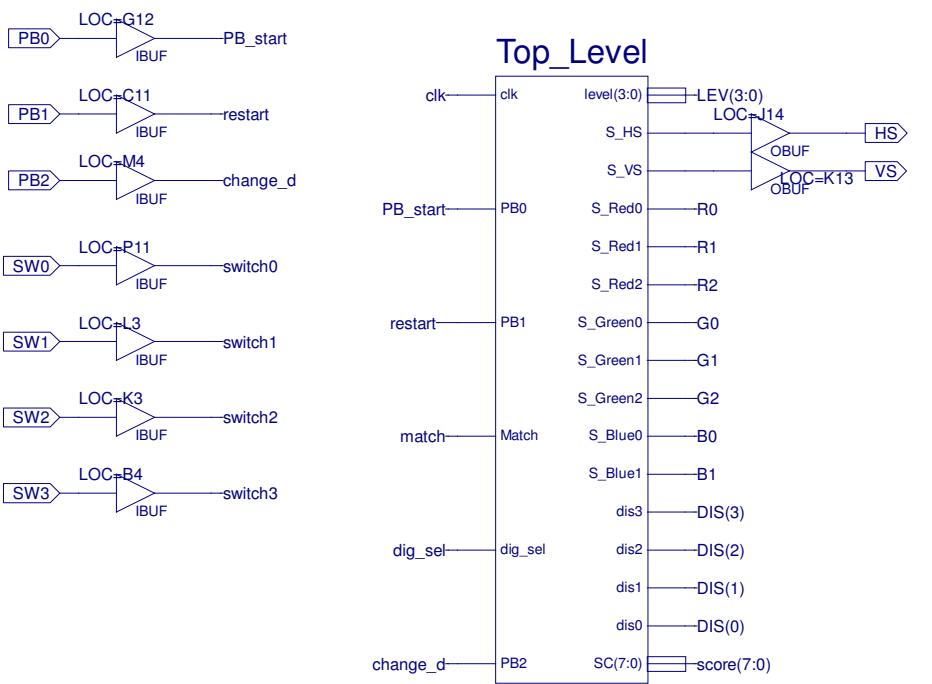
Synchronizer



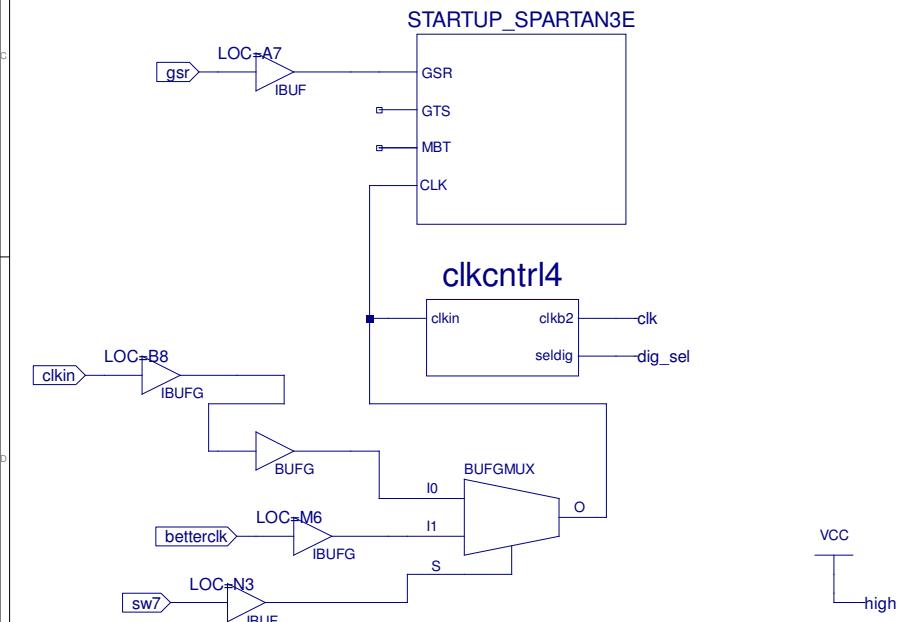
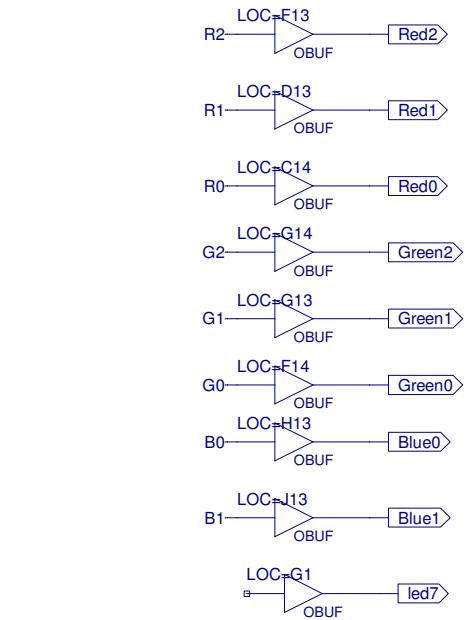
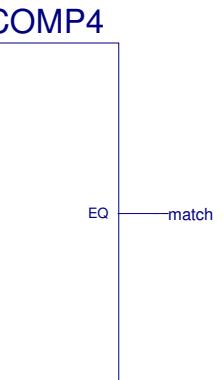
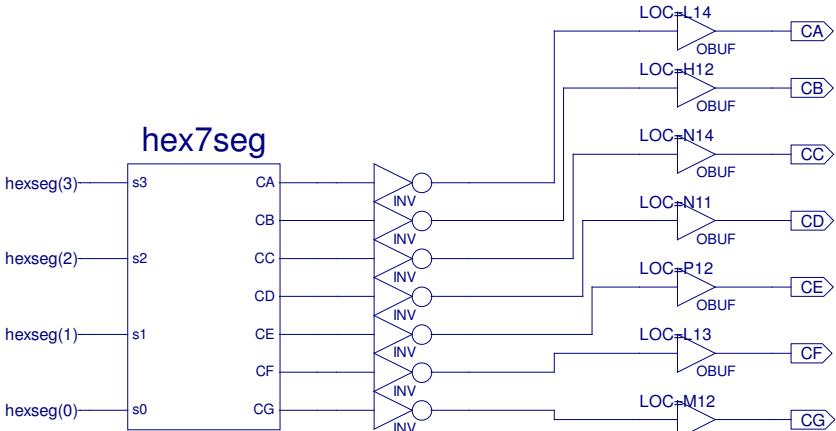
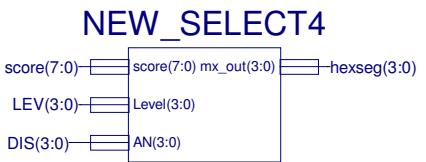
Selector





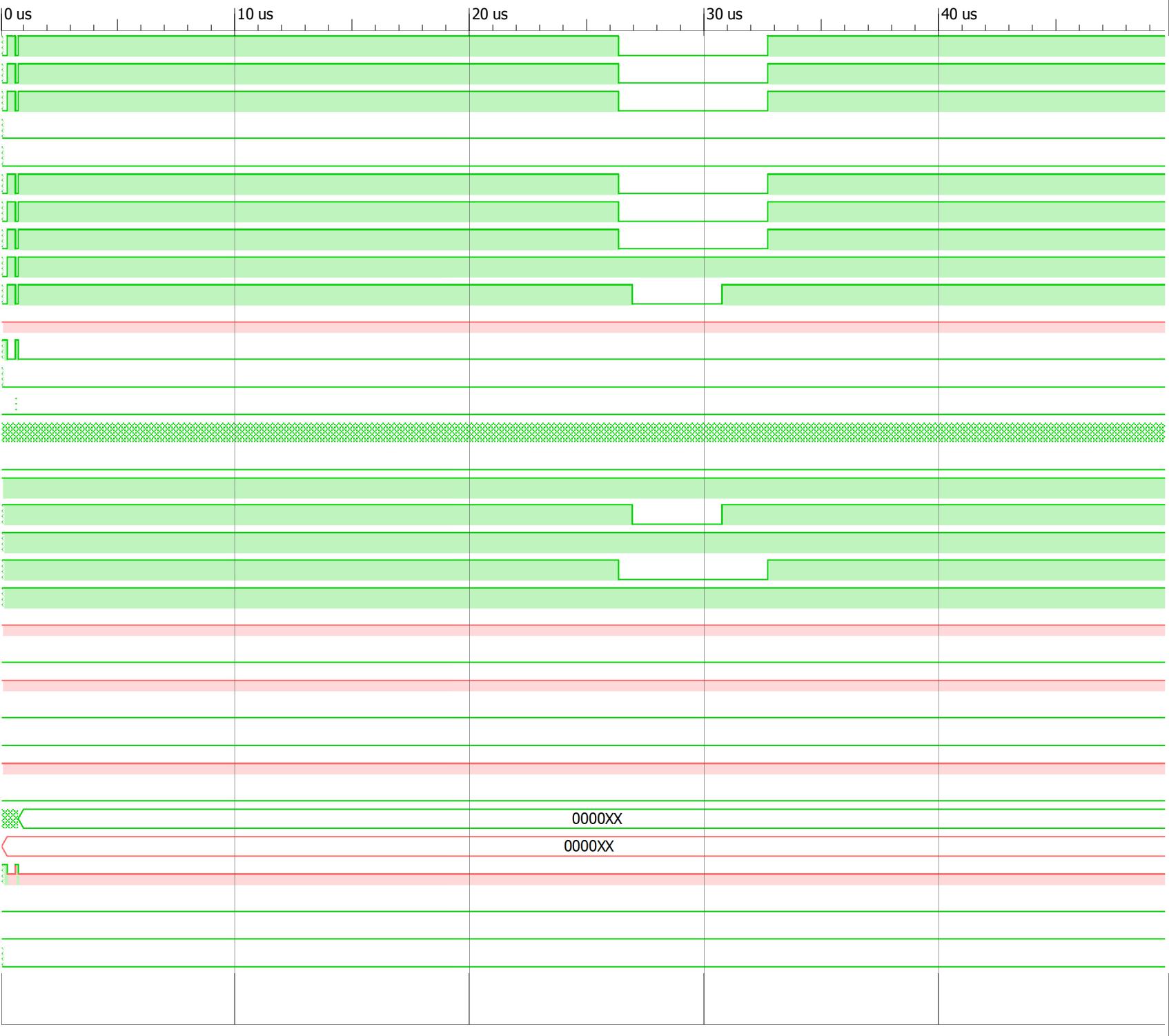


TOP_TOP

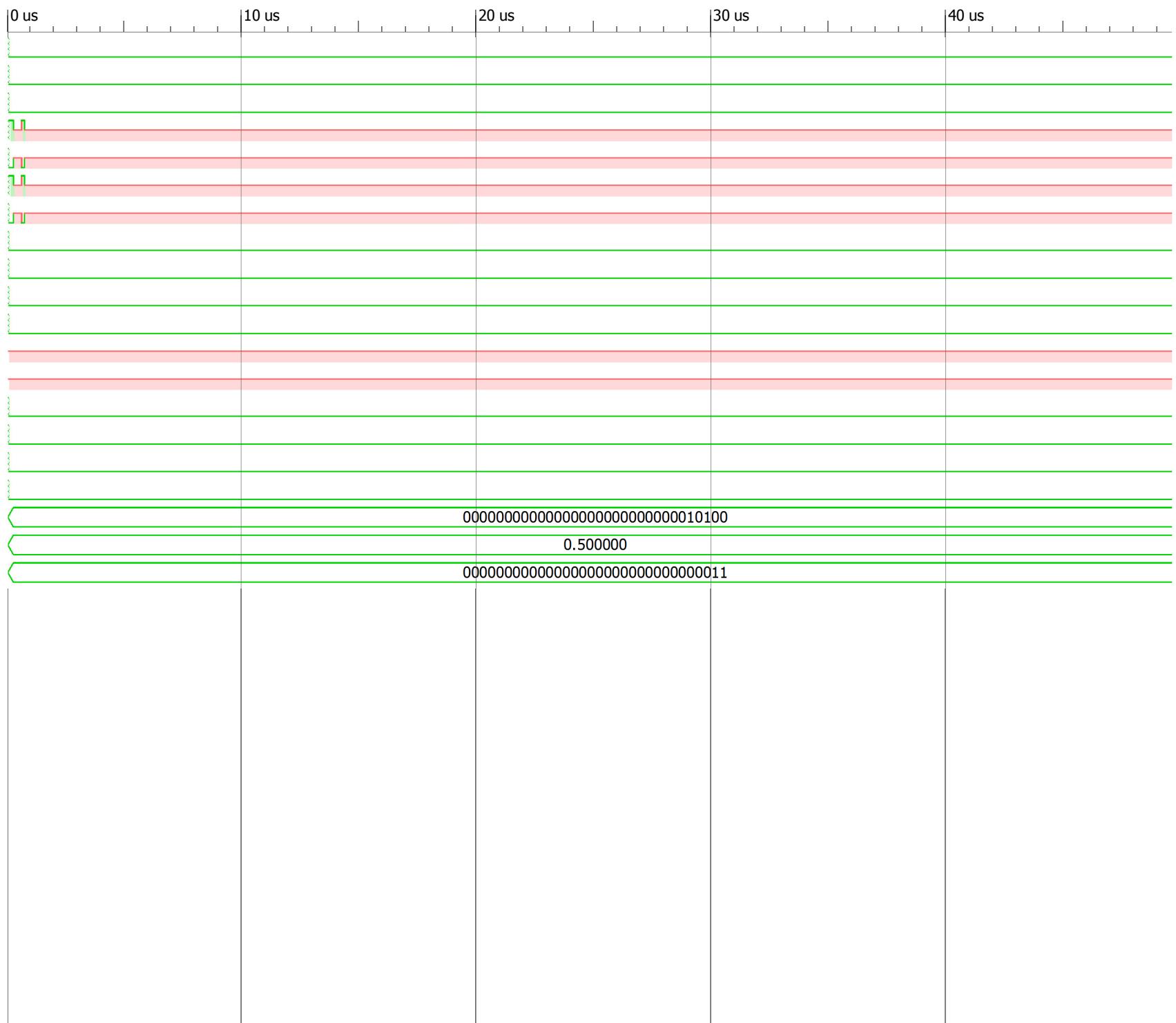


VR2
VR1
VR0
VB0
VB1
VG2
VG0
VG1
VS
HS
led7
oops
rgb_oops
gsr
...

betterclk
clkin
sw7
good_HS
good_VS
activeH
activeV
Start
FourSecs
Restart
Collision
Meteor_TEN
Match
Diamond_Gone
PS[5:0]
NS[5:0]
Reset
Timer_RE
Score_INC
Slug_Stop



- F_Slug_Magenta
- Flash
- Victory_Dance
- Center_Red
- Diamond_Start
- Idle
- Start_Game
- Lose
- Level_Up
- Grow
- Win
- Next_Idle
- Next_Start_Game
- Next_Lose
- Next_Level_Up
- Next_Grow
- Next_Win
- PERIOD[31:0]
- DUTY_CYCLE
- OFFSET[31:0]



```

-----
Release 14.7 Trace (nt64)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.

C:\Xilinx\14.7\ISE_DS\ISE\bin\nt64\unwrapped\trce.exe -filter
C:/Users/hqayum/Desktop/Lab7_DONE!!!/Lab7_CE100/iseconfig/filter.filter
-intstyle ise -v 3 -s 5 -n 3 -fastpaths -xml TOP_TOP.twx TOP_TOP.ncd -o
TOP_TOP.twr TOP_TOP.pcf

Design file:          TOP_TOP.ncd
Physical constraint file: TOP_TOP.pcf
Device,package,speed: xc3s100e,cp132,-5 (PRODUCTION 1.27 2013-10-13)
Report level:         verbose report

Environment Variable      Effect
-----
NONE                      No environment variables were set
-----

INFO:Timing:2698 - No timing constraints found, doing default enumeration.

INFO:Timing:3412 - To improve timing, see the Timing Closure User Guide \(UG612\).
INFO:Timing:2752 - To get complete path coverage, use the unconstrained paths
option. All paths that are not constrained will be reported in the
unconstrained paths section(s) of the report.
INFO:Timing:3339 - The clock-to-out numbers in this timing report are based on
a 50 Ohm transmission line loading model. For the details of this model,
and for more information on accounting for different loading conditions,
please see the device datasheet.
INFO:Timing:3390 - This architecture does not support a default System Jitter
value, please add SYSTEM_JITTER constraint to the UCF to modify the Clock
Uncertainty calculation.
INFO:Timing:3389 - This architecture does not support 'Discrete Jitter' and
'Phase Error' calculations, these terms will be zero in the Clock
Uncertainty calculation. Please make appropriate modification to
SYSTEM_JITTER to account for the unsupported Discrete Jitter and Phase
Error.
```

Data Sheet report:

All values displayed in nanoseconds (ns)

Setup/Hold to clock betterclk

Source	Max Setup to	Max Hold to	Clock	Phase
	clk (edge)	clk (edge)	Internal Clock(s)	
PB0	2.277(R)	-0.156(R)	clk	0.000
PB1	1.629(R)	0.566(R)	clk	0.000
PB2	-0.062(R)	1.145(R)	clk	0.000
SW0	4.154(R)	-1.382(R)	clk	0.000
SW1	5.149(R)	-2.178(R)	clk	0.000
SW2	4.643(R)	-1.773(R)	clk	0.000
SW3	3.344(R)	-0.734(R)	clk	0.000
sw7	1.234(R)	-0.132(R)	XLXN_57	0.000

Setup/Hold to clock clkin

Source	Max Setup to	Max Hold to	Internal Clock(s)	Clock	Phase
	clk (edge)	clk (edge)			
PB0	0.094 (R)	2.497 (R)	clk	0.000	
PB1	-0.554 (R)	3.219 (R)	clk	0.000	
PB2	-2.245 (R)	3.798 (R)	clk	0.000	
SW0	1.971 (R)	1.271 (R)	clk	0.000	
SW1	2.966 (R)	0.475 (R)	clk	0.000	
SW2	2.460 (R)	0.880 (R)	clk	0.000	
SW3	1.161 (R)	1.919 (R)	clk	0.000	
sw7	-0.949 (R)	2.521 (R)	XLXN_60	0.000	

Clock betterclk to Pad

Destination	clk (edge)	Internal Clock(s)	Clock	Phase
	to PAD			
AN0	7.419 (R)	clk	0.000	
AN1	7.012 (R)	clk	0.000	
AN3	6.934 (R)	clk	0.000	
Blue0	7.445 (R)	clk	0.000	
Blue1	7.758 (R)	clk	0.000	
CA	12.011 (R)	clk	0.000	
CB	12.219 (R)	clk	0.000	
CC	11.800 (R)	clk	0.000	
CD	12.246 (R)	clk	0.000	
CE	11.728 (R)	clk	0.000	
CF	11.902 (R)	clk	0.000	
CG	12.132 (R)	clk	0.000	
Green0	7.217 (R)	clk	0.000	
Green1	7.099 (R)	clk	0.000	
Green2	7.306 (R)	clk	0.000	
HS	7.638 (R)	clk	0.000	
Red0	7.490 (R)	clk	0.000	
Red1	7.662 (R)	clk	0.000	
Red2	7.201 (R)	clk	0.000	
VS	7.124 (R)	clk	0.000	

Clock clkin to Pad

Destination	clk (edge)	Internal Clock(s)	Clock	Phase
	to PAD			
AN0	10.072 (R)	clk	0.000	
AN1	9.665 (R)	clk	0.000	
AN3	9.587 (R)	clk	0.000	
Blue0	10.098 (R)	clk	0.000	
Blue1	10.411 (R)	clk	0.000	
CA	14.664 (R)	clk	0.000	
CB	14.872 (R)	clk	0.000	
CC	14.453 (R)	clk	0.000	
CD	14.899 (R)	clk	0.000	
CE	14.381 (R)	clk	0.000	

CF		14.555 (R) clk		0.000
CG		14.785 (R) clk		0.000
Green0		9.870 (R) clk		0.000
Green1		9.752 (R) clk		0.000
Green2		9.959 (R) clk		0.000
HS		10.291 (R) clk		0.000
Red0		10.143 (R) clk		0.000
Red1		10.315 (R) clk		0.000
Red2		9.854 (R) clk		0.000
VS		9.777 (R) clk		0.000
-----+-----+-----+-----+				

Clock to Setup on destination clock betterclk

		Source Clock	Dest:Rise	Dest:Rise	Dest:Fall	Dest:Fall
betterclk		20.668				
clkin		20.668				
-----+-----+-----+-----+-----+-----+-----+						

Clock to Setup on destination clock clkin

		Source Clock	Dest:Rise	Dest:Rise	Dest:Fall	Dest:Fall
betterclk		20.668				
clkin		20.668				
-----+-----+-----+-----+-----+-----+-----+						

Analysis completed Fri Jun 03 17:50:35 2016

Trace Settings:

Trace Settings

Peak Memory Usage: 160 MB