

CoSA: Scheduling by Constrained Optimization for Spatial Accelerators

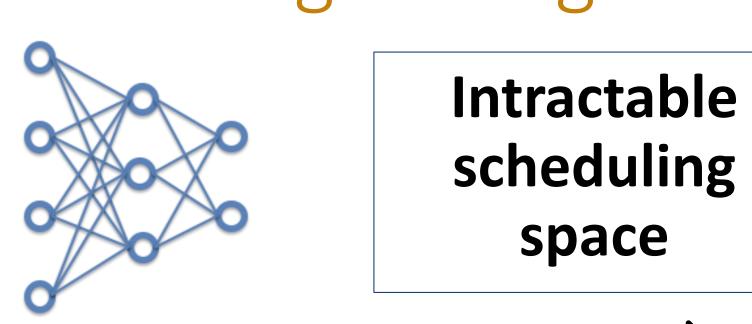
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Email: jennyhuang@nvidia.com Git repo: https://github.com/ucb-bar/cosa



Scheduling is a big challenge





~10¹³ possible mappings for a typical ResNet layer on a 3-level architecture

Accelerator-oriented scheduling

Key DNN accelerator properties to leverage:

Workload Regularity

for k in [0:K) // K = 15

 $OA[k] += IA[c] \times W[c,k]$

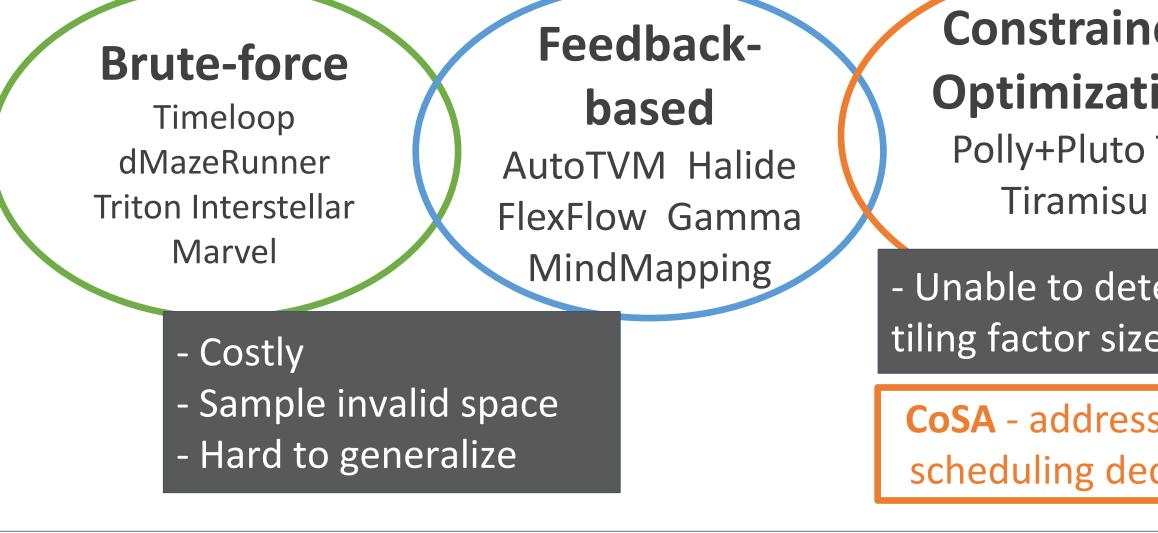
Exponentially growing

algorithm complexity

Known HW Constraints

Explicit Data Management

CoSA: a one-shot approach

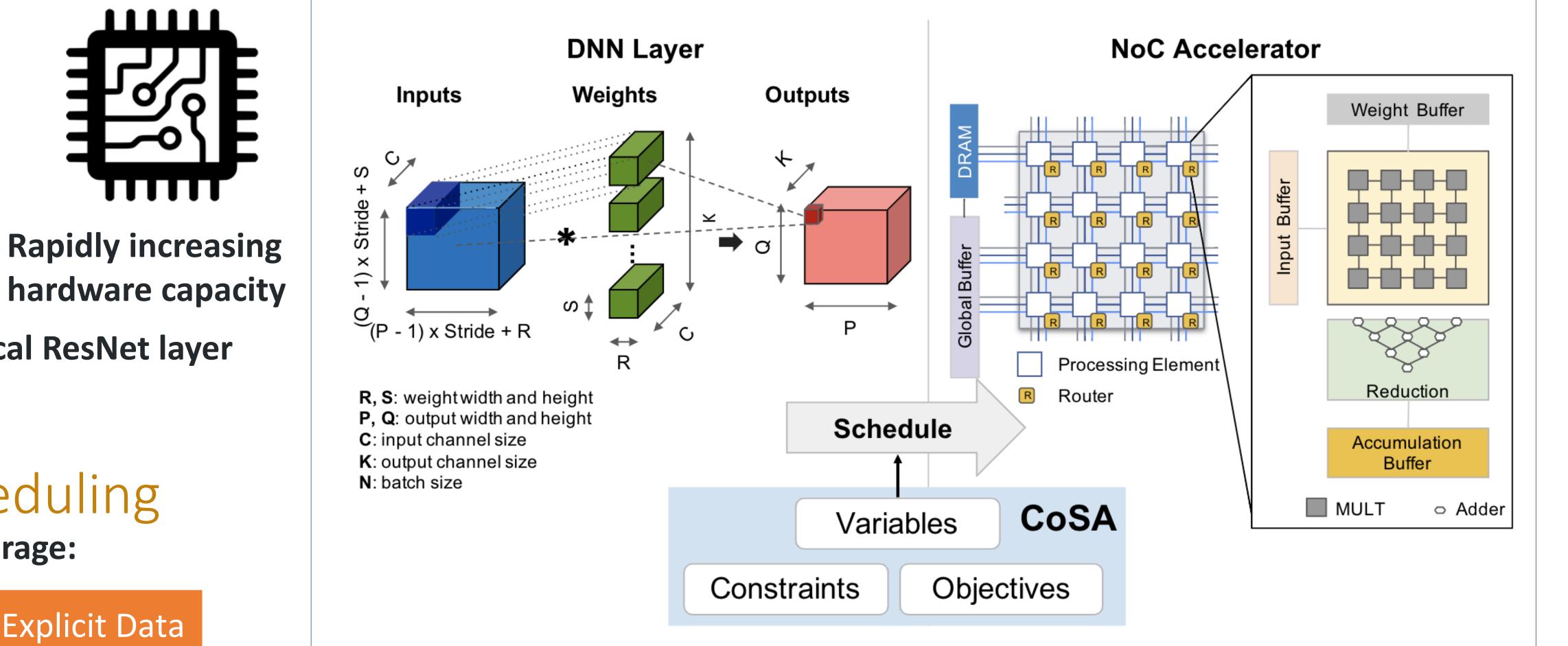


Constrained Optimization Polly+Pluto TC

Unable to determine tiling factor sizes

CoSA - addresses key scheduling decisions

DNN scheduling formulation with CoSA



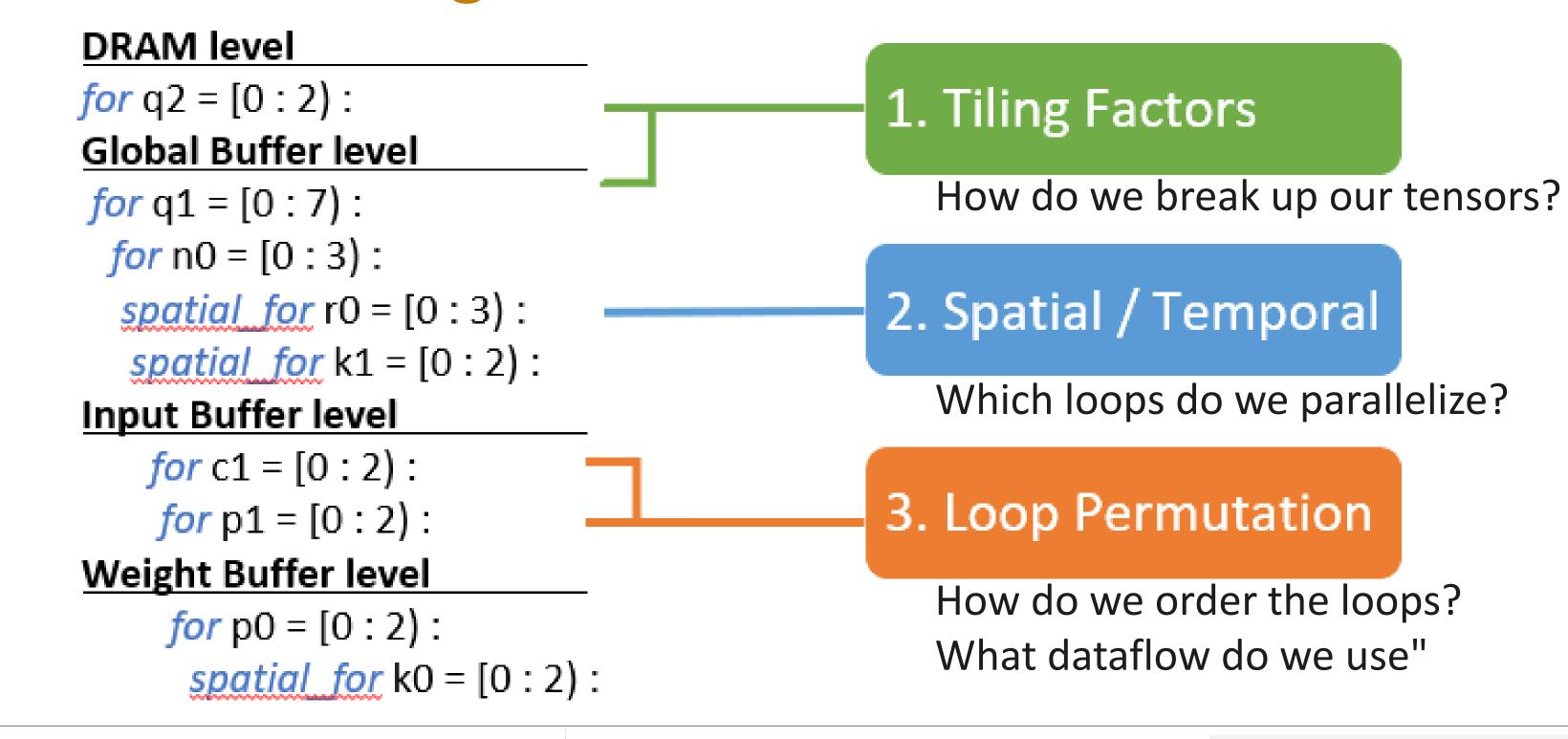
Three scheduling decisions

(Limit = 4)

Prime Factors

Spatial

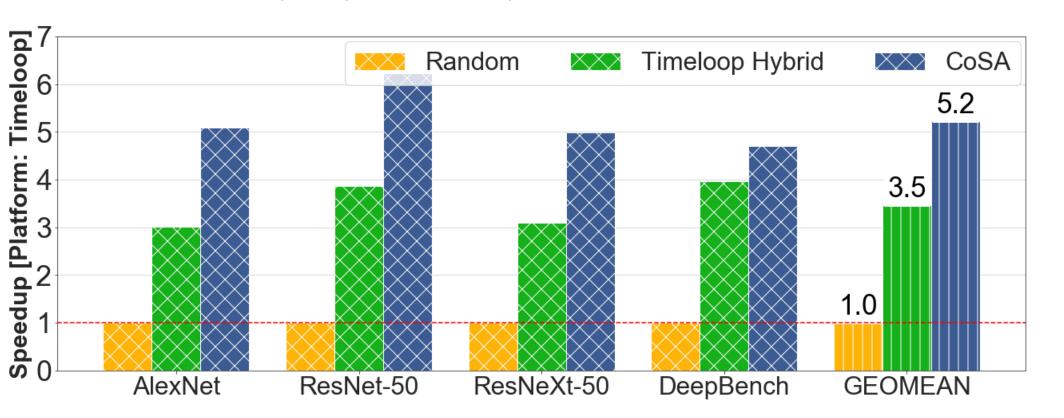
Temporal



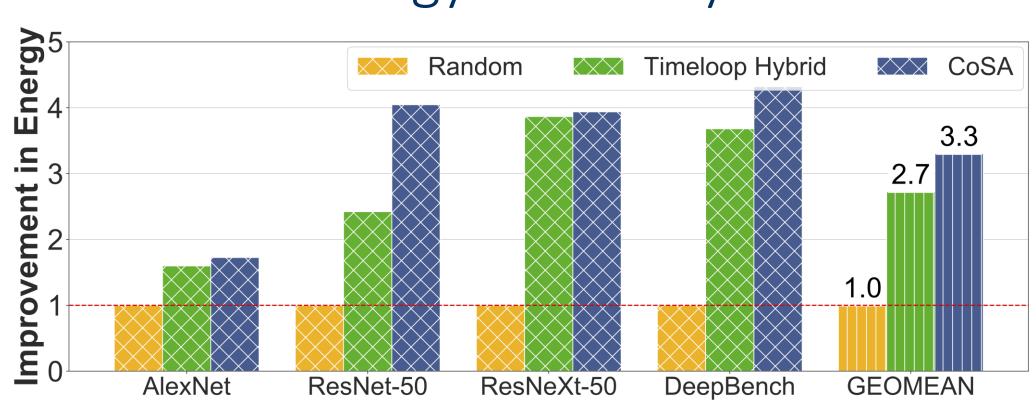
CoSA Evaluation

- Baselines:
- Random (best out of 5 valid schedules)
- Timeloop Hybrid (best out of 16K valid schedules)
- Platforms:
 - Timeloop Simulator

1.5x latency speedup



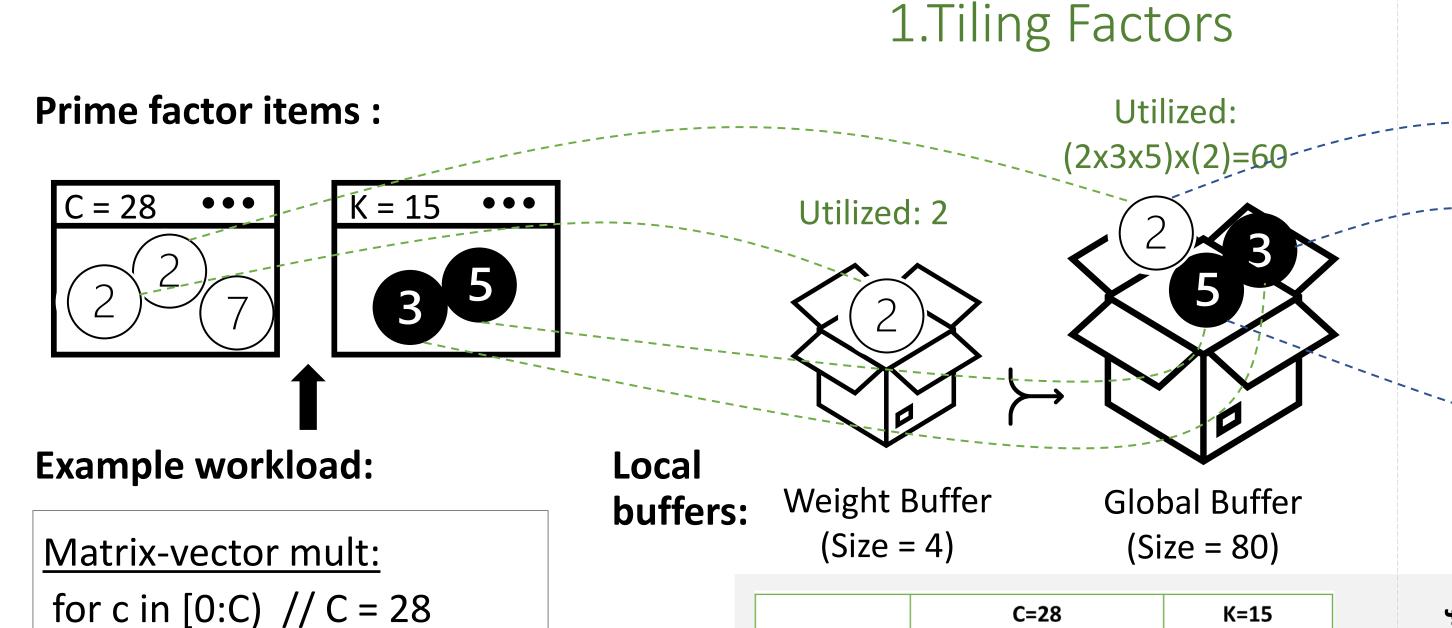
1.2x better energy efficiency



90x faster time-to-solution

	CoSA	Random	Timeloop Hybrid
Runtime / Layer	4.2 s	4.6s (1.1x)	379.9s (90.5x)
Samples / Layer	1	20K	67M
Evaluations/ Layer	1	5	16K

Key idea: prime factor allocation

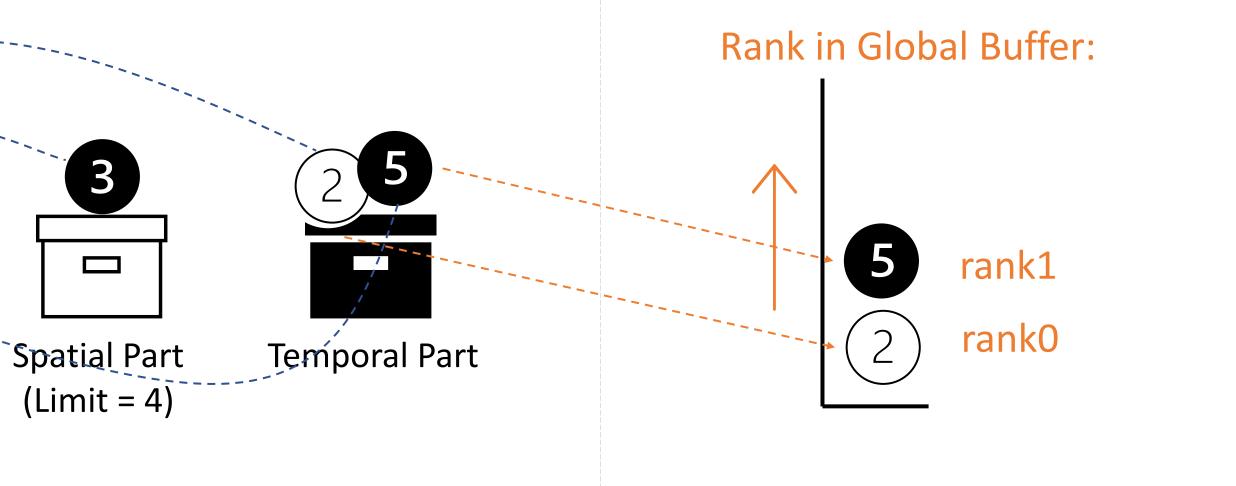


WeightBuf

GlobalBuf

DRAM

2.Spatial/Temporal Factors 3.Loop Permutation



rank0

rank4

K=15

binary allocation C=28 variable X $\sqrt{-1}$

CoSA Variable X

CoSA optimizes the binary allocation variable X using the constraints and the objectives

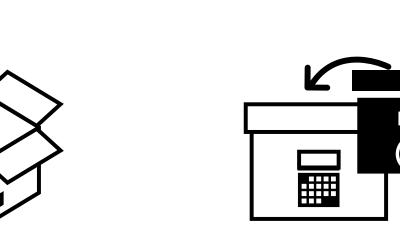


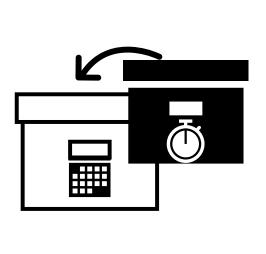
Weight Buffer

(Size = 4)

CoSA Constraints

Buffer Utilization:





Weight Buffer

(Size = 4)



Weight Buffer

(Size = 4)

Utilization-driven

Compute-driven