## 似非SCC SST Ver.

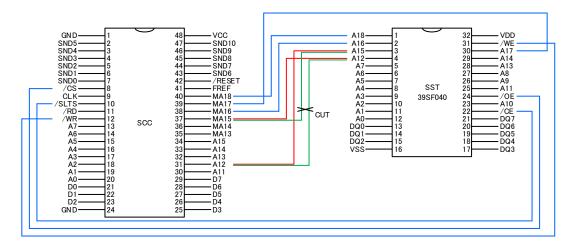
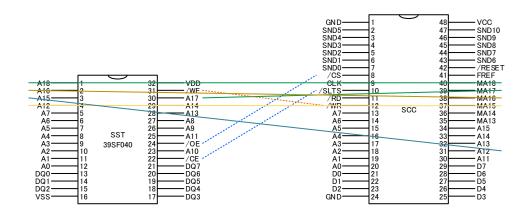




Figure 4: Pin Assignments for 32-pin PDIP



|              | <b>←</b> |    | Bank F | Register | r      | <b>→</b> |    |    |    |   |   |   |   |   |   |   |   |   |   |
|--------------|----------|----|--------|----------|--------|----------|----|----|----|---|---|---|---|---|---|---|---|---|---|
| bit number   | 18       | 17 | 16     | 15       | 14     | 13       | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|              |          |    |        |          |        |          |    |    |    |   |   |   |   |   |   |   |   |   |   |
| 02AAA        | 0        | 0  | 0      | 0        | 0      | 1        | 0  | 1  | 0  | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 |
| bit12←→bit15 | 15       |    |        |          |        |          |    |    |    |   |   |   |   | = |   |   |   |   |   |
| BANK#1, 0AAA | 0        | 0  | 0      | 0        | 0      | 1        | 0  | 1  | 0  | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 |
|              |          |    |        |          |        |          |    |    |    |   |   |   |   |   |   |   |   |   |   |
| 05555        | 0        | 0  | 0      | 0        | 1      | 0        | 1  | 0  | 1  | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |
| bit12←→bit15 |          |    |        |          | $\geq$ | $\leq$   |    |    |    |   |   |   |   |   |   |   |   |   |   |
| BANK#6, 0555 | 0        | 0  | 0      | 1        | 1      | 0        | 0  | 0  | 1  | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |