

[Status Legend]
new : New item, not yet started.
analyze : Under analysis.
design : Under design.
Testing : During verification.
modify : Under modifying.
close : Closed.
pending : Put on hold.
canceled: Canceled.

#	Priority	Status	Last Update	Target	Description	answer
1	High	design	2019-Dec-22	V9958	There is a delay of 2 lines from the expected action before the address update of the sprite attribute table is reflected.	
2	High	analyze	2019-Dec-08	S1990	The two CPUs do not switch as expected.	
3	Top Priority	design	2019-Dec-08	Debug	There is no means of observing the internal state when the operation is different from what is expected.	[2019-Dec-08] Implement a debugging device to enable overlay display of necessary information.
4	High	new	2019-Dec-08	S1990	Causes bus access violation for cartridge slot in 5MHz / 10MHz mode.	[2019-Dec-08] Start after solving No.2.
5	Middle	testing	2019-Dec-08	IPLROM	Incorporate a new IPLROM to add information display.	[2019-Dec-08] I received an IPLROM from caro.
6	Low	canceled	2019-Dec-08	IPLROM	Change to a new boot sequence. IPLROM --> 16KB BigIPLROM on EPCS --> BIOS on EPCS/SDcard.	[2019-Dec-08] There are few benefits. No.5 is sufficient.
7	Low	new	2019-Dec-09	OCM-PLD	Logo speed limiter for turboR	
8	Low	new	2019-Dec-14	V9958	Incompatible the Fighter's Ragnarok	
9	Low	new	2019-Dec-14	V9958	Incompatible the DIX	
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