# Reyrolle Argus Serie

## 1. Argus 1 Relay Series

The relay type model contains all elements of the actual relay. However, in the application of the relay, please consider the following:

1. The logic output is set that the logic in the relay types can be over-written in the relay element logic outputs.
2. The relay manual describes operating time errors as +/- values; in other words the operating times of current elements were assumed to be included in the relay characteristics.

## 2. Argus 2 Relay Series

The relay type model contains all elements of the actual relay. However, in the application of the relay, please consider the following:

1. The logic output would require too many inputs to model each OC or EF element starting and tripping signals into the logic block. For this reason, only trip outputs have been linked to the RelLogdip element. However, for a specific application where it may also be important to get the starting signal outputs, a modification to the RelLogdip element would be required.
2. The logic output is set that the logic in the relay types can be over-written in the relay element logic outputs.
3. The normal setting of the zero sequence MTA are negative values. These have been replaced with positive values in order to get the correct tripping.
4. The relays in the series with two over-current inputs were not modeled and could be represented by relays with three over-current elements as these would normally only be used when EF settings are more sensitive than OC settings. The tripping times of a three element relay would therefore be exactly the same as that of a two element relay.
5. All elements with directionality were modeled as three independent elements; one non-directional (N); one forward element (F); and one reverse element (R).
6. The relay manual describes operating time errors as +/- values; in other words the operating times of current elements were assumed to be included in the relay characteristics.

## 3. Argus 4 Relay Series

The relay type model contains all elements of the actual relay. However, in the application of the relay, please consider the following:

1. The logic output would require too many inputs to model each OC or EF element starting and tripping signals into the logic block. For this reason, only trip outputs have been linked to the RelLogdip element. However, for a specific application where it may also be important to get the starting signal outputs, a modification to the RelLogdip element would be required.
2. The logic output is set that the logic in the relay types can be over-written in the relay element logic outputs.
3. *Cold Load Protection* function is not included in the model.
4. *Auto-Reclose-Sequence* is not included in the model.
5. *Multiple Settings Group* is not included in the model.
6. *Flashing Fault Protection* is not included in the model.