

PowerFactory 2021

Technical ReferenceABB SACE EMAX PR33x

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1 Model information

Manufacturer ABB SACE

Model EMAX PR33x

Variants This PowerFactory relay model type simulates the different firmware versions of the ABB SACE PR331/P, PR332/P with PR330/V Low Voltage breakers.

2 General description

The ABB SACE EMAX PR33x is a microprocessor air circuit breakers up to 6300A which have been designed to increase efficiency. Accordingly to the manufacturer brochures it is the only breaker that protects electrical circuits and also reduces energy consumption based on the user's needs, therefore leading to massive reductions in energy waste.

The ABB SACE EMAX PR33x is a protection assembly containing a directional phase overcurrent definite time element, phase over current long time element, a short time element and an instantaneous element. A Definite time ground overcurrent element with an activable I2t feature is also available. The additional PR120/V module provide four voltage elements.

The ABB SACE EMAX PR33x Low Voltage Breaker has been modeled using one PowerFactory relay model which includes most of the features available in the Low Voltage Breaker.

The model implementation has been based on the information available in the low voltage breaker documentation provided by the manufacturer and freely available [1].

3 Supported features

3.1 Measurement and acquisition & input signals

It represents the interface between the power system and the Low Voltage Breaker protective elements. The currents flowing in the power system are converted by an element simulating a 3 phase CT , the voltages by an element simulating a 3 phase VT; the secondary voltages and currents are then measured by three measurement elements which models the digital filter of the relay.

3.1.1 Available Units

- one 3 phase current transformer ("Ct" block).
- one 3 phase voltage transformer ("Vt" block).
- one 3 phase measurement element ("Measure" block).
- one 3 phase sequence measurement element ("Measure Sequence" block).

- one frequency measurement element ("Meas Freq" block).
- two input signal ("block" and 'block1" input signal).

3.1.2 Functionality

The "Ct" block represents an ideal CT. Using the CT default configuration the current at the primary side are converted to the secondary side using the CT ratio. The CT saturation and/or its magnetizing characteristic are not considered. Please set the "Detailed Model" check box in the "Detailed Data" tab page of the CT dialog and insert the data regarding the CT burden, the CT secondary resistance and the CT excitation parameter if more accurate simulation results are required.

The "Measure" and the "Measure Sequence" block calculate the RMS value of the current and voltage values sampling the input channels at 24 samples/cycle and calculating a rectangular integral.

The "Measure Sequence" block calculates the voltage zero sequence and negative sequence component RMS values.

The input signals can be used to block the "G" element ("block" input signal) and the "S" element ("block1" input signal) to implement *Zone selectivity* logics.

3.1.3 Data input

The ratio of the "Ct" must be 1/1.

The Low Voltage Breaker primary rated current value must be set in the current measurement element ("Measure" block, "Nominal current" parameter). The following values are available:

• 800	• 2000	• 5000
• 1000	• 2500	• 6300
• 1250	• 3200	
• 1600	• 4000	

The Low Voltage Breaker primary rated voltage must be inserted both in the "Measure" block and in the "Measure Sequence" block ("Nominal Voltage" parameter). The following values are available:

• 380 • 400 • 690

The "Vt" secondary side rated voltage must be equal to the value inserted in the "Nominal Voltage" of the measurement blocks.

3.2 Protective elements

A set of consisting of a directional definite time overcurrent element, inverse time and a definite time overcurrent elements, four definite time voltage and two definite time frequency elements models the low voltage breaker protective functions.

3.2.1 Available Units

- one 3 phase directional overcurrent definite time element ("DFw", and "DBw" block).
- one 3 phase inverse time overcurrent element ("L" block).
- one 3 phase inverse/definite time overcurrent element ("S" block).
- one 3 phase definite time overcurrent element ("I" block).
- one ground definite time overcurrent element with I2t feature ("G" block).
- one 3 phase definite time overvoltage element ("OV" block).
- one 3 phase definite time underervoltage element ("UV" block).
- one negative sequence definite time overvoltage element ("U" block).
- one definite time residual overvoltage element ("RV" block).
- one definite overfrequency element ("OF" block).
- one definite underfrequency element ("UF" block).
- one 3 phase definite time overcurrent element with temporized enabling signal("MCR" block).
- one timer ("MCR Time" block).
- one logic element detecting the switch status ("MCR breaker" block).
- one definite time overpower element ("RP").
- three logic blocks calculating the reverse power magnitude and direction ("Power Calculator", "PQ calc", and "Tripconst" block).

3.2.2 Functionality

All the inverse characteristics available in the relay are supported by the inverse time overcurrent element. In such trip characteristics the relationship between current and time complies with the IEC 60255-3 standard.

The following trip characteristics are modeled:

L

- IEC 60255-3 (type A)
- IEC 60255-3 (type B)
- IEC 60255-3 (type C)
- L-Curve

The *L-Curve* equation is $t = 9t_p/(I/I_p)^2$ where T_p is the Trip Time and I_p is the Trip Threshold

S

- S const
- S-I2t

The *S const* characteristic model the definite time trip curve of the *S* element, the *S-l2t* characteristic the definite time trip curve when the l2t feature has been enabled.

G

- G-const
- G-I2t

The *G-const* characteristic model the definite time trip curve of the *G* element, the *G-l2t* characteristic the definite time trip curve when the l2t feature has been enabled.

MCR The "MCR" element models a 3 phase definite time instantaneous threshold which is active only 0.5 s after that the breaker has been closed. The status of the breaker is detected by the "MCR breaker" block which monitors by default the breaker present in the cubicle where the relay model is. A different breaker can be set in the "MCR breaker" dialog in the *Circuit-Breaker* matrix.

RP The "RP" element models a reverse active power threshold with definite time delay. The active power value and direction is calculated by the "Power Calculator" and by the "PQ calc" block.

The following tolerances are used in the ABB SACE EMAX PR33x model:

- **D** Trip Threshold tolerance $\pm 10\%$ I1, Trip Time tolerance $\pm 10\%$.
- **L** Trip Threshold tolerance 105%-120% I1, Trip Time tolerance $\pm 10\%$.
- **S** *S const* trip curve: Trip Threshold tolerance $\pm 7\%$ I1, Trip Time tolerance $\pm 10\%$. *S-I2t* trip curve: Trip Threshold tolerance $\pm 7\%$ I1, Trip Time tolerance -15% + 10%.
- I Trip Threshold tolerance $\pm 10\%$
- **G** *G-const* trip curve: Trip Threshold tolerance $\pm 7\%$ I1, Trip Time tolerance $\pm 10\%$. *S-I2t* trip curve: Trip Threshold tolerance $\pm 7\%$ I1, Trip Time tolerance $\pm 15\%$ + 10%.
- UV & OV Trip Threshold tolerance 10% Trip Time tolerance 0.04 s.

RV Trip Threshold tolerance 10% Trip Time tolerance 0.04 s.

OF & UF Trip Threshold tolerance $\pm 5\%$ Trip Time tolerance 0.04 s.

RP Trip Threshold tolerance $\pm 10\%$ Trip Time tolerance 0.04 s.

3.2.3 Data input

The relationships between the relay settins and the model parameters can be found in the following table:

Address	Relay Setting	Model block	Model Parameter	Note
	L Trip Threshold	L	Current Setting (Ipset)	
	L Trip Time	L	Time Dial (Tpset)	
	L (t=k/i2) curve or IEC 60255-3	L	Characteristic (pcharac)	
	S Disabling	S	Out of Service (outserv)	
	S Trip Threshold	S	Current Setting (Ipset)	
	S Trip Time	S	Time Dial (Tpset)	
	S $(t=k/i2)$ or $(t=k)$	S	Characteristic (pcharac)	
	I Disabling	1	Out of Service (outserv)	
	I Trip Threshold	1	Current Setting (Ipset)	
	U Disabling	U	Out of Service (outserv)	
	U Trip Threshold	U	Input Setting (Ipset)	
	U Trip Time	U	Time Dial (Tpset)	
	UV Disabling	UV	Out of Service (outserv)	
	UV Trip Threshold	UV	Input Setting (Ipset)	
	UV Time	UV	Time Dial (Tpset)	
	OV Disabling	OV	Out of Service (outserv)	
	OV Trip Threshold	OV	Input Setting (Ipset)	
	OV Time	OV	Time Dial (Tpset)	
	RV Disabling	RV	Out of Service (outserv)	
	RV Trip Threshold	RV	Input Setting (Ipset)	
	RV Time	RV	Time Dial (Tpset)	
	U F Disabling	UF	Out of Service (outserv)	
	U F Trip Threshold	UF	Input Setting (Ipset)	
	U F Time	UF	Time Dial (Tpset)	
	O F Disabling	OF	Out of Service (outserv)	
	O F Trip Threshold	OF	Input Setting (Ipset)	
	O F Time	OF	Time Dial (Tpset)	
	Warning current lw Disabling	Warning current lw	Out of Service (outserv)	
	Warning current lw	Warning current lw	Current Setting (Ipset)	
	Threshold LC1 Disabling	Threshold LC1	Out of Service (outserv)	
	Threshold LC1	Threshold LC1	Current Setting (Ipset)	
	Threshold LC2 Disabling	Threshold LC2	Out of Service (outserv)	
	Threshold LC2	Threshold LC2	Current Setting (Ipset)	
	D Disabling	D Directional	Out of Service (outserv)	
	D Threshold	D Fw	Current Setting (Ipset)	
	t7Fw	D Fw	Time Dial (Tpset)	
	t7Bw	D Bw	Time Dial (Tpset)	
	MCR	MCR	Current Setting (Ipset)	
	RP	RP	Input Setting (Ipset)	

3.3 Output logic

It represents the output stage of the low voltage breaker.

3.3.1 Available Units

- one output element ("Output Logic" block).
- foor output signals ("yout", "PR120K1", "PR120K2", "PR120K3", and "PR120K4" signal)

3.3.2 Functionality

The "Output Logic" block collects the trip signals coming from the protective functions; it operates the low voltage breaker power contact using the "yout" trip signal. It provides four additional output signals which can be used to implement a *Load Control* logic.

3.3.3 Data input

To disable completely the low voltage breaker model ability to open the power circuit disable the "Output Logic" block. The *Load Control* logic (implemented by the PR120/K in the ABB SACE EMAX PR33x Low Voltage Breaker) can be modified in the "Logic" tab page of the "Output Logic" block. By default the "PR120K1" output signal when any phase current is greater than the "Warning current lw" or the "Threshold LC1" or the "Threshold LC2" trip threshold.

4 Features not supported

The following features are not supported:

- "Gext" logic.
- "Rc" logic.
- "OT".
- "linst" logic.
- "Warning Iw".

5 References

[1] ABB SACE S.p.A., Divisione Interruttori B.T., Via Baioni, 35 - 24123 Bergamo - Italy. Operating istructions for T7-T8-X1 low voltage air circuit breakers protection releases Emax-Tmax 1SDH000587R0002 L3821.