



POWERFACTORY

PowerFactory 2021

Technical Reference

Time Overcurrent

RelToc, TypToc

PF2021

POWER SYSTEM SOLUTIONS
MADE IN GERMANY

Publisher:

DlgSILENT GmbH
Heinrich-Hertz-Straße 9
72810 Gomaringen / Germany
Tel.: +49 (0) 7072-9168-0
Fax: +49 (0) 7072-9168-88
info@digsilent.de

Please visit our homepage at:
<https://www.digsilent.de>

Copyright © 2020 DlgSILENT GmbH

All rights reserved. No part of this
publication may be reproduced or
distributed in any form without written
permission of DlgSILENT GmbH.

December 1, 2020
PowerFactory 2021
Revision 1

Contents

1	General Description	1
2	Features & User interface	2
2.1	Time-Overcurrent (RelToc)	2
2.1.1	Basic data	2
2.1.2	Tripping times	2
2.1.3	Blocking	2
2.1.4	Description	3
2.2	Time Overcurrent Type(TypToc)	3
2.2.1	Basic data	3
	Additional fields	4
	Trip characteristic	4
	Reset characteristic	5
2.2.2	Total Clear Curve	5
	Breaker operating time	5
	“Total Clear Curve” definition	6
2.2.3	Blocking	6
3	Integration in the relay scheme	7
4	Logic	9
4.1	Single phase	9
4.2	3 phase	11
4.3	Inverse characteristic trip time calculation	13
A	Parameter Definitions	14
A.1	Toc Type (TypToc)	14
A.2	Toc Element (RelToc)	15
B	Signal Definitions	16
B.1	Single phase	16
B.2	3 phase	16

List of Figures	17
List of Tables	18

1 General Description

The *Time Overcurrent* “RelToc” block implements a directional or non directional overcurrent or undercurrent time dependent element. The block defines a trip curve shape with associated reset characteristic, a current threshold, a time delay, a maximum and a minimum trip time, and a reset time; it creates in the time-current diagram a trip characteristic with a shape similar to the characteristic represented here below in Figure 1.1:

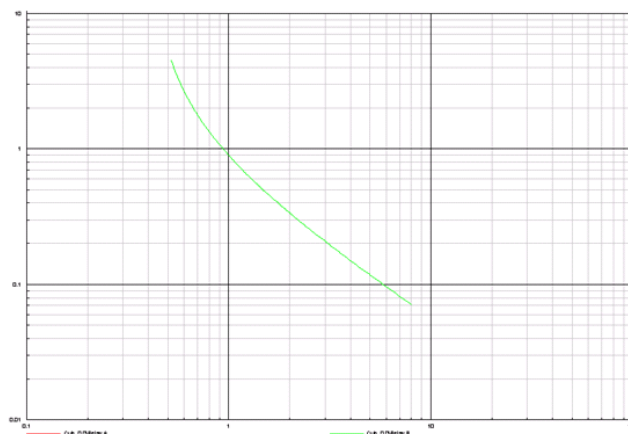


Figure 1.1: *DlgSILENT* The *Time Overcurrent* “RelToc” Time-Current diagram.

Moreover the user can define a “Total Clear Curve” characteristic to represent the overall time spent to remove the fault or the maximum time spent by the device to detect the fault. The Toc characteristic when the “Total Clear Curve” is defined is represented in Figure 1.2.

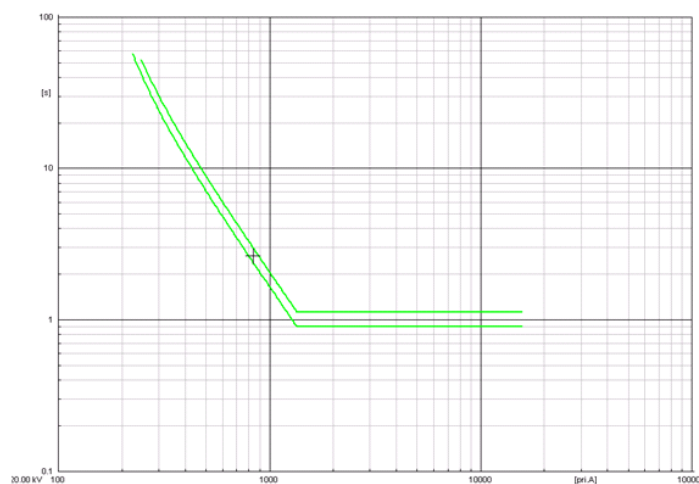


Figure 1.2: *DlgSILENT* The *Time Overcurrent* “RelToc” Time-Current diagram with the “Total Time Curve” enabled.

The block can be set to calculate the trip time using the “Minimum Trip Curve” or the “Total Clear Curve” characteristic. The *Time Overcurrent* “RelToc” block is operational during short circuit, load flow and RMS/EMT simulations.

2 Features & User interface

2.1 Time-Overcurrent (RelToc)

The user can change the block settings using the “Time overcurrent” dialogue (“RelToc” class). The dialogue consists of 4 tab pages: *Basic data*, *Tripping times*, *Blocking*, and *Description*. The main settings are located in the *Basic data* tab page.

2.1.1 Basic data

The “Time overcurrent” dialogue provides a *presentation* area where the red text shows some info regarding:

- The international symbols used to represent the block protective function.
- Which currents are measured by the block.

The block can be disabled using the “Out of service” check box. A directional feature can be set using the “Tripping direction” combo box. Please notice that the directional logic relies on a separate block (‘Directional Overcurrent “RelDir” class). The trip curve shape is set using the “Characteristic” combo box. The current threshold, time delay, reset delay, and minimum and maximum trip time can be set using the “Current Setting”, the “Time dial”, the “Reset delay”, the “Min. Time”, and the “Max. Time” control. They are showed as a combo box for ranges of discrete values or otherwise as an edit box. The ranges are defined inside the trip characteristic dialogue (“TypChatoc” class). In this way it is possible to define different ranges for each trip characteristic.

The blue text provides additional info regarding the current threshold in terms of primary current.

2.1.2 Tripping times

The *Tripping times* tab page allows the user to select whether the *Minimum Trip Curve* or the *Total Clear Curve* is used to calculate the element tripping time. The radio buttons are only displayed if the *Total Clear Curve* has been defined and is associated with the *Minimum Trip Curve* which has been selected.

2.1.3 Blocking

This tab page defines the blocking input signals configuration.

If the “Consider Blocking” setting is set to *User configurable* in the relevant *Blocking* tab page of the *Time Overcurrent Type(TypToc)* dialogue (see 2.2.3), the “Consider Blocking” check box is displayed. If the check box is *not* set, the input blocking signals are not considered and the *Time overcurrent* (“RelToc”) block is allowed to trip. If the check box *is* set the block doesn’t trip if the blocking input signals are *on* and before the “Release Blocking Time” time has expired.

In the “Release Blocking Time” the user can insert the expiration time for the blocking input signals. When a block trip is triggered, if the blocking input signals are active, a timer is activated. When the timer has counted a time equal to the expiration time, the blocking input signals are ignored and the block is allowed to trip.

The relevant *Blocking* tab page of the *Time Overcurrent Type(TypToc)* dialogue (see 2.2.3) must be used to define which features are available.

2.1.4 Description

The *Description* tab page can be used to insert some information to identify the Toc protective element (both with a generic string and with an unique textual string similar to the *Foreign Key* approach used in the relational databases) and to identify the source of the data used to create it.

2.2 Time Overcurrent Type(TypToc)

The *Time overcurrent* block main characteristics must be configured in the “Time Overcurrent Type” dialogue (*TypToc* class). The dialogue contains three tab pages: *Basic data*, *Total clear curve*, and *Blocking*.

2.2.1 Basic data

The *Basic data* tab page contains most of the controls used to configure the *Time Overcurrent* block.

The block can be configured using the “Type” combo box variable as:

- 3 phase element (“Phase Current (3ph)”)
- single phase element (“Phase Current (1ph)”)
- earth fault element (“Earth Current (3*I0)”)
- sensitive earth fault element (“Sensitive Earth Current (3*I0)”)
- zero sequence element (“Zero Sequence Current (I0)”)
- negative sequence element (“Negative Sequence Current (I2)”)
- unbalance current element (“3*Negative Sequence Current (3*I2)”)
- Phase A element (“Phase A Current”)
- Phase B element (“Phase B Current”)
- Phase C element (“Phase C Current”)
- Thermal image (3ph)
- Thermal image (1ph)
- 3 phase element for special purposes (“3ph (other)”)
- single phase element for special purposes (“1ph (other)”)

Furthermore if the “External Starting” check box is set, the block starts only when the “wstart.A”, “wstart.B”, and “wstart.C” input signals are active.

The block can be controlled by a recloser block (“RelRecl” object). The “Reclosing” item (to perform a multi-shot reclosing) or the “Lockout” item (to block the recloser after the first trip) must be set in the “Recl.Feature” combo box for this purpose.

Many of the types detailed above are provided for “documentation only” purpose. The underlying types are:

- a 3 phase block (3 phase element and 3 phase element for special purposes)
- a single phase block (single phase element, earth fault element, sensitive earth fault element, zero sequence element, negative sequence element, unbalance current element, Phase A element, Phase B element, Phase C element and single phase element for special purposes)
- a 3 phase block with external starting (“External Starting” check box set, 3 phase element and 3 phase element for special purposes)
- a single phase block with external starting (“External Starting” check box set, single phase element, earth fault element, sensitive earth fault element, zero sequence element, negative sequence element, unbalance current element, Phase A element, Phase B element, Phase C element and single phase element for special purposes)

The *Time Overcurrent* (“RelToc”) element has been conceived to work together with the other relay elements: some settings can be set to depend upon the settings of other overcurrent blocks:

- The element can be set to get a threshold value reference from another block: the “Pickup current” value becomes a multiplier of the current threshold set in the other block.
- The directional characteristic can be set using the *Time Overcurrent* “RelToc” block dialogue or can be set depending on the setting of an external “Directional” block.

Additional fields To support certain models of US reclosers the “Additional fields” frame has been added inside the “TypToc” dialogue. When the frame is enabled the “Time adder” and the “Min resp.Time” controls are enabled and the relevant controls are shown inside the *Time Overcurrent* “RelToc” dialogue. The trip time is calculated using the “Time dial” value, the “Time Adder” value and the “Min.Response Time” value. If the “Min.Response Time” value multiplied by the “Time Dial” value is greater than the trip value calculated using the “Time dial” value, the “Min.Response Time” value multiplied by the “Time Dial” value is used as the trip value. The “Time Adder” value is added then to the trip time calculated in the previous step. Please refer to the US recloser manuals (i.e. Cooper Power Systems Form4C) for more details about such settings.

Trip characteristic The list of the available trip curves is shown in lower part of the “Time Overcurrent Type” dialogue (“TypToc” class). The trip characteristics are on the left in the “I-t Characteristic” column, the reset characteristics are on the right in the “I-t Reset Characteristic” column. Clicking the right mouse button on one of the columns a popup menu is displayed; through this menu it is possible to create new characteristics, to delete or to edit an existing characteristic.

Selecting the “edit Element/Type” item the “I-t characteristic” dialogue is opened (“TypChatoc” class).

The *I-t Characteristic* dialogue allows the definition of the Toc characteristic (“TypChatoc” class), both an equation or a set of points can be used for this purpose. Please note that the range

of the time dial is defined independently for each characteristic. In the “Time dial” frame the “Range” control defines the allowed range for the characteristic’s “time dial” variable.

Reset characteristic Just above the list of the available characteristics, the “Reset characteristic configuration” combobox allows enabling/disabling the reset characteristics; 3 options are available:

- Enabled
- Disabled
- User Configurable

Please note that the the “I-t Reset Characteristic” column can be modified only when the “Enabled” or the “User Configurable” option is selected. When the “User Configurable” option is selected an additional combo box is displayed in the Toc dialogue (“RelToc” class) which allows the user to enable or disable the reset characteristic. When the reset characteristic is disabled and the current goes below the trip threshold multiplied by the reset factor (kr variable available in the TypToc dialogue) the toc elements reset after the reset time delay has expired (Tr variable available in the TypToc dialogue).

The reset characteristics are managed exactly as the trip characteristics. It is important to understand that the reset characteristic must be defined for $I/I_p \geq 0$ and $I/I_p < 1$. In the example below the “Min Current” (imin variable) value is 0 I/Ip and the “Max current” (imax variable) value is 0.999 I/Ip. Please notice that in the range where the reset characteristic is not defined the reset is not performed.

If the “User Configurable” option is selected in the “Reset characteristic configuration” combo box of the “Time Overcurrent Type” dialogue (“TypToc” class), then the “Reset characteristic” check box is displayed in the “ElmToc” dialogue.

2.2.2 Total Clear Curve

The “Total clear curve” tab page contains settings which allow definition of the curve representing the overall time spent clearing the fault or the maximum time spent by the device in order to detect the fault.

The following features are available:

- Ability to automatically add the *Breaker Opening Time* to the *Total Clear Curve* characteristic.
- Definition of the current and time multipliers and shift factors applied to the *Minimum Trip Curve* in order to generate the *Total Clear Curve* characteristic.

Breaker operating time The “Total Clear Curve” can include the “Breaker operating time” if required.

The user can configure the behaviour by adapting the “Include Breaker Operating Time” check box. When the “Include Breaker Operating Time” check box is not checked and the “Consider Breaker Operating time” check box in the “Overcurrent Plot Settings” dialogue (SetOcplot class) is checked, the “Total Clear” curve time values used to draw the curve are increased by the operating time of the breaker associated with the relay.

The *Breaker operating time* value can be inserted inside the “Breaker time” control located in the *Basic Data* tab page (“t_{open}” variable) inside the “Switch Type” dialogue of the switchgear associated with the relay.

If in the Toc element dialogue (RelToc class) the “Compute Time Using” is set to “Total Clear Curve” and in the *Time Overcurrent Type* (“TypToc”) dialogue the “Include Breaker Operating Time” check box is not checked, the tripping time is equal to the breaker operating time plus the tripping time calculated using the “Total Clear” curve characteristic.

No value is added in the graphical representation or to the tripping time calculation if no switch type is available or if the “Breaker time” is zero.

“Total Clear Curve” definition The “Enable” check box can be used to enable or disable the “Total Clear” curve definition and the relevant controls in the dialogue.

Please note that the “Total clear” curve is always defined and drawn inside the overcurrent plot windows even when the “Enable” check box is not checked, the “Include Breaker Operating Time” check box is not checked and the “Consider Breaker Operating time” check box in the “Overcurrent Plot Settings” dialogue (“SetOcplot” class) is checked.

The “Total Clear” curve can be defined with the application of a “shifter” and “multiplier” both to the current values and to the time values which define the “Min trip” curve. The “shifter” is simply an additive factor, the “multiplier” is a multiplicative factor.

The “Total Clear” curve current values are calculated using the following formula:

$$I_{TotalClear} = (I_{MinTrip} * "CurrentMultiplier") + "CurrentShifter" \quad (1)$$

The “Total Clear” curve time values are calculated using the following formula:

$$t_{TotalClear} = (t_{MinTrip} * "TimeMultiplier") + "TimeShifter" \quad (2)$$

2.2.3 Blocking

The *Blocking* tab page allows definition of the effect of the blocking input signals: the block can be

- Always enabled
- Always disabled
- User configurable

Moreover it is possible to define the range of the expiration time for the blocking input signals. When a block trip is triggered, if the blocking input signals are active, a timer is activated. When the timer has counted a time equal to the expiration time the blocking input signals are ignored and the block is allowed to trip.

3 Integration in the relay scheme

The *Time Overcurrent* “RelToc” type class name is *TypToc*. The *Time Overcurrent* dialogue class name is *RelToc*. As already shown, there are two main versions of the block: a single phase and a three phase version. The number and the name of the input signals depends only upon which of these versions is used. Please note that inside the “TypToc” dialogue if the “Type” is set as “3ph (other)” (or “1ph (other)”) a normal 3 phase Toc element (or a single phase “RelToc”) is still used. The only difference is that the Time Current characteristic is not displayed in the “Time-Overcurrent plot”. It can be useful if the *Time Overcurrent* “RelToc” element is for instance used for control purposes and is not directly settable by the user. The typical connection of a single phase *Time Overcurrent* “RelToc” block is showed in Figure 3.1.



Figure 3.1: *DlgSILENT* The typical connection scheme of a single phase *Time Overcurrent* “RelToc” block.

If the *Time Overcurrent* “RelToc” block is directional the “wfd” and the “wrev” signals must be connected to a directional block (see 3.2).

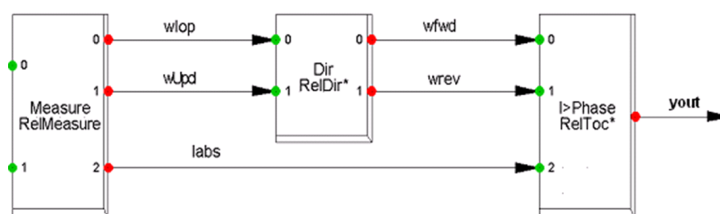


Figure 3.2: *DlgSILENT* Typical connection scheme of a single phase directional *Time Overcurrent* “RelToc” block.

The connections associated with a three phase *Time Overcurrent* “RelToc” block are quite similar. The main difference is that a current signal and a forward/reverse directional signal for each phase is included. The *Time Overcurrent* “RelToc” block can be made sensitive to the current flow direction by using the “Tripping direction” combobox in the “RelToc” dialogue.

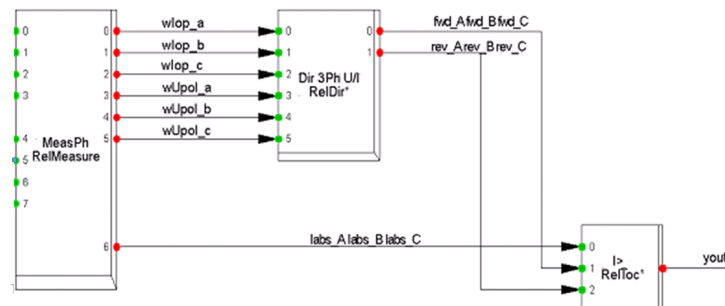


Figure 3.3: *DlgSILENT* Typical connection scheme of a three phase directional *Time Overcurrent* “RelToc” block.

The available options present in the combobox must be set using the “Directional” combobox in the “TypToc” dialogue. Please keep in mind the follow rules:

- If the “Forward” direction is set the “fwd” signal (“fwd_A” or “fwd_B” or “fwd_C” for the 3phase version) must be activated to allow starting of the element.
- If the “Reverse” direction is set the “rev” signal (“rev_A” or “rev_B” or “rev_C” for the 3phase version) must be activated to allow starting of the element.
- If the “External” direction is set the “fwd” signal (“fwd_A” or “fwd_B” or “fwd_C” for the 3phase version) must be activated to allow starting of the element. The “rev” signal status is ignored (it can be disconnected). “External” makes the block direction dependant on an external “directional” block setting.

If the “External Starting” check box is set the block starting depends upon the status of the wstart input signal: in the 3 phase version each phase works independently so the starting depends upon the status of the wstart_A, wstart_B, wstart_C input signals.

To control a *Time Overcurrent* “RelToc” block with a reclosing element (“RelRecl” object) the following configuration must be used:

- the “iblock” input signal must be connected with the output signal of the reclosing element, where the reclosing element has been programmed to block (yblock_TOCx with $1 \leq x \leq 5$ or yblock_Logick with $1 \leq k \leq 16$). Please read the “RelRecl” documentation for more details about the way to program a reclosing sequence.
- in the “TypeToc” dialogue the “Recl. Feature” combobox must be selected to “Normal” or “Lockout”. If “Normal” is selected a standard reclosing sequence is triggered, if “Lockout” is selected a trip of the *Time Overcurrent* “RelToc” element puts the reclosing element in lockout status and no other reclosing is attempted. Please note that if “None” is the selected option in the “Recl. Feature” combobox no reclosing operation is attempted. This is also true if the *Time Overcurrent* “RelToc” element “iblock” input is connected to a reclosing element. If a reclosing element is not present the iblock signal (in the 3 phase version also iblock_A, iblock_B and iblock_C to act on each phase) can be used by any other element to block the starting of the *Time Overcurrent* “RelToc” element.

To get a current threshold value reference from another block the following operation must be performed :

- Inside the “TypToc” dialogue set the “Ref. Current from” item with the link to the element from which the *Time Overcurrent* “RelToc” element gets the reference value.
- Inside the “TypToc” dialogue set the “Current Range” unit equal to “p.u.”. Please note that if “Sec.A” is selected as the unit then the whole feature does not work and the *Time Overcurrent* “RelToc” secondary amps threshold is used.

4 Logic

4.1 Single phase

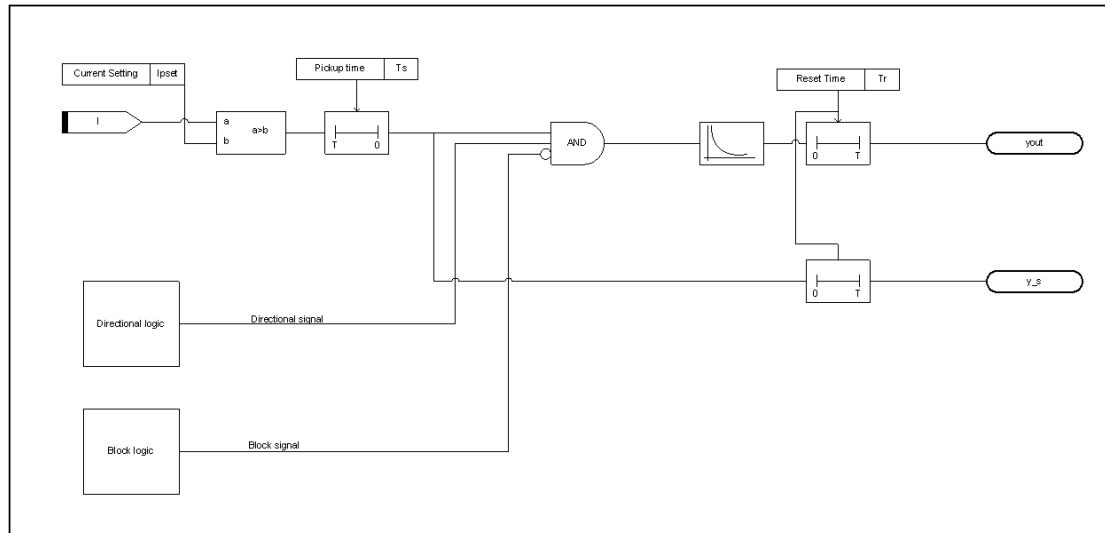


Figure 4.1: The *DlgSILENT Time Overcurrent (RelToc)* logic

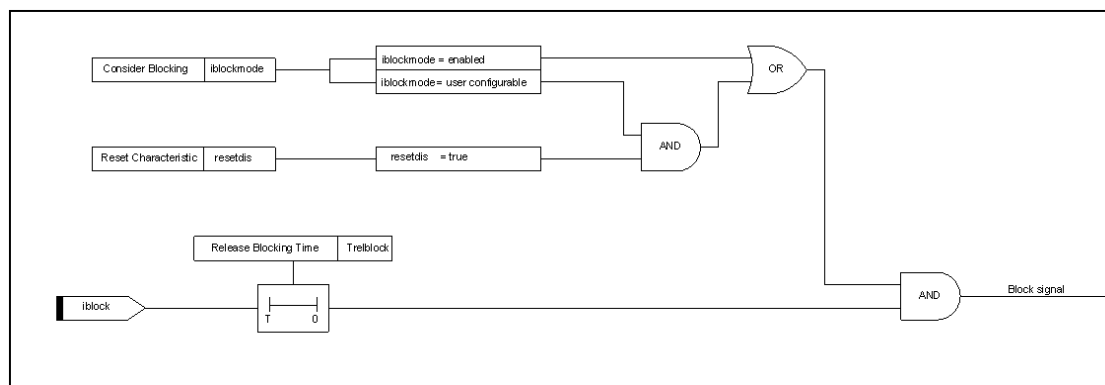
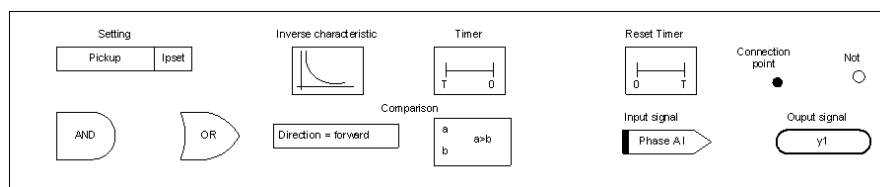


Figure 4.2: The *DlgSILENT Time Overcurrent (RelToc) Block* logic



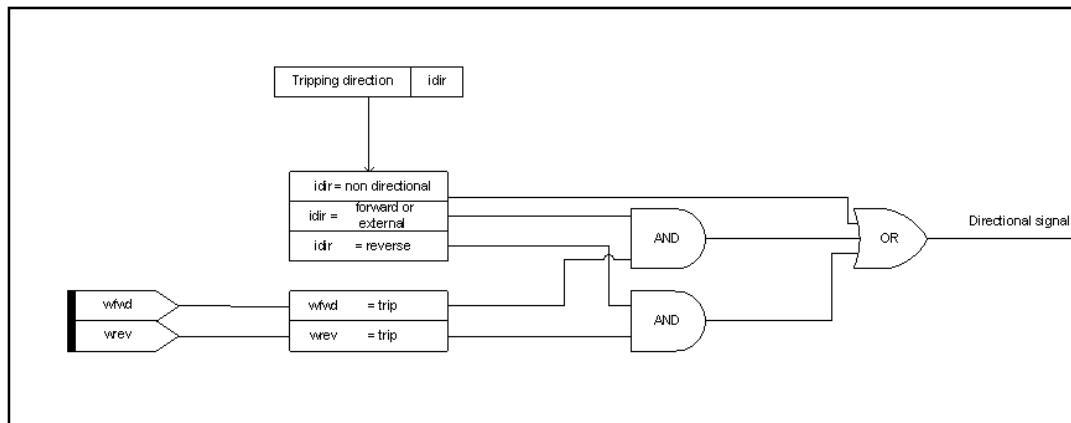
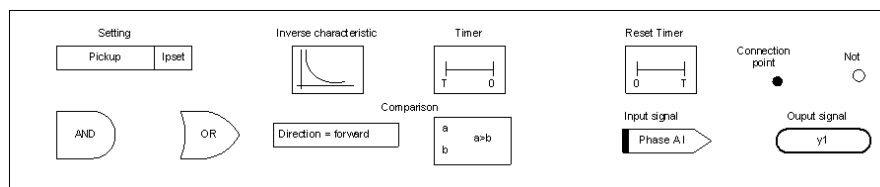
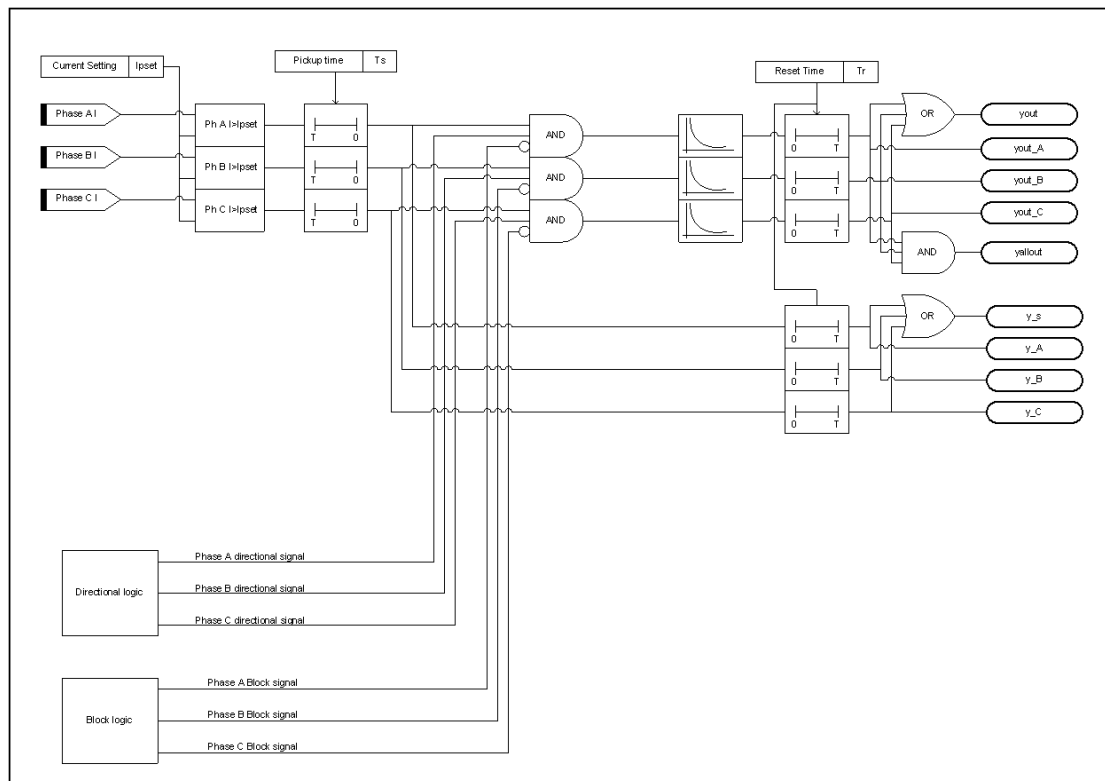
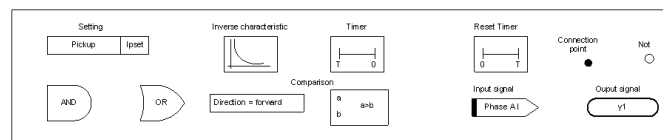


Figure 4.3: The *DlgSILENT Time Overcurrent (RelToc) Directional logic*



4.2 3 phase

Figure 4.4: The *DIgSILENT Time Overcurrent (RelToc)* logic

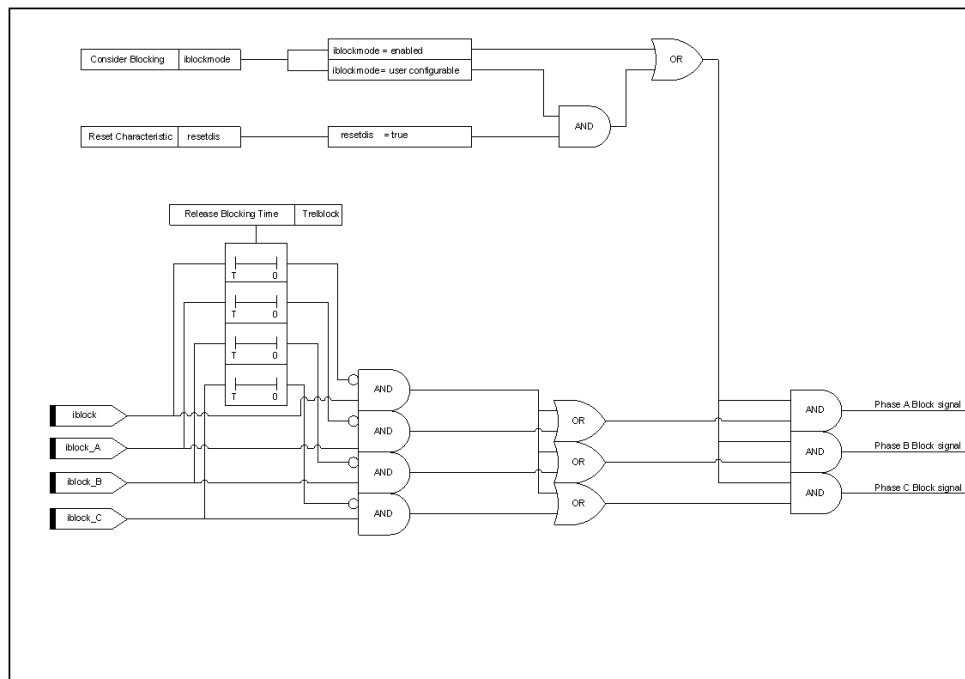


Figure 4.5: The *DlgSILENT Time Overcurrent (RelToc)* Block logic

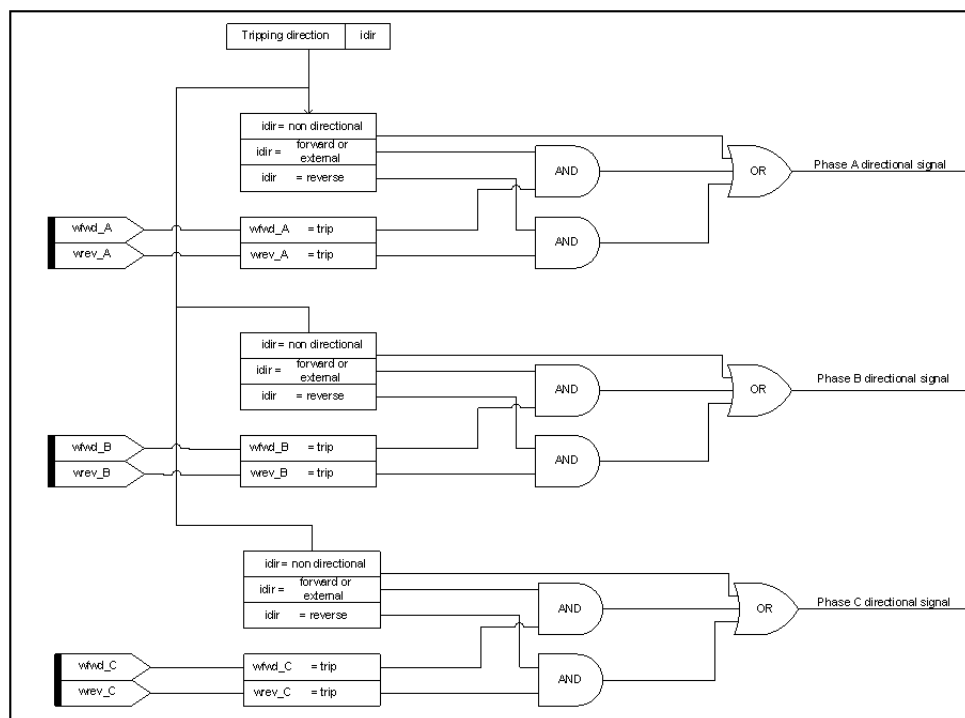
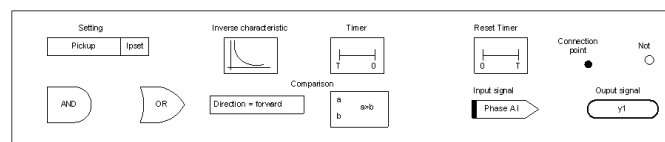


Figure 4.6: The *DlgSILENT Time Overcurrent (RelToc)* Directional logic



4.3 Inverse characteristic trip time calculation

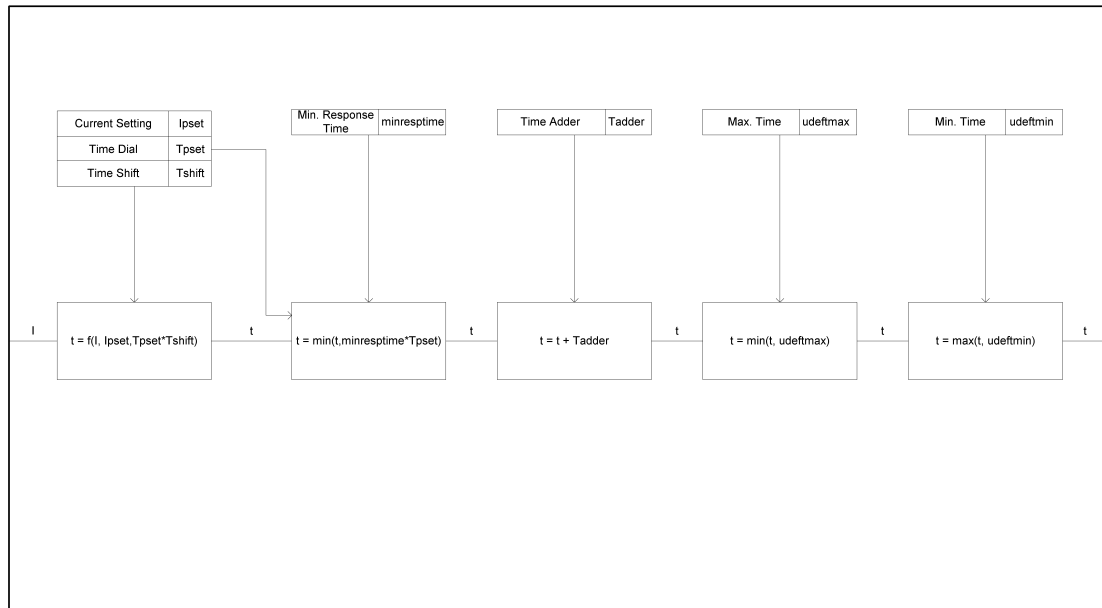


Figure 4.7: The *DlgSILENT* Inverse characteristic trip time calculation logic

A Parameter Definitions

A.1 Toc Type (TypToc)

Table A.1: Input parameters of Toc type (*TypToc*)

Parameter	Description	Unit
loc_name	Name assigned by the user to the block type	Text
sfiec	IEC symbol (I>>,IE>>,I2>>)	Text
sfansi	ANSI symbol (50,50N,46)	Text
itype	Block Type ("Phase Current (3ph)", "Phase Current (1ph)", "Earth Current (3*I0)", "Sensitive Earth Current (3*I0)", "Zero Sequence Current (I0)", "Negative Sequence Current (I2)", "3*Negative Sequence Current (3*I2)", "Phase A Current", "Phase B Current", "Phase C Current", "3ph (other)", "1ph (other)") Integer IrecItarget Reclosing feature (None, Normal, Lock-out)	Integer
inegout	Flag to set the element to act as an undercurrent element	Integer
idirpos	Directional available options (None, None/Forward, None/Forward/Reverse, Forward/Reverse, None/External, External)	Integer
iincTs	Are the timers including the pick up time?	Y/N
ltextstart	Is the block starting depending upon an external signal?	Y/N
plref	Pointer to the block providing the current reference(optional)	Pointer
rlpset	Range of the Current threshold	Text
iunit	Current threshold unit (Sec. Amps, pu)	Integer
rTshift	Range of the Time Shift	Seconds
cpslayout	Flag to enable/disable the new controls added to support the US reclosers	Y/N
rTadder	Range of the time adder variable	Text
itaddunit	Time adder unit (Seconds, cycles)	Integer
rminresptime	Range of the minimum response time variable	Text
imrtunit	Minimum response time unit(Seconds, cycles)	Integer
pcharac	Vector of the available trip characteristic shapes	Vector
iresetconf	Reset configuration (available options "Enabled", "Disabled", "User configurable")	Integer
Ts	Pick up time, i.e. the time spent measuring the currents in the load flow and short circuit calculation and in the RMS simulation	Seconds
Tr	Reset time, i.e. the delay with which the block reset the trip outputs after that the current went below the trip threshold * Kr	Seconds
Kr	Reset ratio	Real
iinckbreakt	Flag: if it is "true" the clear curve definition already includes the breaker time operating time, so the breaker operating time is not added to the curve itself	Y/N
ienable	Flag to enable/disable the curve definition	Y/N
ishifter	Additive factor added to each current value of the points belonging to the "Min trip" curve to generate the "Total clear" curve	pu or Sec.Amps
imultiplier	Multiplicative factor applied to each current value of the points belonging to the "Min trip" curve to generate the "Total clear" curve	Real
imcunit	ishifter unit (pu or Sec.Amps)	Integer
tshifter	Additive factor added to each time value of the points belonging to the "Min trip" curve to generate the "Total clear" curve	Seconds or cycles
tmultiplier	Multiplicative factor applied to each time value of the points belonging to the "Min trip" curve to generate the "Total clear" curve	Real
tmcunit	tshifter unit (seconds or cycles)	Integer
iblockmode	How the input block signals is considered (<i>Ignored, Always considered, User configurable</i>)	Integer
rTrelblock	Range of the <i>Release Blocking Time</i> variable	Text

A.2 Toc Element (RelToc)

Table A.2: Input parameters of Toc element (*RelToc*)

Parameter	Description	Unit
loc_name	Name assigned to the user to the block element	Text
Typ_id	Pointer to the relevant TyToc object	Pointer
outserv	Flag to put out of service the block	Y/N
idir	Tripping direction (None, external, forward, reverse the available options depend upon the idirpos value in the relevant TypToc object)	Integer
Ipset	Current threshold in Amps	Sec Amps
Ipsetr	Current threshold in pu	pu
Tpset	Time dial	Real
ResetT	Reset Delay	Seconds
udeftmin	Minimum Time	Seconds
udeftmax	Maximum Time	Seconds
Tshift	Time shifter	Seconds
pcharac	Trip characteristic shape	Vector
Modframe	Flag to enable the additional modifiers added to support the US reclosers	Y/N
Tadder	Time adder variable in seconds	Seconds
cTadder	Time adder variable in cycles	Cycles
minresptime	Minimum response time in seconds	Seconds
cminresptime	Minimum response time in cycles	Cycles
resetdis	Flag to enable/disable the reset characteristic (available only if the reset is "User configurable", iresetconf variable in the TypToc class)	Y/N
calcuse	Flag to select which curve should be used to calculate the element tripping time. 0 = "Minimum trip curve", 1 = "Total time curve"	Integer
iacceptblock	Flag to enable/disable the ability to consider input block signal (available only if "Consider blocking"("iblockmode" variable in the TypToc class) is "User configurable")	Integer
Trelblock	Maximum time that the input block signals are considered	Seconds

B Signal Definitions

B.1 Single phase

Table B.1: Input/output signals of the single phase Toc element (*Ca/Toc1p*)

Name	Description	Unit	Type	Model
labs	Input current	Sec Amps	IN	Any
iblock	Blocking signal	Seconds(or 1/0 RMS/EMT simulation)	IN	Any
wfwd	Forward current signal	Seconds(or 1/0 RMS/EMT simulation)	IN	Any
wrev	Reverse current signal	Seconds(or 1/0 RMS/EMT simulation)	IN	Any
wstart	External starting signal (available only if iextstart = 1 in TypToc)	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
yout	Trip signal	Seconds(or 1/0 RMS/EMT simulation)	OUT	Any
y_s	Start signal	Seconds(or 1/0 RMS/EMT simulation)	OUT	Any

Table B.2: Single phase Toc element state variables

Name	Description	Unit
xl	Integrated current (1 = trip)	Real

B.2 3 phase

Table B.3: Input/output signals of 3 phase Toc element (*Ca/Toc*)

Name	Description	Unit	Type	Model
labs_A	Phase A input current	Sec Amps	IN	Any
labs_B	Phase B input current	Sec Amps	IN	Any
labs_C	Phase C input current	Sec Amps	IN	Any
iblock	Blocking signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
iblock_A	Phase A Blocking signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
iblock_B	Phase B Blocking signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
iblock_C	Phase C Blocking signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wfwd_A	Phase A forward current signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wfwd_B	Phase B forward current signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wfwd_C	Phase C forward current signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wrev_A	Phase A reverse current signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wrev_B	Phase B reverse current signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wrev_C	Phase C reverse current signal	Seconds (or 1/0 RMS/EMT simulation)	IN	Any
wstart_A	Phase A external starting signal (available only if iextstart = 1 in TypToc)	Seconds(or 1/0 RMS/EMT simulation)	IN	Any
wstart_B	Phase B external starting signal (available only if iextstart = 1 in TypToc)	Seconds(or 1/0 RMS/EMT simulation)	IN	Any
wstart_C	Phase C external starting signal (available only if iextstart = 1 in TypToc)	Seconds(or 1/0 RMS/EMT simulation)	IN	Any
yout	Trip signal (any phase)	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
yout_A	Phase A trip signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
yout_B	Phase B trip signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
yout_C	Phase C trip signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
yallout	All phases trip signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
y_s	Start signal (any phase)	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
y_A	Phase A start signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
y_B	Phase B start signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any
y_C	Phase C start signal	Seconds (or 1/0 RMS/EMT simulation)	OUT	Any

Table B.4: 3 phase Toc element state variables

Name	Description	Unit
xl_A	Phase A integrated current (1 = trip)	Real
xl_B	Phase B integrated current (1 = trip)	Real
xl_C	Phase C integrated current (1 = trip)	Real

List of Figures

1.1	<i>DlgSILENT</i> The <i>Time Overcurrent</i> “RelToc” Time-Current diagram.	1
1.2	<i>DlgSILENT</i> The <i>Time Overcurrent</i> “RelToc” Time-Current diagram with the “Total Time Curve” enabled.	1
3.1	<i>DlgSILENT</i> The typical connection scheme of a single phase <i>Time Overcurrent</i> “RelToc” block.	7
3.2	<i>DlgSILENT</i> Typical connection scheme of a single phase directional <i>Time Overcurrent</i> “RelToc” block.	7
3.3	<i>DlgSILENT</i> Typical connection scheme of a three phase directional <i>Time Overcurrent</i> “RelToc” block.	7
4.1	The <i>DlgSILENT Time Overcurrent (RelToc) logic</i>	9
4.2	The <i>DlgSILENT Time Overcurrent (RelToc) Block logic</i>	9
4.3	The <i>DlgSILENT Time Overcurrent (RelToc) Directional logic</i>	10
4.4	The <i>DlgSILENT Time Overcurrent (RelToc) logic</i>	11
4.5	The <i>DlgSILENT Time Overcurrent (RelToc) Block logic</i>	12
4.6	The <i>DlgSILENT Time Overcurrent (RelToc) Directional logic</i>	12
4.7	The <i>DlgSILENT Inverse characteristic trip time calculation logic</i>	13

List of Tables

A.1	Input parameters of Toc type (<i>TypToc</i>)	14
A.2	Input parameters of Toc element (<i>RelToc</i>)	15
B.1	Input/output signals of the single phase Toc element (<i>CalToc1p</i>)	16
B.2	Single phase Toc element state variables	16
B.3	Input/output signals of 3 phase Toc element (<i>CalToc</i>)	16
B.4	3 phase Toc element state variables	16